

XR-210

ELECTRICAL PERFORMANCE CHARACTERISTICS - XR-210

TEST	SYMBOL	CONDITIONS	TEMPERATURE	LIMITS		UNIT	GROUP A SUBGROUP
				MIN	MAX		
Supply Current	I_{CC}	$V_{CC} = \pm 6V$	$T_A = +25^\circ C$	5.0	16.0	mA	1
			$-55^\circ C \leq T_A \leq +125^\circ C$	5.0	20.0	mA	2,3
Supply Current	I_{CC}	$V_{CC} = \pm 13V$	$T_A = +25^\circ C$		26.0	mA	1
			$-55^\circ C \leq T_A \leq +125^\circ C$		26.0	mA	2,3
VCO Power Supply Stability	PSR	$\pm 6V \leq V_{CC} \leq \pm 12V$	$T_A = +25^\circ C$		0.5	%/V	9
			$-55^\circ C \leq T_A \leq +125^\circ C$		1.0	%/V	10,11
VCO Sweep Range	FSW		$T_A = +25^\circ C$	5:1			9
			$-55^\circ C \leq T_A \leq +125^\circ C$	3:1			10,11
VCO Duty Cycle Asymmetry	DC		$T_A = +25^\circ C$		± 3	%	9
			$-55^\circ C \leq T_A \leq +125^\circ C$		± 10	%	10,11
Phase Detector Output Offset Voltage		Measured Across Pin 1 and Pin 3, $V_{IN} = 0$	$T_A = +25^\circ C$		± 150	mV	1
			$-55^\circ C \leq T_A \leq +125^\circ C$		± 150	mV	2,3
Logic Output Leakage Current	I_{OH}	$V_{CC} = \pm 12V$	$T_A = +25^\circ C$		10.0	μA	1
		$V_{CC} = \pm 6V$	$-55^\circ C \leq T_A \leq +125^\circ C$		100.0	μA	2,3
Logic Output Low Voltage	V_{OL}	$I_L = 10 \text{ mA}$	$T_A = +25^\circ C$		0.4	V	1
			$-55^\circ C \leq T_A \leq +125^\circ C$		0.7	V	2,3
Logic Output Sink Current	I_{SINK}	$V_O \leq 1V$	$T_A = +25^\circ C$	30		mA	1
			$-55^\circ C \leq T_A \leq +125^\circ C$	25		mA	2,3