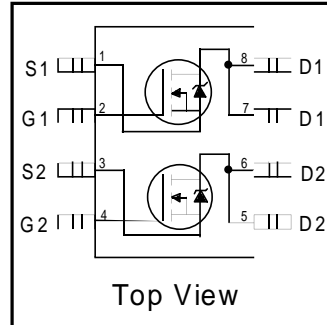


- Advanced Process Technology
- Ultra Low On-Resistance
- Dual N-Channel MOSFET
- Surface Mount
- Available in Tape & Reel
- Dynamic dv/dt Rating
- Fast Switching

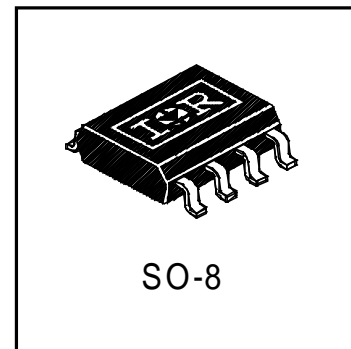
**Description**

Fourth Generation HEXFETs from International Rectifier utilize advanced processing techniques to achieve the lowest possible on-resistance per silicon area. This benefit, combined with the fast switching speed and ruggedized device design that HEXFET Power MOSFETs are well known for, provides the designer with an extremely efficient device for use in a wide variety of applications.

The SO-8 has been modified through a customized leadframe for enhanced thermal characteristics and dual-die capability making it ideal in a variety of power applications. With these improvements, multiple devices can be used in an application with dramatically reduced board space. The package is designed for vapor phase, infra red, or wave soldering techniques. Power dissipation of greater than 0.8W is possible in a typical PCB mount application.



$V_{DSS} = 20V$
$R_{DS(on)} = 0.10\Omega$
$I_D = 3.5A$



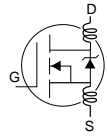
**Absolute Maximum Ratings**

	Parameter	Max.	Units
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	3.5	A
$I_D @ T_A = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	2.3	
$I_{DM}$	Pulsed Drain Current ①	14	
$P_D @ T_C = 25^\circ C$	Power Dissipation	2.0	W
	Linear Derating Factor	0.016	W/°C
$V_{GS}$	Gate-to-Source Voltage	$\pm 12$	V
dv/dt	Peak Diode Recovery dv/dt②	3.0	V/nS
$T_J, T_{STG}$	Junction and Storage Temperature Range	-55 to +150	°C
	Soldering Temperature, for 10 seconds	300(1.6mm from case)	

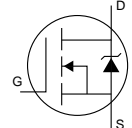
**Thermal Resistance Ratings**

	Parameter	Min.	Typ.	Max	Units
$R_{\theta JA}$	Maximum Junction-to-Ambient ④	—	—	62.5	°C/W

## Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source Breakdown Voltage	20	—	—	V	$V_{GS} = 0V, I_D = 250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	—	0.025	—	V/°C	Reference to $25^\circ\text{C}, I_D = 1mA$
$R_{DS(ON)}$	Static Drain-to-Source On-Resistance	—	—	0.10	$\Omega$	$V_{GS} = 10V, I_D = 1.8A$ ③
		—	—	0.15		$V_{GS} = 4.5V, I_D = 1.0A$ ③
$V_{GS(th)}$	Gate Threshold Voltage	1.0	—	3.0	V	$V_{DS} = V_{GS}, I_D = 250\mu A$
$g_{fs}$	Forward Transconductance	1.1	—	—	S	$V_{DS} = 15V, I_D = 3.5A$ ③
$I_{DSS}$	Drain-to-Source Leakage Current	—	—	2.0	$\mu A$	$V_{DS} = 20V, V_{GS} = 0V$
		—	—	250		$V_{DS} = 16V, V_{GS} = 0V, T_J = 125^\circ\text{C}$
$I_{GSS}$	Gate-to-Source Forward Leakage	—	—	100	nA	$V_{GS} = 12V$
	Gate-to-Source Reverse Leakage	—	—	-100		$V_{GS} = -12V$
$Q_g$	Total Gate Charge	—	—	15	nC	$I_D = 1.8A$
$Q_{gs}$	Gate-to-Source Charge	—	—	2.0		$V_{DS} = 16V$
$Q_{gd}$	Gate-to-Drain ("Miller") Charge	—	—	3.6		$V_{GS} = 10V$
$t_{d(on)}$	Turn-On Delay Time	—	7.0	—	ns	$V_{DD} = 10V$
$t_r$	Rise Time	—	10	—		$I_D = 1.8A$
$t_{d(off)}$	Turn-Off Delay Time	—	24	—		$R_G = 8.2\Omega$
$t_f$	Fall Time	—	30	—		$R_D = 26\Omega$
$L_D$	Internal Drain Inductance	—	4.0	—	nH	Between lead, 6mm (0.25in.) from package and center of die contact 
$L_S$	Internal Source Inductance	—	6.0	—		
$C_{iss}$	Input Capacitance	—	320	—	pF	$V_{GS} = 0V$
$C_{oss}$	Output Capacitance	—	250	—		$V_{DS} = 15V$
$C_{rss}$	Reverse Transfer Capacitance	—	75	—		$f = 1.0MHz$

## Source-Drain Ratings and Characteristics

	Parameter	Min.	Typ.	Max.	Units	Conditions
$I_S$	Continuous Source Current (Body Diode)	—	—	2.0	A	MOSFET symbol showing the integral reverse p-n junction diode. 
$I_{SM}$	Pulsed Source Current (Body Diode) ①	—	—	14		
$V_{SD}$	Diode Forward Voltage	—	—	1.2	V	$T_J = 25^\circ\text{C}, I_S = 1.7A, V_{GS} = 0V$ ③
$t_{rr}$	Reverse Recovery Time	—	36	54	ns	$T_J = 25^\circ\text{C}, I_F = 1.7A$
$Q_{rr}$	Reverse Recovery Charge	—	41	62	nC	$di/dt = 100A/\mu s$ ③
$t_{on}$	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by $L_S + L_D$ )				

### Notes:

① Repetitive rating; pulse width limited by max. junction temperature.

③ Pulse width  $\leq 300\mu s$ ; duty cycle  $\leq 2\%$ .

②  $I_{SD} \leq 3.5A, di/dt \leq 90A/\mu s, V_{DD} \leq V_{(BR)DSS}, T_J \leq 150^\circ\text{C}$

④ Surface mounted on FR-4 board,  $t \leq 10sec$ .

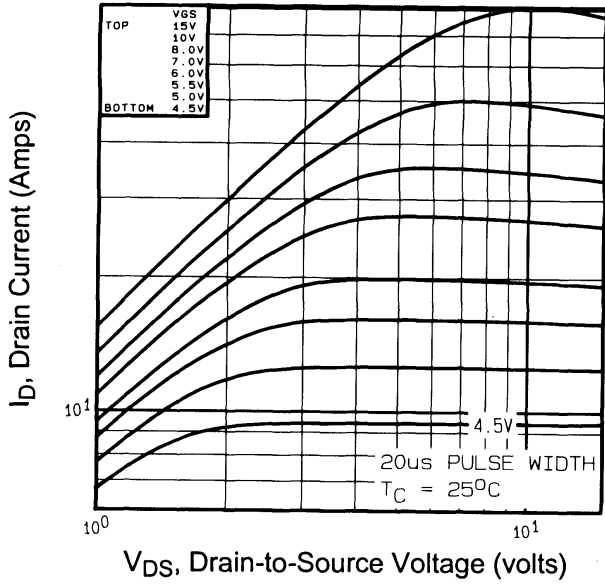


Fig 1. Typical Output Characteristics,

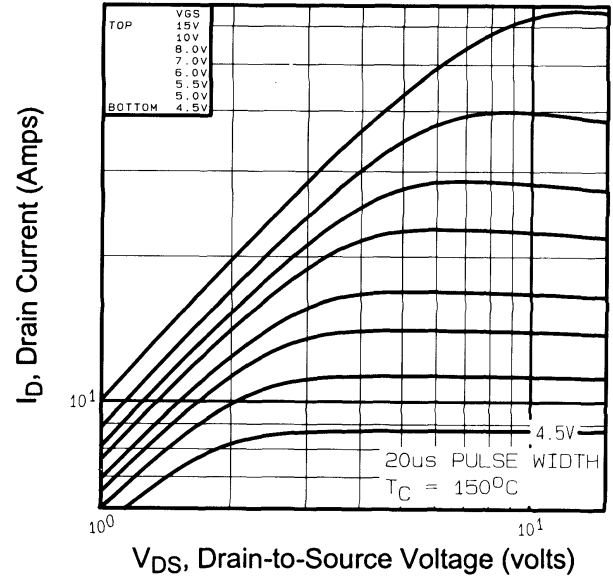


Fig 2. Typical Output Characteristics,

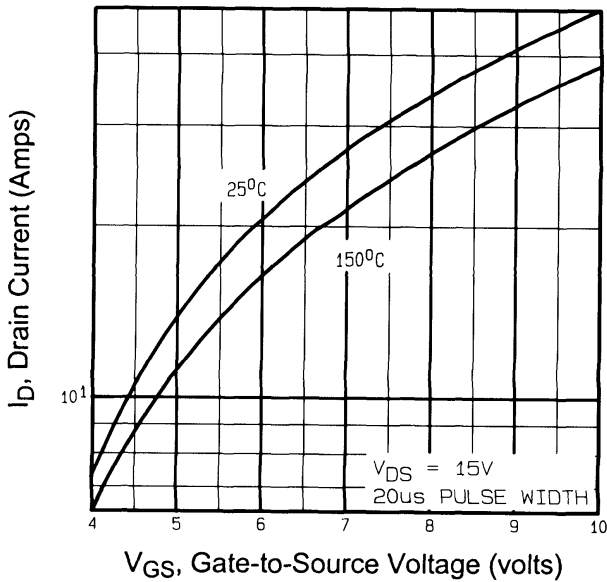


Fig 3. Typical Transfer Characteristics

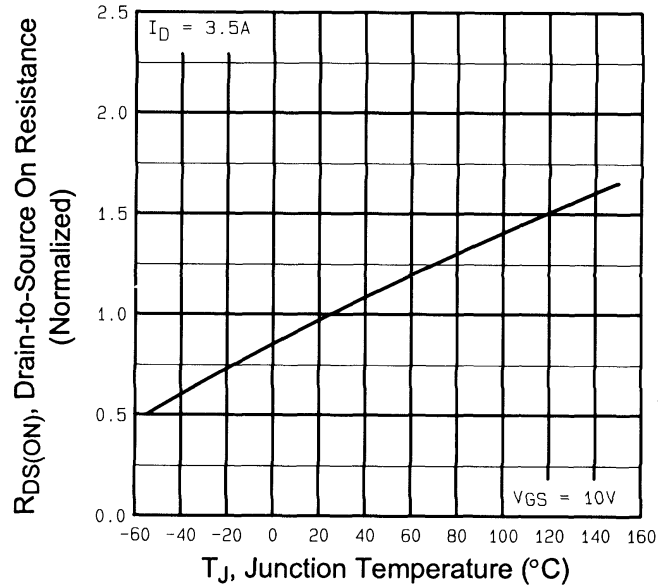
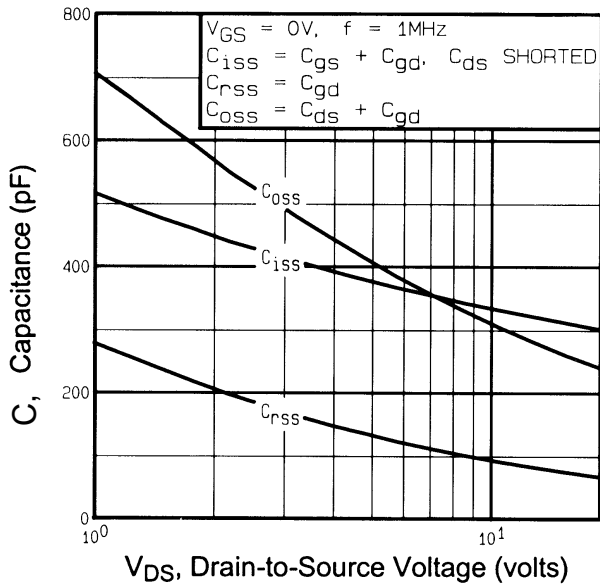
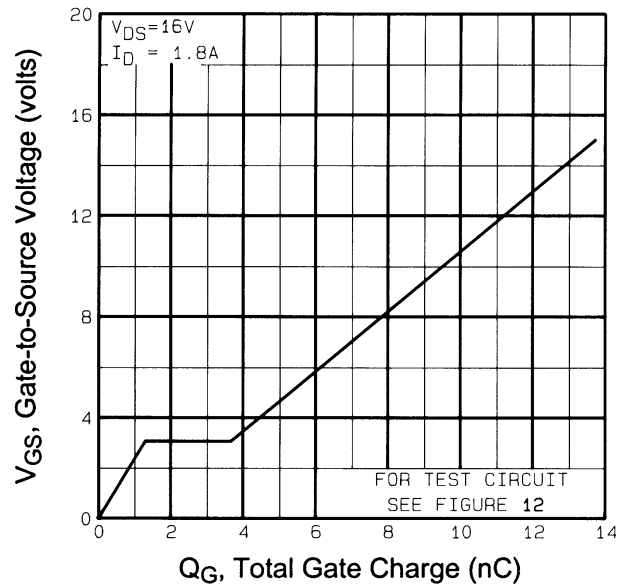


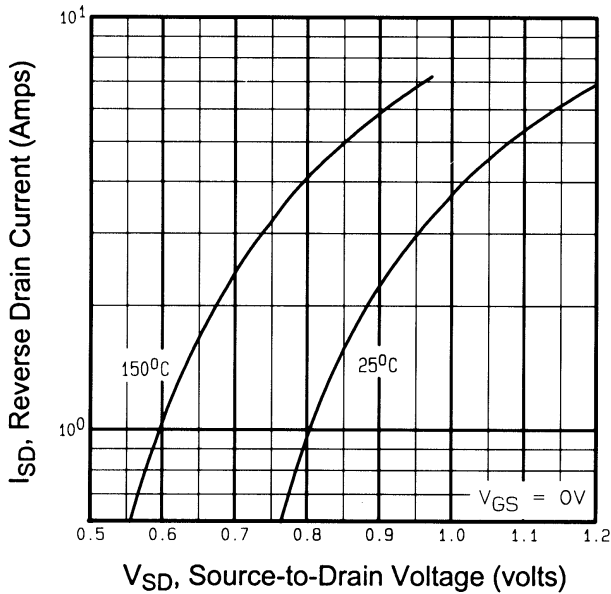
Fig 4. Normalized On-Resistance Vs. Temperature



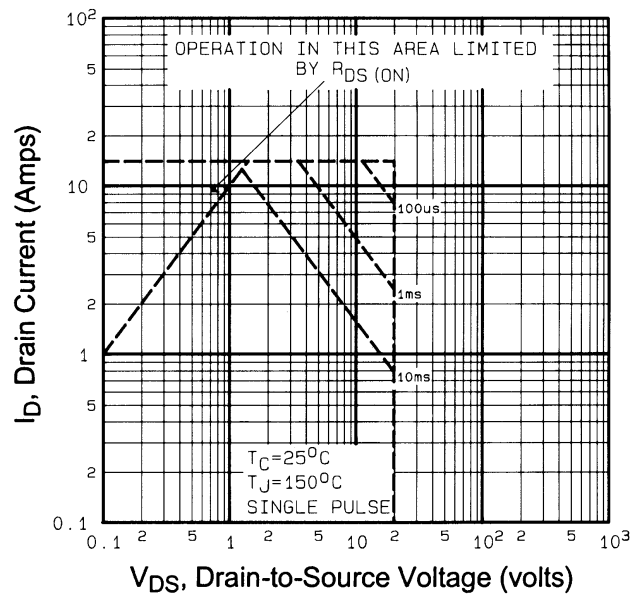
**Fig 5.** Typical Capacitance Vs. Drain-to-Source Voltage



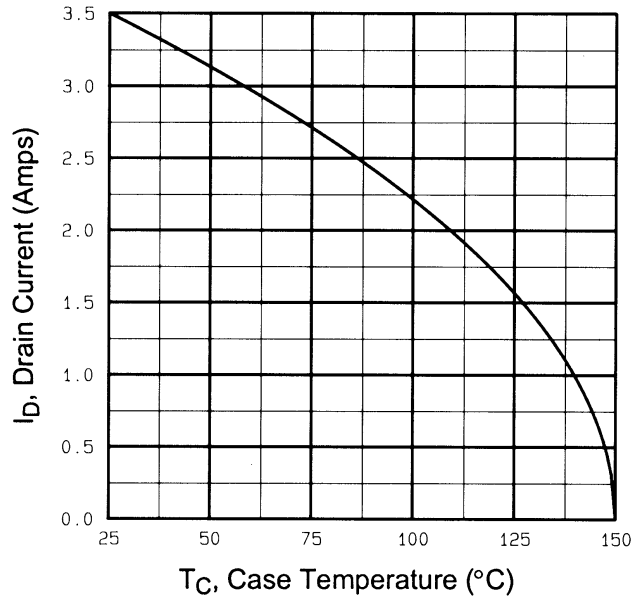
**Fig 6.** Typical Gate Charge Vs. Gate-to-Source Voltage



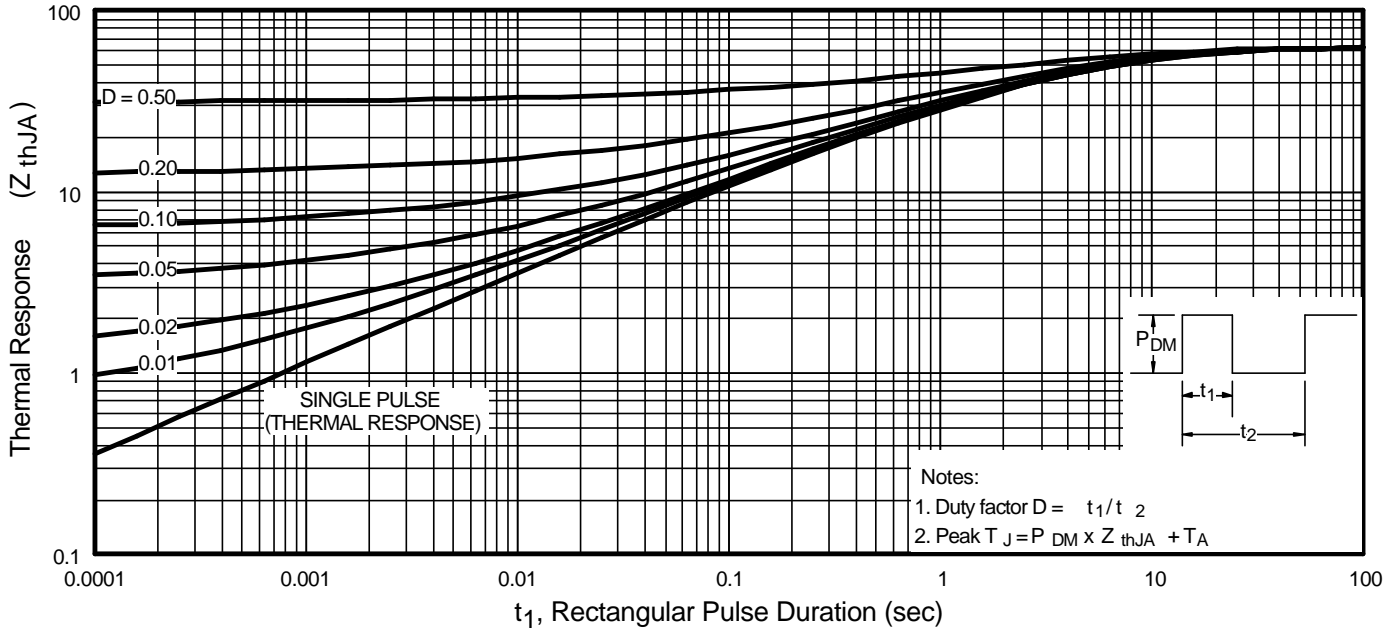
**Fig 7.** Typical Source-Drain Diode Forward Voltage



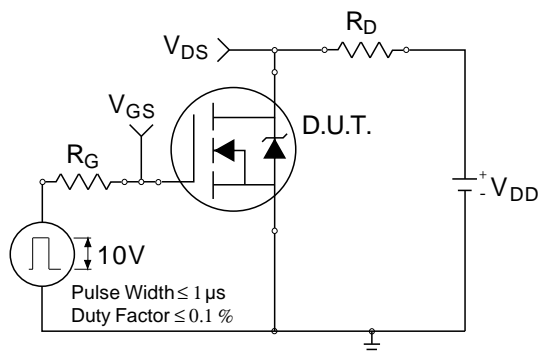
**Fig 8.** Maximum Safe Operating Area



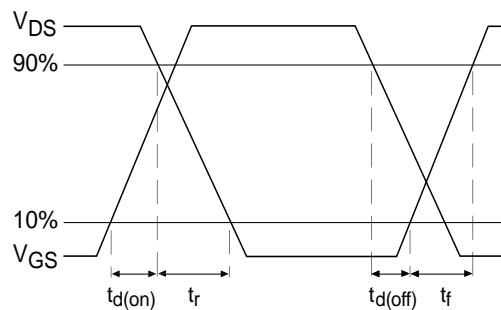
**Fig 9.** Maximum Drain Current Vs. Case Temperature



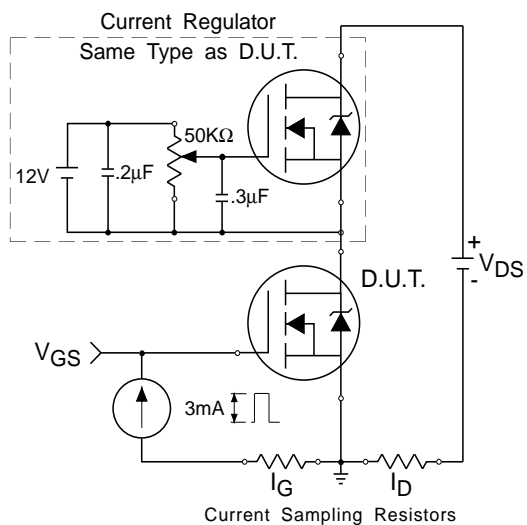
**Fig 10.** Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



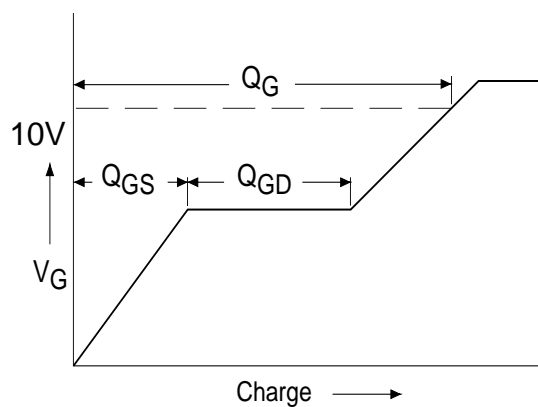
**Fig 11a.** Switching Time Test Circuit



**Fig 11b.** Switching Time Waveforms

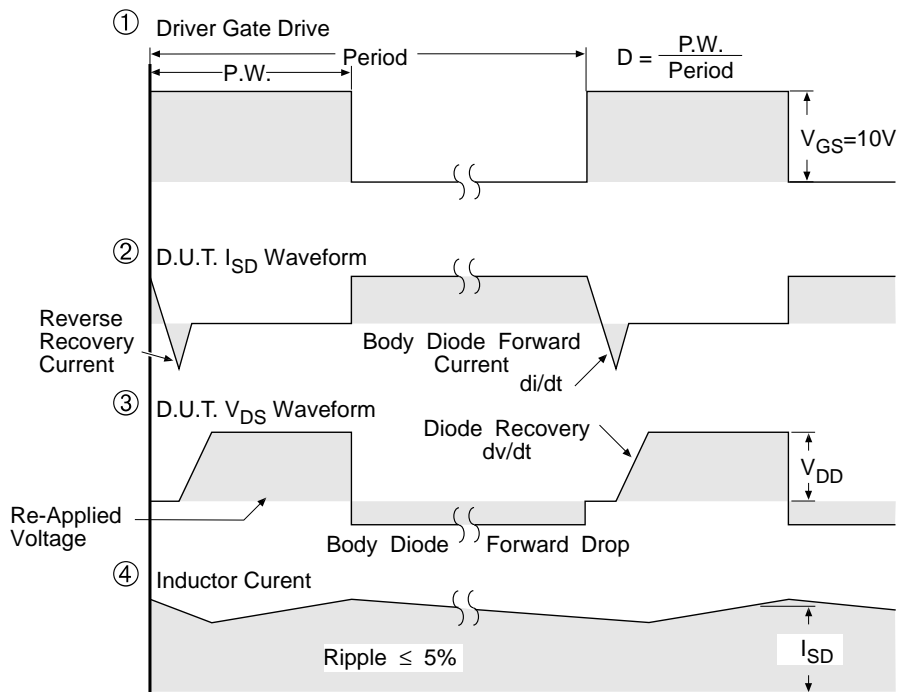
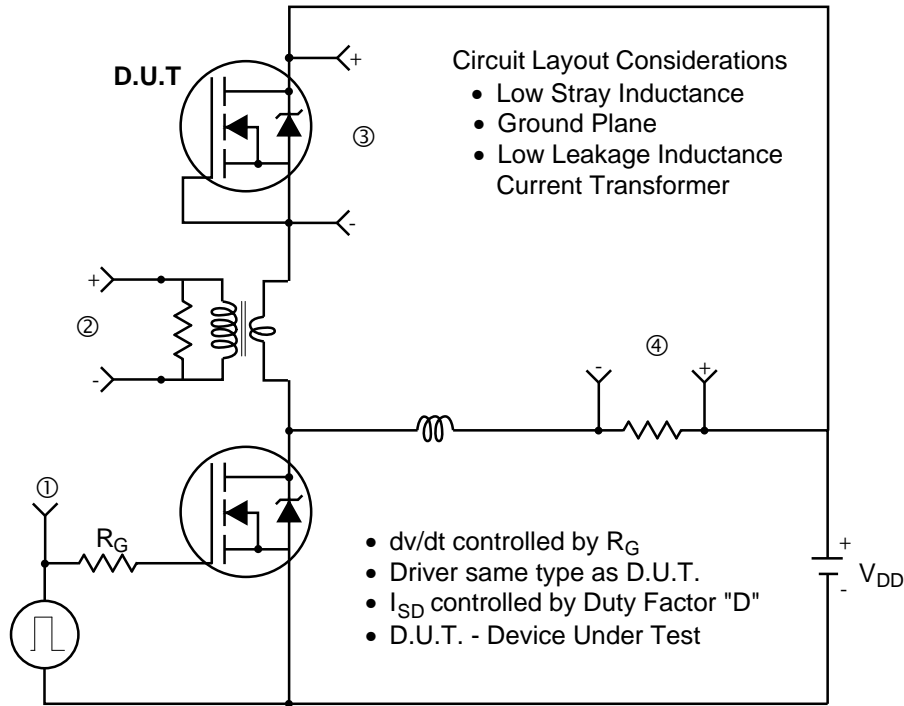


**Fig 12a.** Gate Charge Test Circuit



**Fig 12b.** Basic Gate Charge Waveform

**Peak Diode Recovery dv/dt Test Circuit**



\*  $V_{GS} = 5V$  for Logic Level Devices

**Fig 13.** For N-Channel HEXFETS

# IRF7101

## Package Outline SO8 Outline

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.0532	.0688	1.35	1.75
A1	.0040	.0098	0.10	0.25
B	.014	.018	0.36	0.46
C	.0075	.0098	0.19	0.25
D	.189	.196	4.80	4.98
E	.150	.157	3.81	3.99
e	.050 BASIC		1.27 BASIC	
e1	.025 BASIC		0.635 BASIC	
H	.2284	.2440	5.80	6.20
K	.011	.019	0.28	0.48
L	0.16	.050	0.41	1.27
θ	0°	8°	0°	8°

**RECOMMENDED FOOTPRINT**

**NOTES:**

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M-1982.
- CONTROLLING DIMENSION : INCH.
- DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
- OUTLINE CONFORMS TO JEDEC OUTLINE MS-012AA.
- 5 DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS  
MOLD PROTRUSIONS NOT TO EXCEED 0.25 (.006).
- 6 DIMENSIONS IS THE LENGTH OF LEAD FOR SOLDERING TO A SUBSTRATE..

## Part Marking Information

### SO8

EXAMPLE : THIS IS AN IRF7101

**TOP VIEW:**

- INTERNATIONAL RECTIFIER LOGO**: IR logo
- PART NUMBER**: 312
- DATE CODE (YWW)**: Y = LAST DIGIT OF THE YEAR, WW = WEEK
- Part Marking**: F7101

**BOTTOM VIEW:**

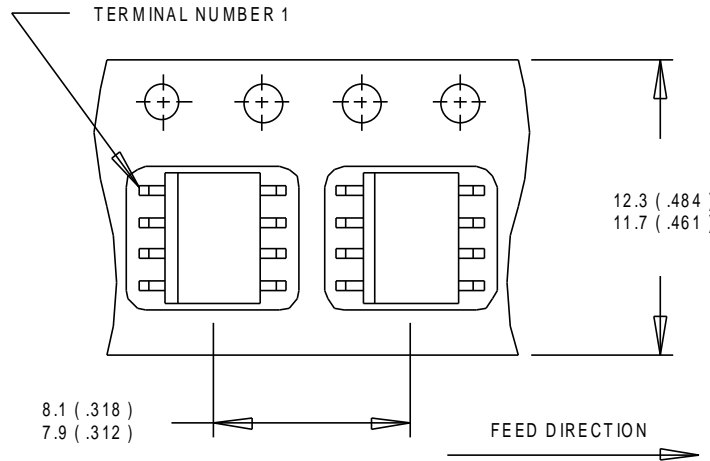
- WAFER LOT CODE (LAST 4 DIGITS)**: XXXX



**Tape & Reel Information**

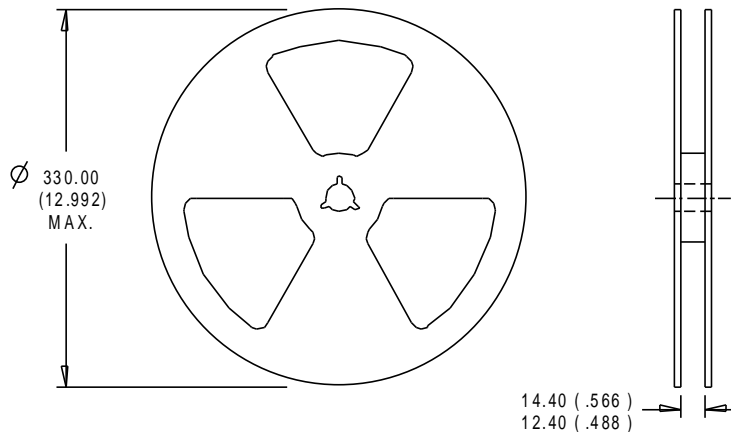
**S08**

Dimensions are shown in millimeters (inches)



**NOTES:**

1. CONTROLLING DIMENSION : MILLIMETER.
2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS(INCHES).
3. OUTLINE CONFORMS TO EIA-481 & EIA-541.



**NOTES :**

1. CONTROLLING DIMENSION : MILLIMETER.
2. OUTLINE CONFORMS TO EIA-481 & EIA-541.