

SANYO Semiconductors DATA SHEET



Monolithic Linear IC LA1266 — Electronic Tuning Type **AM/FM** Tuner System

Features

- Minimum number of external parts required.
- Excellent S/N
- Local OSC with ALC
- Local OSC buffer
- Tuning indicator pin (common with narrow-band stop signal and muting drive output)
- Variable stop sensitivity (variable separately for FM, AM)
- Less tweet interference
- Signal meter pin
- IF count output

Functions

- FM : IF amplifier, quadrature detector, AF preamplifier, signal meter, IF count outputlifier, tuning indicator drive output (common with stop signal, muting drive output)
- AM : RF amplifier, MIX, OSC (with ALC), IF amplifier, detector, AGC, signal meter, tuning indicator drive output (common with stop signal), IF count output, local OSC buffer.

Any and all SANYO Semiconductor Co., Ltd. products described or contained herein are, with regard to "standard application", intended for the use as general electronics equipment (home appliances, AV equipment, communication device, office equipment, industrial equipment etc.). The products mentioned herein shall not be intended for use for any "special application" (medical equipment whose purpose is to sustain life, aerospace instrument, nuclear control device, burning appliances, transportation machine, traffic signal system, safety equipment etc.) that shall require extremely high level of reliability and can directly threaten human lives in case of failure or malfunction of the product or may cause harm to human bodies, nor shall they grant any guarantee thereof. If you should intend to use our products for applications outside the standard applications of our customer who is considering such use and/or outside the scope of our intended standard applications, please consult with us prior to the intended use. If there is no consultation or inquiry before the intended use, our customer shall be solely responsible for the use.

Specifications of any and all SANYO Semiconductor Co., Ltd. products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

Specifications

Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max	Pins 7,8,20	16	V
Flow-in current	۱ ₈	Pin 8	20	mA
Flow-out current	I ₂₂	Pin 22	1	mA
	I ₂₄	Pin 24	2	mA
Allowable power dissipation	Pd max	Ta ≤ 60°C	700	mW
Operating temperature	Topr		-20 to +70	°C
Storage temperature	Tstg		-40 to +125	°C

Operating Conditions at $Ta = 25^{\circ}C$

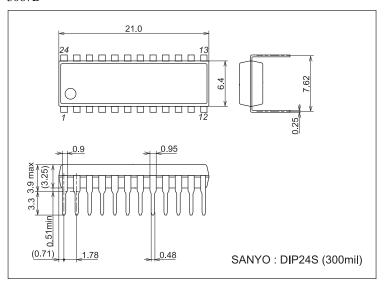
Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V _{CC}		8.5	V
Operating voltage range	V _{CC} op		6 to 14	V

Electrical Characteristics at $Ta = 25^{\circ}C$, $V_{CC} = 8.5V$, See Test Circuit.

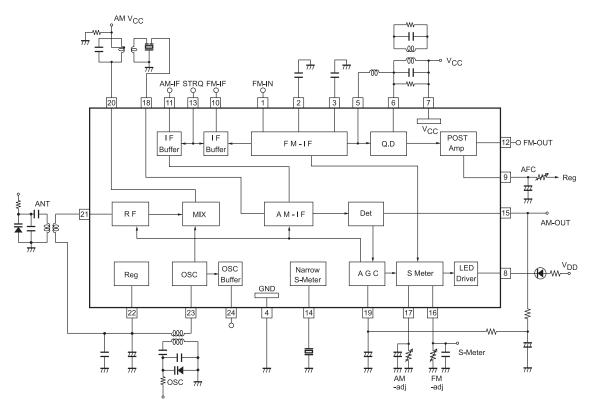
Parameter		Conditions		Ratings		
	Symbol		min	typ	max	Unit
AM : fc = 1MHz, fm = 1kHz						
Quiescent current	ICCO	No input		22	30	mA
Detection output	V _O 1	$V_{IN} = 20$ dB μ , 30% mod.	30	60	110	mV
	V _O 2	$V_{IN} = 80$ dB μ , 30% mod.	90	150	210	mV
Signal to noise ratio	S/N1	$V_{IN} = 20 dB\mu$	15	19		dB
	S/N2	$V_{IN} = 80 dB\mu$	49	54		dB
Total harmonic distortion	THD1	V _{IN} = 80dBμ, 30% mod.		0.3	1.0	%
	THD2	V _{IN} = 107dBμ, 30% mod.		0.3	1.0	%
Signal meter output	V _{SM} 1	No input	0	0	0.2	V
	V _{SM} 2	$V_{IN} = 80 dB\mu$	1.8	2.8	3.5	V
LED drive sensitivity	LED-ON	I _{LED} = 1mA	13	23	33	dBm
Local OSC buffer output	Vosc	fosc = 1.45MHz	220	280	340	mV
IF buffer output	VIF	$V_{IN} = 20 dB\mu$	120	180	250	mV
FM : fc = 10.7MHz, fm = 1kHz	<u>.</u>					
Quiescent current	ICCO	No input		29	40	mA
Input limiting sensitivity	–3dBL.S.	3dB down, 100% mod.		31	37	dBμ
Demodulation output	VO	V _{IN} = 100dBμ, 100% mod.	270	390	540	mV
S/N ratio	S/N	V _{IN} = 100dBμ	78	84		dB
Total harmonic distortion	THD	V _{IN} = 100dBμ, 100% mod.		0.03	0.3	%
Signal meter output	V _{SM} 1	No input	0	0	0.2	V
	V _{SM} 2	V _{IN} = 100dBμ	1.7	2.3	3.1	V
LED drive sensitivity	LED-ON	I _{LED} = 1mA	46	61	76	dBμ
LED drive bandwidth	LED-BW	$V_{IN} = 100 dB\mu$, $I_{LED} = 1 mA$	70	105	140	kHz
AM rejection ratio	AMR	V _{IN} = 100dBμ, 100% mod. AM-1kHz, 30% mod.	45	58		dB
IF buffer output	VIF	$V_{IN} = 50 dB\mu$	110	160	230	mV

Package Dimensions

unit : mm (typ) 3067B

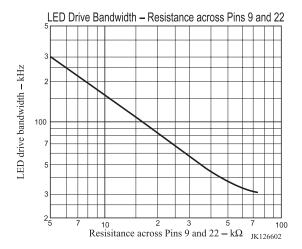


Block Diagram

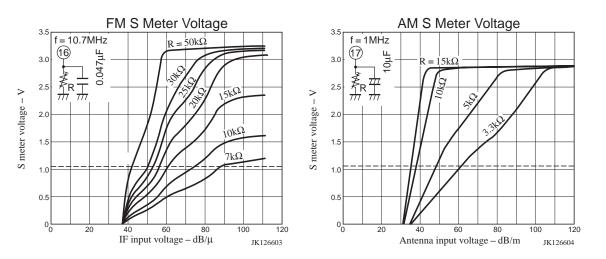


How to use LA1266

- 1. LED driver, muting drive output, stop signal (SD).
- For LED drive, muting drive output, stop signal, the output at pin 8 is used.
- Voltage on pin 8, when tuned, turns from "H" to "L" . (Active-low)
- Signal bandwidth at pin 8
 - For AM, the bandwidth depends on the C.F (BFU450CN) at pin 14. If a capacitor is connected in place of the C.F, the bandwidth will get wider.
- For FM, the bandwidth depends on the resistance across pins 9 and 22. If the resistance is increased, the bandwidth will get narrower. $R = 15k\Omega$ makes the bandwidth approximately 110kHz.

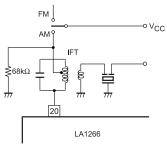


- Sensitivity adjustment of LED, muting, stop signal
 - For FM, the semifixed variable resistor across pin 16 and GND is used.
- For AM, the semifixed variable resistor across pin 17 and GND is used.
- The relation (for AM, FM) between signal meter voltage and input voltage with the resistance of the semifixed variable resistor as a parameter is shown below.



2. AM/FM changeover

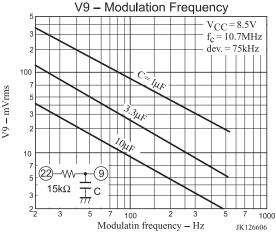
- AM/FM changeover is made using pin 20 as shown right.
- However, the voltage on pin 20 relative to V_{CC} (pin 7) must be within the range of +0.1V to -0.8V. If not within this range, distortion and selectivity will get worse. A resistance of $68k\Omega$ at the IFT cold terminal, which is used to prevent the changeover circuit from malfunctioning, must be connected.



- 3. Local OSC buffer output
- When local OSC buffer output waveform is saw-toothed at the SW mode, connect a reistance of $1.2k\Omega$ or thereabouts across pin 24 and GND.
- 4. Capacitance across pin 9 and GND

A large capacitance across pin 9 and GND may cause a misstop at an adjacent channel when the channel select speed is made faster at the autmatic channel select mode. In thes case, decrease the capacitance across pin 9 and GND.

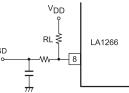
However, if too decreased, the LED will flutter at low modulation frequencies at the time of detuning. Therefore, it is recommended to fix the capacitance across pin 9 and GND to be 3.3μ F to 10μ F. The relation between modulation frequency and demodulation output voltage on pin 9 with the capacitance across pin 9 and GND as a parameter is shown right.



- 5. If the coupling coefficient of the local OSC coil is small and an antiresonance point of approximately 100MHz is present or the stray capacitance across pin 24 and pin 23 is large, a parasitic oscillation of approximately 100MHz may occur in the buffer output (pin 24). In this case, connect a capacitance of approximately 30pF across pin 24 and GND.
- 6. AM OSC coil

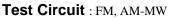
Generally speaking, the following should be noted. Avoid winding with loose coupling between primary side and secondary side (especially SW1, SW2). To be concrete, the pot core type is better than the screw type which is loose in coupling. This prevents the local OSC frequency from turning third resonance frequency related to the coupling coefficient.

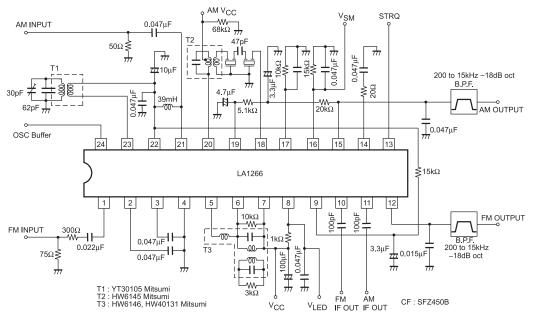
7. Resistance across pin 8 and V_{DD} If pin 8 is used for stop signal (SD) only, without using LED, it is recommended to fix resistance R_L across pin 8 and V_{DD} to be $51k\Omega$ to $100k\Omega$.



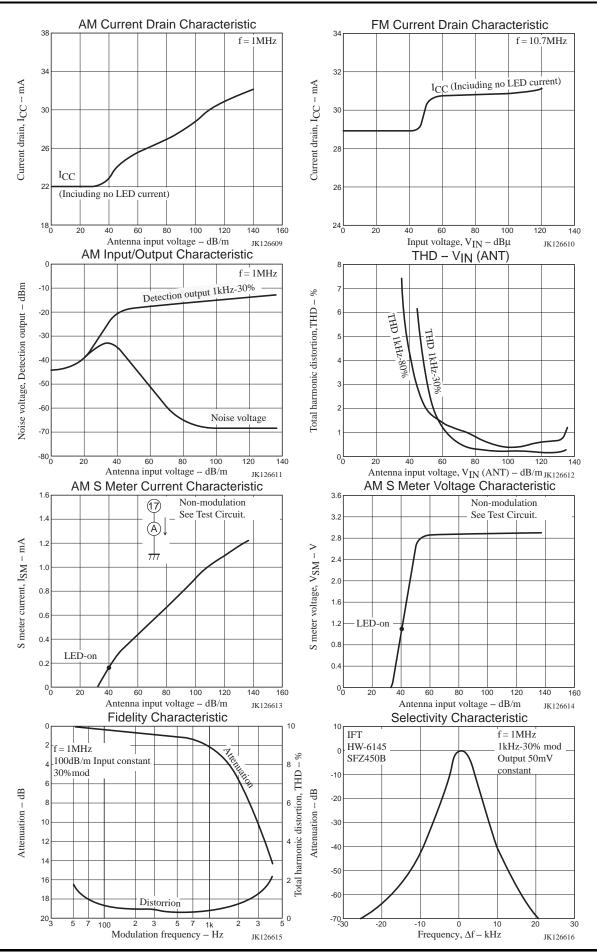
- 8. To prevent whistle from worsening, make the pattern of AM output pin 15 as short as possible.
- 9. AM, FM IF buffer output

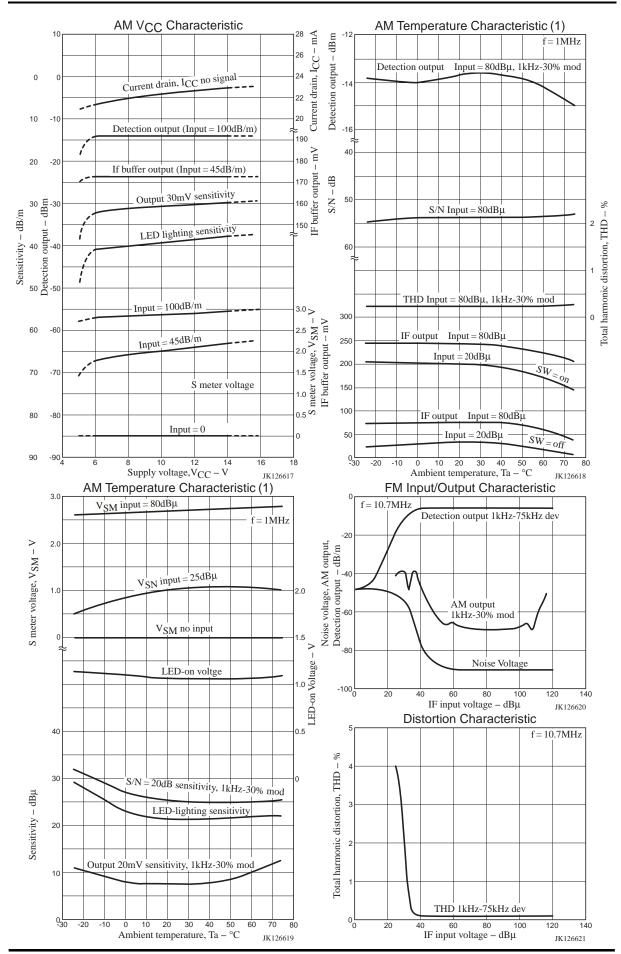
Application of a voltage to pin 13 (STRQ) causes AM IF(455kHz) signal (at AM mode) and FM IF(10.7MHz) signal (at FM mode) to be delivered at pin 11 and pin 10, respectively. It is recommended that the voltage to be applied to

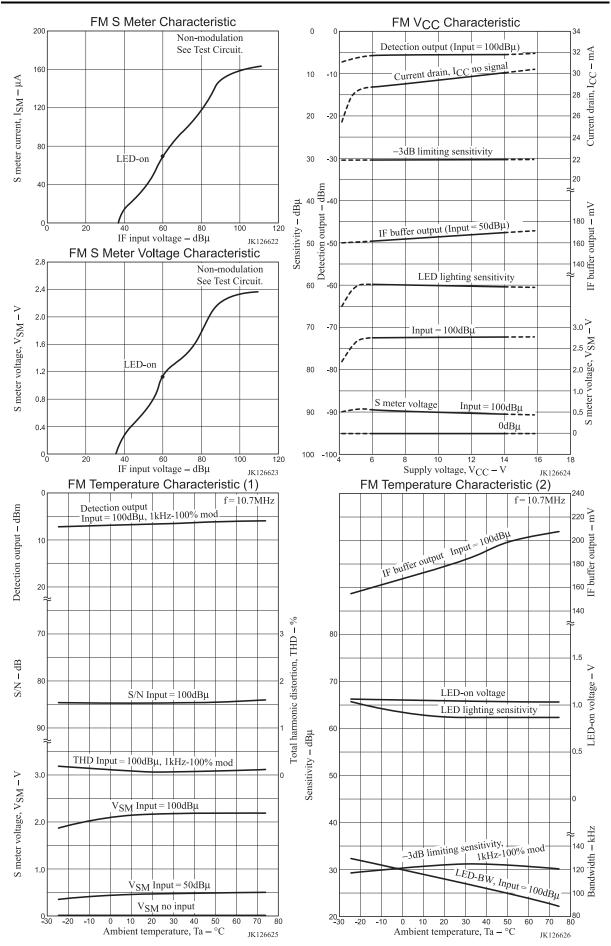


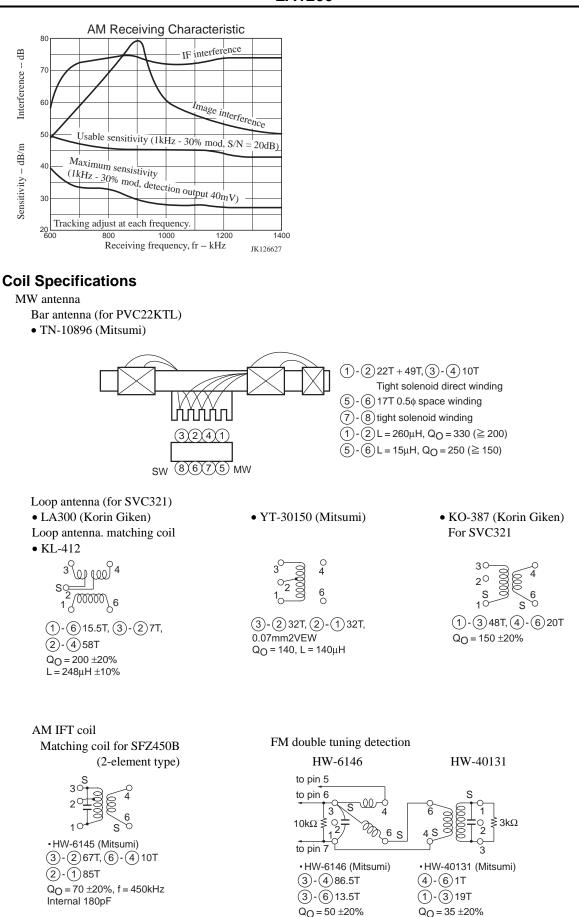


No.2291-5/11



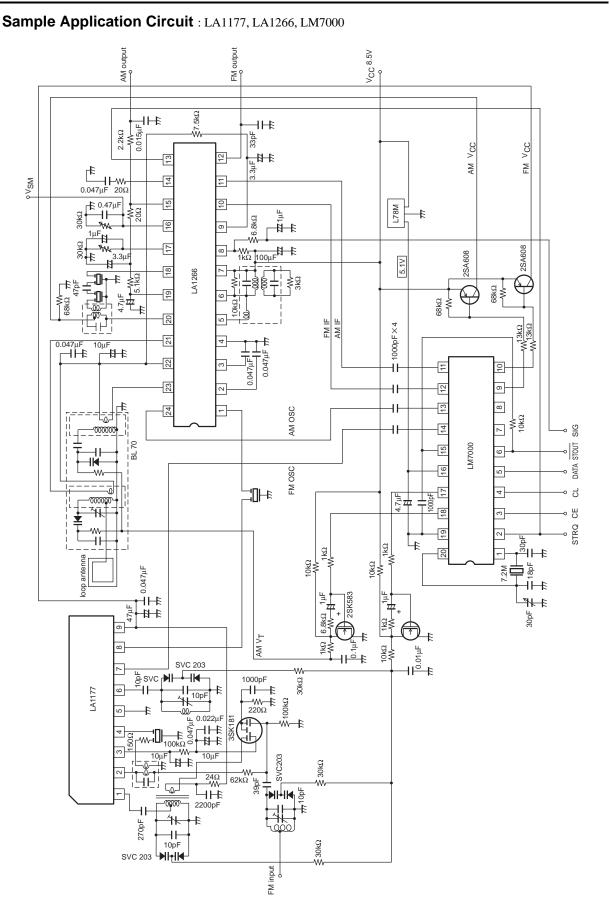


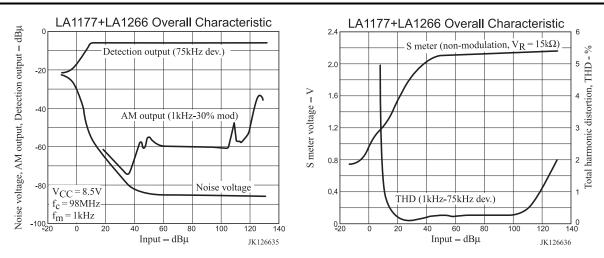




Internal 100pF ±10%

Internal 100pF ±10%





- SANYO Semiconductor Co.,Ltd. assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all SANYO Semiconductor Co.,Ltd. products described or contained herein.
- SANYO Semiconductor Co.,Ltd. strives to supply high-quality high-reliability products, however, any and all semiconductor products fail or malfunction with some probability. It is possible that these probabilistic failures or malfunction could give rise to accidents or events that could endanger human lives, trouble that could give rise to smoke or fire, or accidents that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all SANYO Semiconductor Co.,Ltd. products described or contained herein are controlled under any of applicable local export control laws and regulations, such products may require the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written consent of SANYO Semiconductor Co.,Ltd.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the SANYO Semiconductor Co.,Ltd. product that you intend to use.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production.
- Upon using the technical information or products described herein, neither warranty nor license shall be granted with regard to intellectual property rights or any other rights of SANYO Semiconductor Co.,Ltd. or any third party. SANYO Semiconductor Co.,Ltd. shall not be liable for any claim or suits with regard to a third party's intellectual property rights which has resulted from the use of the technical information and products mentioned above.

This catalog provides information as of May, 2008. Specifications and information herein are subject to change without notice.