

SANYO Semiconductors DATA SHEET

LA6339 — Monolithic Linear IC High-Performance Quad Comparator

Overview

The LA6339 is a high-performance quad comparator that is capable of operating from a single power supply over a wide range of 2V to 36V. Because of its excellent input characteristics and low power, it can be very conveniently applied to multisignal parallel comparator circuits that require high-density assembly.

Features

- Wide supply voltage range (Single supply : 2.0 to 36.0V, dual supplies : ± 1.0 to ± 18.0 V).
- Wide common-mode input voltage range (0 to V_{CC}-1.5V).
- Open collector output enabling wired OR.
- Small current dissipation (0.8mA/V_{CC} = 5V, $R_L = \infty$) and low power.

Specifications

Absolute Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max		36	V
Differential input voltage	V _{ID}		36	V
Common-mode input voltage	VICM		–0.3 to +36	V
Allowable power dissipation	Pd max		700	mW
Operating temperature	Topr		-30 to +85	°C
Storage temperature	Tstg		–55 to +125	°C

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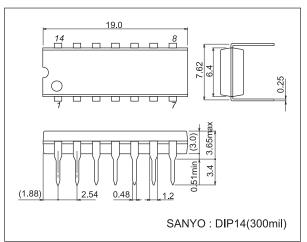
SANYO Semiconductor Co., Ltd. TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

Electrical Characteristics at $Ta = 25^{\circ}C$, $V_{CC} = 5V$

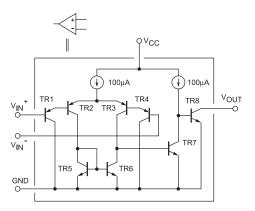
Parameter	Symbol	Conditions	Test Circuit	Ratings			
				min	typ	max	Unit
Input offset voltage	V _{IO}		1		±2	±5	mV
Input offset current	lio		2		±5	±50	nA
Input bias current	۱ _B		3		25	250	nA
Common-mode input voltage	VICM			0		V _{CC} -1.5	V
Current drain	lcc	$R_L = \infty$	4		0.8	2	mA
Voltage gain	VG	$R_L = 15k\Omega$	5		200		V/mV
Response time		$V_{RL} = 5V, R_L = 5.1 k\Omega$	6		1.3		μs
Output sink current	ISINK	$V_{IN}^{-} = 1V, V_{IN}^{+} = 0V, V_{O} \le 1.5V$	7	6	16		mA
Output saturation voltage	V _{OL}	$V_{IN}^{-} = 1V, V_{IN}^{+} = 0V, I_{SINK} \le 3mA$	8		0.2	0.4	V
Output leakage current	ILEAK	$V_{IN}^{-} = 0V, V_{IN}^{+} = 1V, V_{O} = 5V$	9		0.1		nA

Package Dimensions

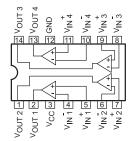
unit : mm (typ) 3003B



Equivalent Circuit (1 unit)

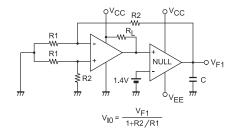


Pin Assignment

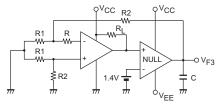


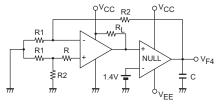
Test Circuits

1. Input Offset Voltage



3. Input Bias Current





9VCC R2

1.4V

V_{I0} =

 R_{L}

٩[∨]cc

οV_{F2}

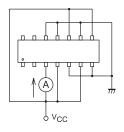
† c

NULL

V_{F2}-V_{F1} R(1+R2/R1)

 $I_{B} = \frac{|V_{F3} - V_{F4}|}{2R(1 + R2/R1)}$

4. Current Drain



5. Voltage Gain

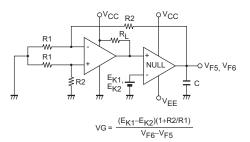
2. Input Offset Current

R

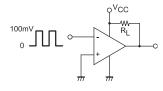
≹R2 ‴

R1 R

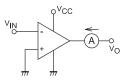
-W-R1 -W-



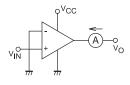
6. Response Time

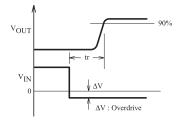


7. Output Sink Current

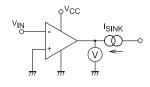


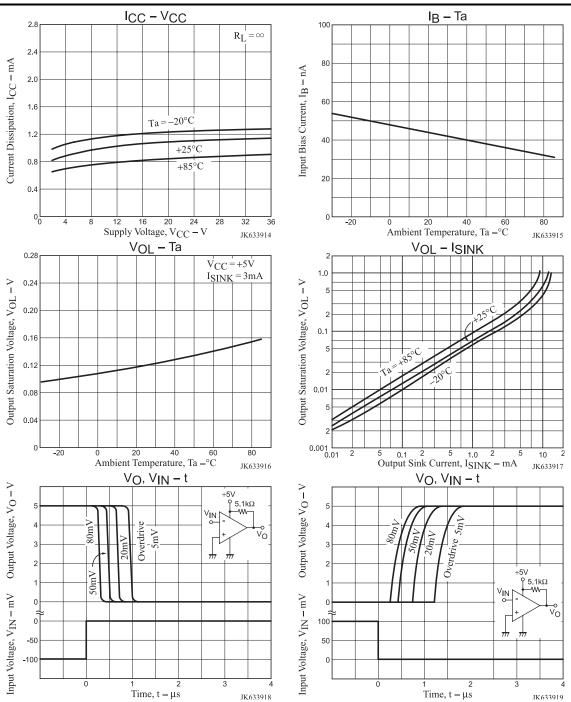
9. Output Leakage Current



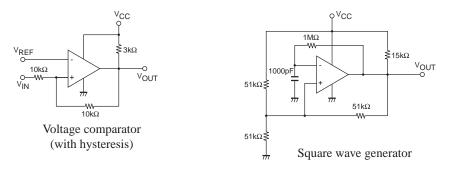


8. Output Saturation Voltage





Sample Application Circuits



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