

# DBL 5021

## UNIVERSAL SPEECH NETWORK

The DBL5021 can fulfill the standards of other countries by changing the external component.

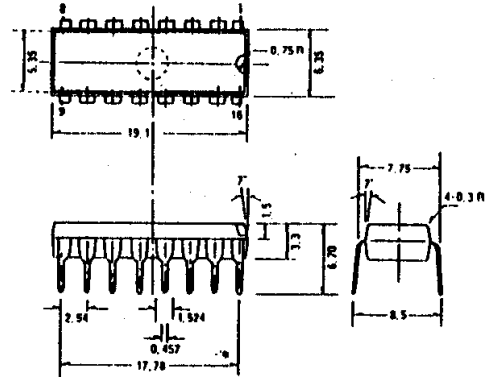
Dynamic or piezoelectric type can be used with receiver. Each gain is controlled with AGC in accordance with the line current. The DBL5021 can be operated down to DC line 1.6V

### FEATURES

- Independent adjustment of DC resistance and AC impedance.
- Voltage regulator with adjustable static resistance
- Symmetrical high-impedance inputs for dynamic magnetic or piezo-electric microphones
- Receiving amplifier for dynamic, magnetic or piezo-electric earpieces
- DTMF signal interface
- Low DC line voltage : operateds down to 1.6V
- Mute function
- DC line voltage adjustment facility

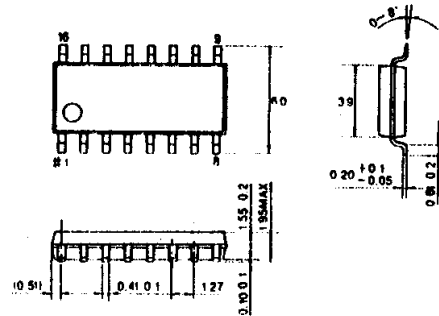
16DIP

Unit : mm



16SOP

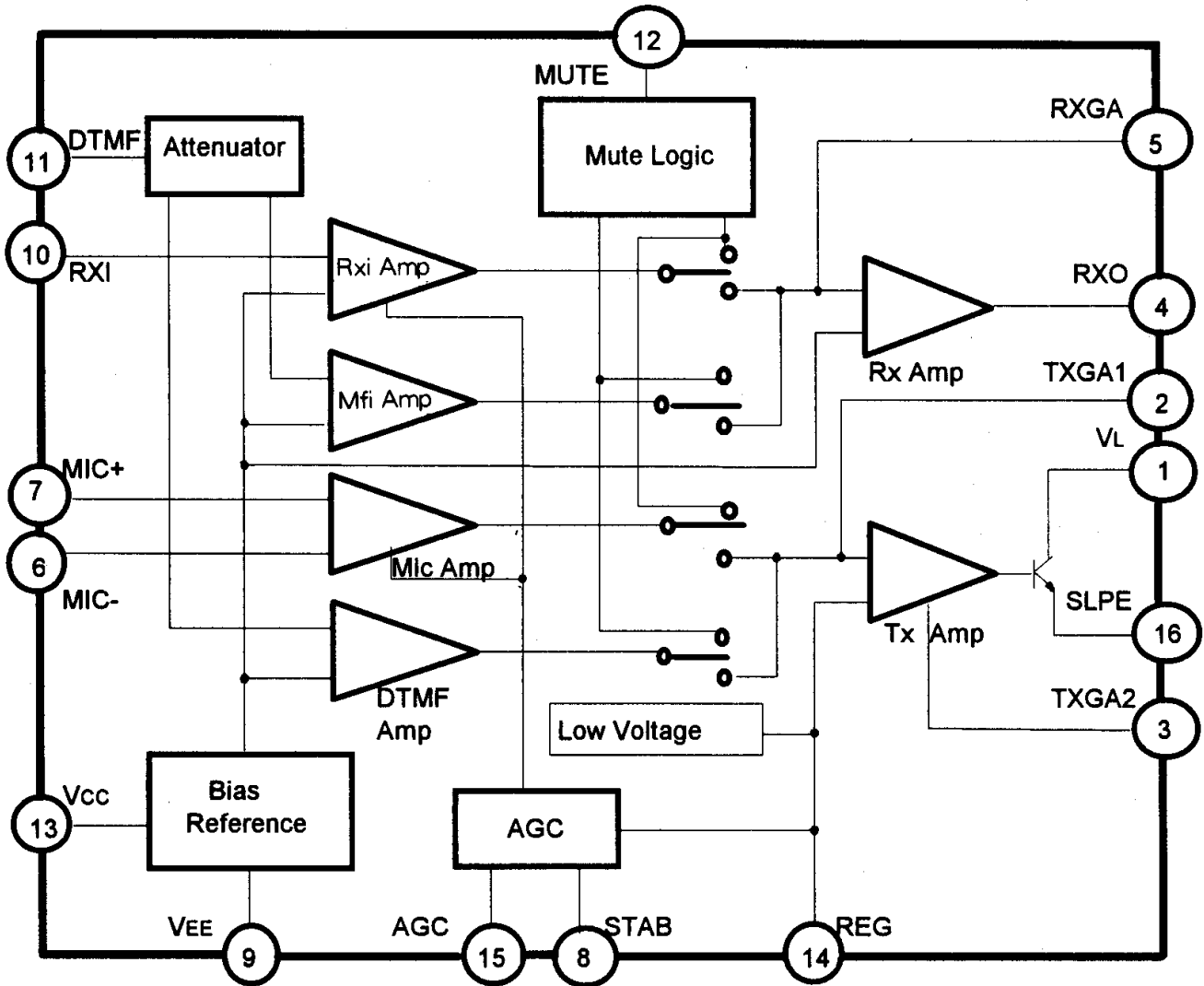
DBL5021-V



### MAXIMUM RATINGS

Characteristic	Symbol	Rating	Unit
Line Voltage	$V_L$	12	V
Line Current	$I_L$	140	mA
Power Dissipation	$P_D$	666	mW
Operating Temperature	$T_{OPr}$	-25 ~ +70	°C
Storage Temperature	$T_{stg}$	-40 ~ +125	°C

## □ BLOCK DIAGRAM



## □ PIN DESCRIPTION

Pin No	Symbol	Description
1	VL	Positive power line and TX Amp output
2	TXGA1	TX gain adjustment
3	TXGA2	TX gain adjustment
4	RXO	RX output
5	RXGA	RX gain adjustment
6	MIC <sup>-</sup>	Mic(-) Input
7	MIC <sup>+</sup>	Mic(+ ) Input

# DBL 5021

## PIN DESCRIPTION

Pin No	Symbol	Description
8	SRAB	Current stabilizer
9	V <sub>EE</sub>	Ground
10	RXI	Rx input
11	DTMF	DTMF signal input
12	MUTE	'H' Mute input
13	V <sub>CC</sub>	Internal power supply pin
14	REG	Voltage regulator decoupling
15	AGC	Automation gain control input
16	SLPE	Slope(DC resistance)adjustment

## ELECTRICAL CHARACTERISTICS

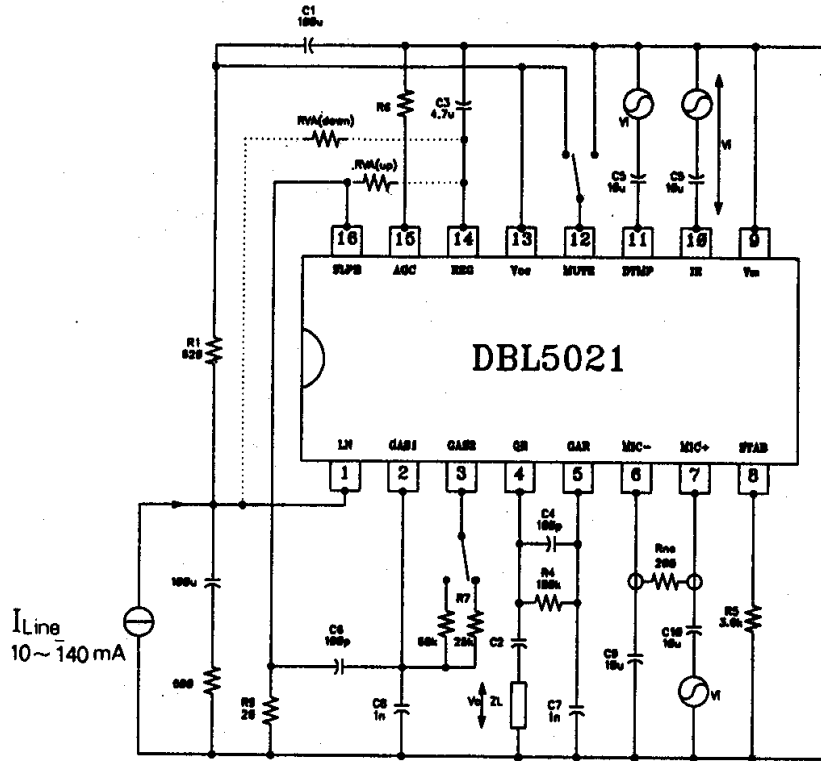
Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit	
Line Voltage	V <sub>L</sub>	I <sub>Line</sub> = 1mA	—	1.6	—	V	
		I <sub>Line</sub> = 4mA	—	1.9	—		
		I <sub>Line</sub> = 15mA	3.55	4	4.25		
		I <sub>Line</sub> = 100mA	4.9	5.7	6.5		
		I <sub>Line</sub> = 140mA	—	—	7.5		
Variation with temperature	$\Delta V_L / \Delta T$	I <sub>Line</sub> = 15mA	—	(-)-0.3	—	mV/k	
Voltage drop over circuit between V <sub>L</sub> & V <sub>EE</sub> with external resistor R <sub>AV</sub>	R <sub>VA</sub>	I <sub>Line</sub> = 15mA R <sub>VA</sub> (R <sub>REG</sub> to S <sub>LPE</sub> ) = 39K	—	4.5	—	V	
		I <sub>Line</sub> = 15mA R <sub>VA</sub> (V <sub>L</sub> to R <sub>REG</sub> ) = 68K	—	3.5	—	V	
Supply Voltage	V <sub>CC</sub>	Mute = High	I <sub>p</sub> = 1.2mA	2.2	2.7	—	V
			I <sub>p</sub> = 0mA	—	3.4	—	
MIC input impedance	Z <sub>i</sub> (mic)	Between MIC- & MIC+	—	64	—	K $\Omega$	
		MIC- or MIC+ to V-	—	32	—		
Common mode rejection ratio	CMRR(T <sub>x</sub> )	—	—	82	—	dB	
TX Gain	G <sub>v</sub> (T <sub>x</sub> )	R7 = 68K, V <sub>in</sub> = 3mV <sub>rms</sub>	50.5	52	53.5	dB	
TX Gain frequency response	$\Delta G_{TXF}$	f = 300 ~ 3400Hz	—	±0.2	—	dB	
DTMF input impedance	Z <sub>i</sub> (MF)	—	—	20.7	—	K $\Omega$	

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit	
DTMF Gain	$G_V(MF)$	R7=68K, DTMF mode	24	25.5	27	dB	
DTMF Gain response	$\Delta G_{MF}$	R7=68K, f=300~3400Hz	-	$\pm 0.2$	$\pm 0.5$	dB	
TX Gain adjustment	$\Delta G_{vad}(Tx)$	R7=68K→25K	(-)12	(-)8	(-)7	dB	
TX Dynamic output voltage	$T_o(Tx)$	THD=10%	IL=4mA	-	0.8	-	Vrms
			IL=15mA	1.7	2.3	-	
TX noise output voltage	$V_{no}(Tx)$	R7=68K Between pin6 & pin7=200 $\Omega$	-	(-)69	-	dBmp	
RX input impedance	$Z_i(Rx)$	-	-	21	-	K $\Omega$	
RX output impedance	$Z_o(Rx)$	-	-	4	-	K $\Omega$	
RX Gain	$G_V(Rx)$	R4=100K, RL=300 $\Omega$	29.5	31	32.5	dB	
RX Gain frequency response	$\Delta G_{TXF}$	f=300~3400Hz	-	$\pm 0.2$	-	dB	
RX Dynamic output voltage	$V_o(Rx)$	THD=2% R4=100K	RL=150 $\Omega$	0.22	0.33	-	V
			RL=450 $\Omega$	0.3	0.48	-	
RX noise output voltage	$V_{no}(Rx)$	RL=300 $\Omega$ , R4=100K IL=15mA, Rxi=open	-	50	-	$\mu$ V	
RX Gain adjustment	$\Delta G_{v.adj}(Rx)$	R4=100K→28K	(-)13	(-)11	(-)9	dB	
Mute input voltage	$V_{IH}(Mute)$	-	1.5	-	$V_{CC}$	V	
	$V_{IL}(Mute)$	-	-	-	0.3		
Mute input current	$I_{Mute}$	-	-	8	15	$\mu$ A	
Mute attenuation	$M_{att}(TX)$	MIC+ or MIC- to VL (CCIT weighted)	-	70	-	dB	
DTMF Gain	$G_V(DTMF)$	voltage gain from DTMF to Rxo	-	70	-	dB	
TX Gain variation range by AGC	$\Delta G_{TXA}$	R6=110K IL=15mA~70mA	-	(-)5.8	-	dB	
RX Gain variation range by AGC	$\Delta G_{RXA}$	R6=110K IL=15mA~70mA	-	(-)5.5	-	dB	
Highest line current for maximum gain	$I_{HL}(AGC)$	R6=110K	17	23	26	mA	
Lowest line current for minimum gain	$I_{LL}(AGC)$	R6=110K	54	61	73	mA	

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TEST CIRCUIT



APPLICATION CIRCUIT

