Speech network for telephones

BA8216

The BA8216 is a speech network IC which possesses the basic functions required for handset communications. In addition to amplifying signals from a transmitter and sending them to a telephone line, it also amplifies only reception signals from a telephone line and drives the receiver.

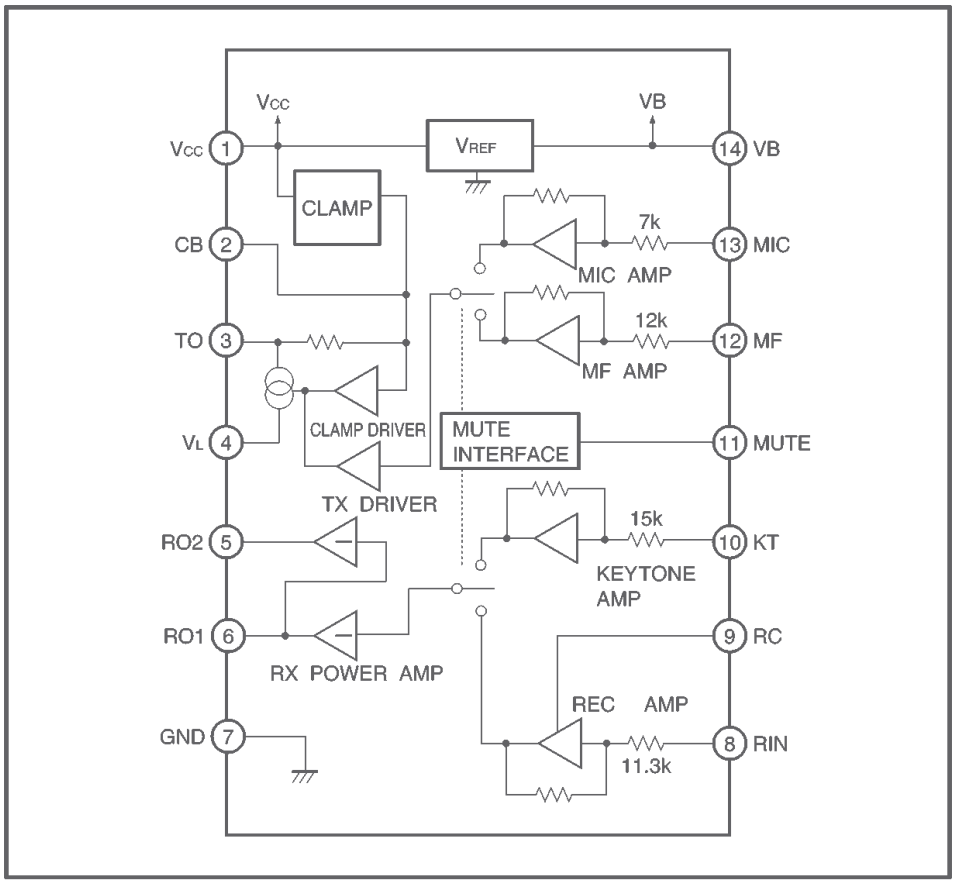
●Applications

Telephones and telephone equipment

●Features

1) Basic speech network functions built in.
   - Handset transmit and receive circuits
   - DTMF transmit circuit
   - Key tone input circuit
   - Mute control and side tone masking circuits
2) Can be used with 1.3kΩ loop circuit resistance and 100Ω telephone resistance.
3) A BTL circuit is used for reception output, providing a wide dynamic range which enables use of a ceramic receiver.
4) Few external components required.
5) 14-pin DIP package.

●Block diagram



●Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Limits	Unit
Applied voltage	V_L	18	V
Current dissipation	I_L	135	mA
Power dissipation	Pd	900 *	mW
Operating temperature	Topr	−25～+75	℃
Storage temperature	Tstg	−55～+125	℃

* Reduced by 9 mW for each increase in Ta of 1℃ over 25℃.

●Electrical characteristics (unless otherwise noted, Ta = 25°C, S_1 = 1, S_2 =1, f = 1kHz, BPF = 400Hz to 30kHz)

Parameter			Symbol	Min.	Typ.	Max.	Unit	I_L (mA)	Conditions	Measurement circuit
Line voltage (20)			V_{L20}	3.8	4.15	4.5	V	20		Fig.11
Line voltage (120)			V_{L120}	4.1	4.6	5.5	V	120		
Input high level voltage			V_{IH}	0.8	—	—	V	40	S_2=2	
Input low level voltage			V_{IL}	—	—	0.5	V	40	S_2=2	
Input high level current			I_{IH}	100	200	300	μA	40	S_2=2, V_{IH}=4V	
Electromagnetic	Receive	Gain	G_{RD}	−13.8	−10.8	−7.8	dB	40	V_T=20dBV	Fig.12
		Maximum output	V_{RD}	−19	−15	—	dBV	20	THD=5%	
		Input impedance	Z_{RIN}	8.3	11.3	14.3	kΩ	40		
	KT	Gain	G_{KTD}	10.5	13.5	16.5	dB	40	S_2=3, V_{KT}=−40dBV	Fig.12
		Maximum output	V_{KTD}	−19	−15	—	dBV	20	S_2=3, THD=5%	
		Input impedance	Z_{KT}	11	15	19	kΩ	40		
Piezoelectric	Receive	Gain	G_{RP}	0.9	3.9	6.9	dB	40	S_1=2, V_T=−20dBV	Fig.12
		Maximum output	V_{RP}	1	5	—	dBV	20	S_1=2, THD=5%	
		Input impedance	Z_{RIN}	8.3	11.3	14.3	kΩ	40		
	KT	Gain	G_{KTP}	25.1	28.1	31.1	dB	40	S_1=2, S_2=3 V_{KT}=−40dBV	Fig.12
		Maximum output	V_{KTP}	1	5	—	dBV	20	S_1=2, S_2=3 THD=5%	
		Input impedance	Z_{KT}	11	15	19	kΩ	40		
Transmit	MIC	Gain	G_{MIC}	19.6	22.6	25.6	dB	40	V_M=−40dBV	Fig.13
		Maximum output	V_{MIC}	0	4	—	dBV	20	THD=5%	
		Input impedance	Z_{MIC}	5	7	9	kΩ	40		
	MF	Gain	G_{MF}	21.1	24.1	27.1	dB	40	S_2=3, V_D=−40dBV	Fig.13
		Maximum output	V_{MF}	0	4	—	dBV	15	S_2=3, THD=5%	
		Input impedance	Z_{MF}	9	12	15	kΩ	40		
Mute ratio *			MR_{RD}	30	35	—	dB	40	V_T=−20dBV S_2=1→3	Fig.12
			MR_{RP}	30	35	—	dB	40	V_T=−20dBV S_2=1→3, S_1=2	
			MR_{MIC}	60	67	—	dB	40	V_M=−40dBV S_2=1→3	Fig.13
			MR_{MF}	60	67	—	dB	40	V_D=−40dBV S_2=3→1	
Attenuation during branch			ΔG_{RD}	—	−6	—	dB	—	See Fig. 14	Fig.14
			ΔG_{RP}	—	−5	—	dB	—	See Fig. 14	
			ΔG_{MIC}	—	−15	—	dB	—	See Fig. 14	
Noise level			N_{RD}	—	−75	—	dBV	120	V_T=0	Fig.12
			N_{RP}	—	−73	—	dBV	120	S_1=2, V_T=0	
			N_{MIC}	—	−74	—	dBV	120	V_M=0	Fig.13
			N_{MF}	—	−71	—	dBV	120	S_2=3, V_D=0	

* When using 1 kHz bandpass filter

●Electrical characteristic curves

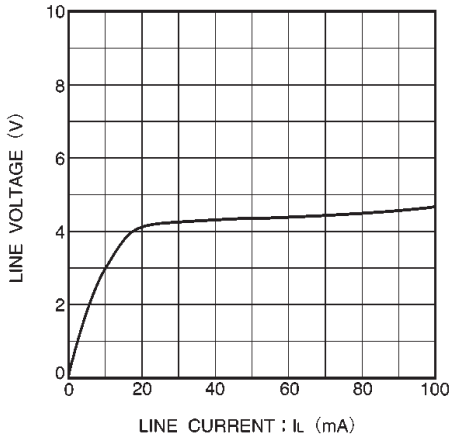


Fig. 1 Line voltage vs. line current

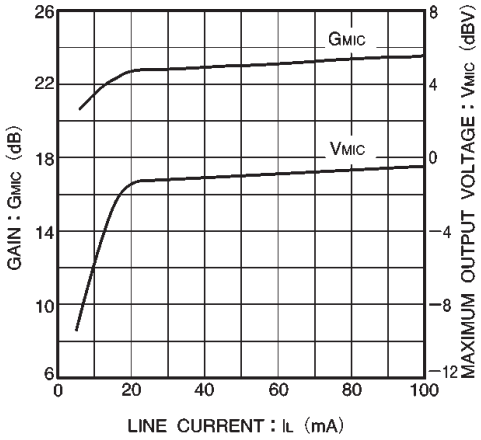


Fig. 2 MIC Line current

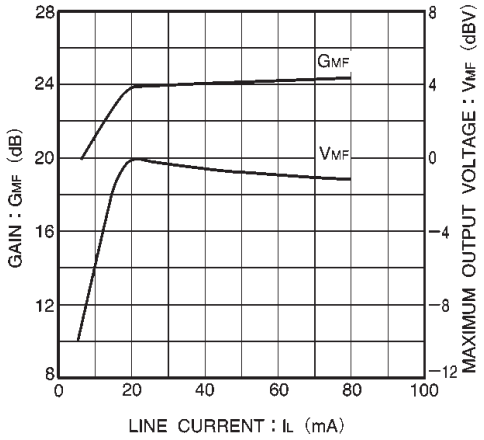


Fig. 3 MF Line current

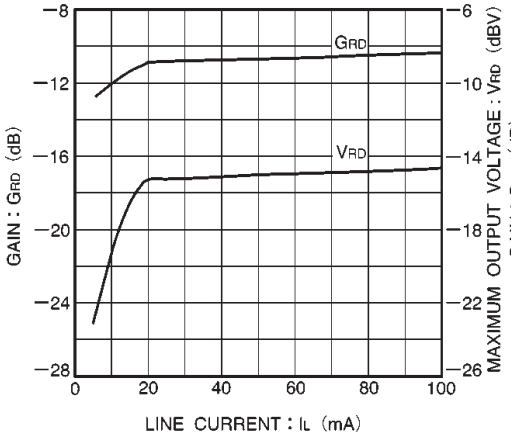


Fig. 4 Electromagnetic receiving Line current

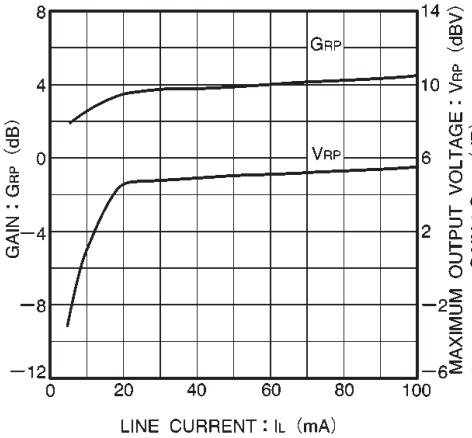


Fig. 5 Piezoelectric receiving Line current

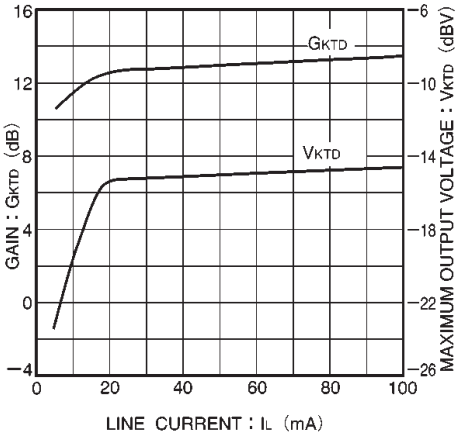


Fig. 6 Electromagnetic KT Line current

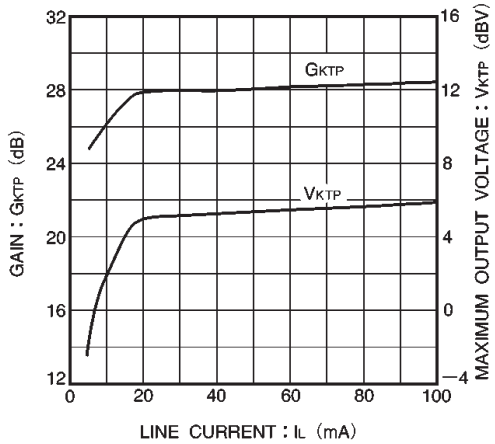


Fig. 7 Piezoelectric KT Line current

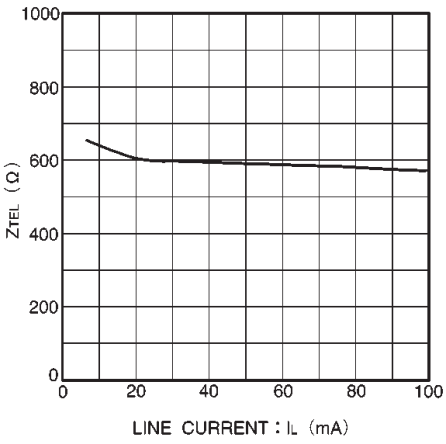


Fig. 8 AC impedance Line current

●Measurement circuits

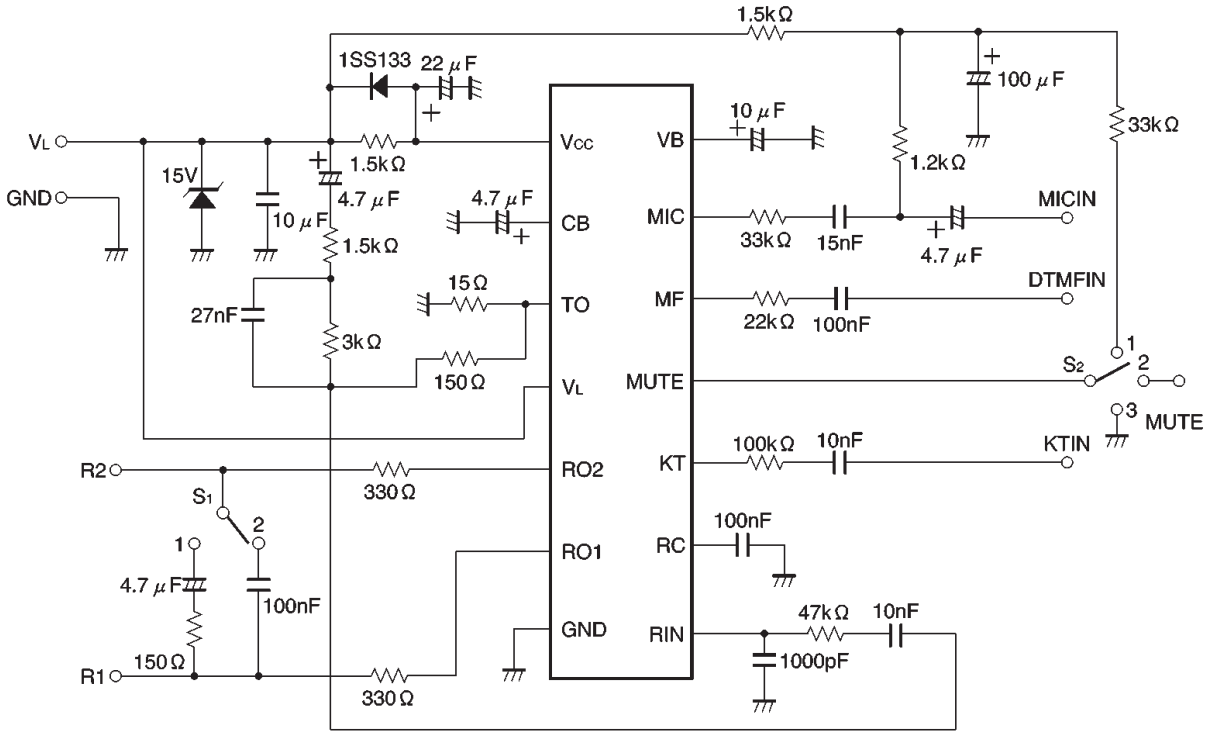


Fig. 9 Basic measurement circuit

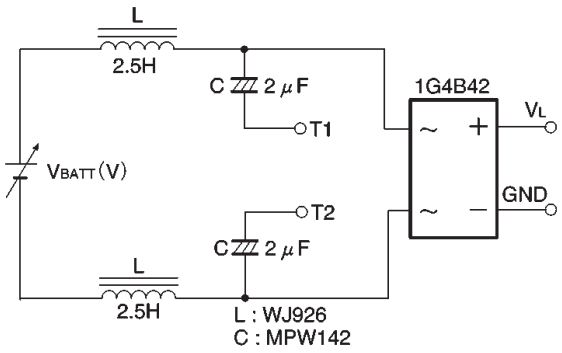


Fig. 10 Trunk circuit

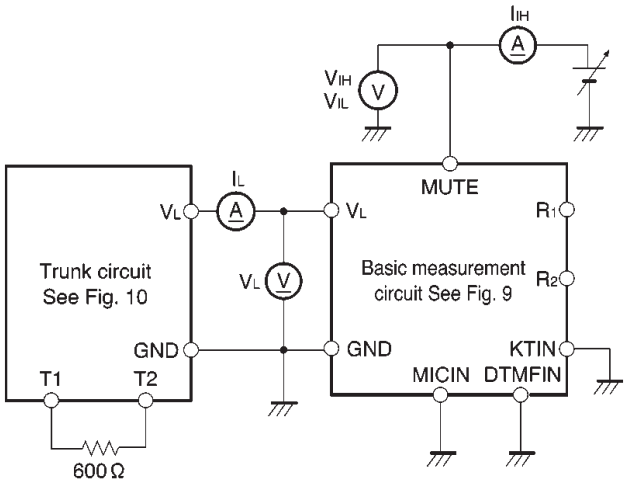


Fig. 11 DC characteristics measurement circuit

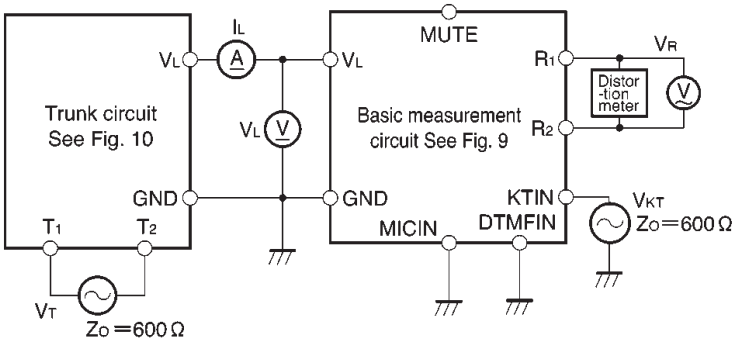


Fig. 12 Reception system measurement circuit

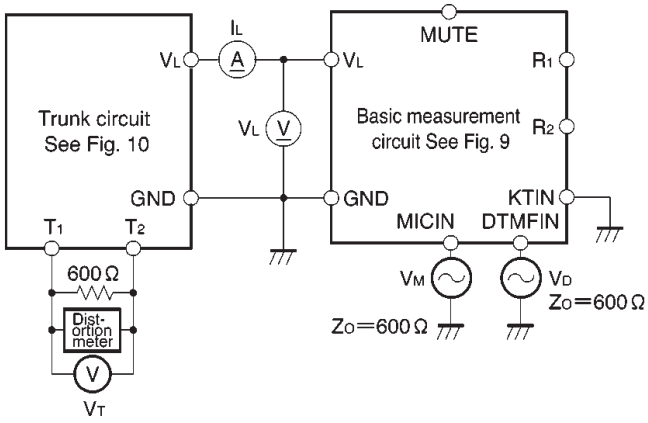


Fig. 13 Transmission system measurement circuit

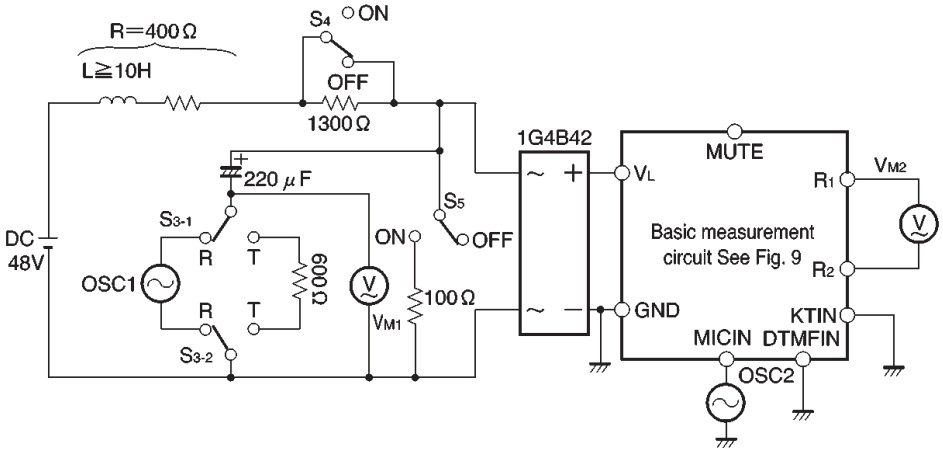


Fig. 14 Branch measurement circuit

●Measurement method (MUTE = HIGH)

(1) Reception

1) Set S_3 to R and S_4 and S_5 to OFF, and input a 1kHz signal from OSC1. Adjust to -10dBV if V_{M2} is piezoelectric, and to -30dBV if V_{M2} is electromagnetic.

2) With S_4 OFF and S_5 ON, record the output level of V_{M2} and note this value as V_{M2} (2).

3) With S_4 ON and S_5 ON, measure the output level of V_{M2}, and note this value as V_{M2} ③.

$\Delta G_R = 20\log (V_{M2}③ / V_{M2}②)$

(2) Transmission

1) Set S_3 = T and S_4 and S_5 to OFF, input a 1kHz signal from OSC2, and adjust so that V_{M1} is –10dBV.

2) With S_4 OFF and S_5 ON, record the output level of V_{M1} and note this value as V_{M1} ②.

3) With S_4 ON and S_5 ON, measure the output level of V_{M1}, and note this value as V_{M1} ③.

$\Delta GMIC = 20\log (V_{M1}③ / V_{M1}②)$

S_3: Send and receive switch (reception side)

S_4: ON / OFF switch for line resistance (1300Ω) (OFF)

S_5: ON / OFF switch for parallel resistance (100Ω) (OFF)

●Circuit opperation

The BA8216 carries out the following basic operations.

(1) Handset talk and receive

The BA8216 receives a voice signal from a telephone line and outputs it to a handset speaker. It also takes a voice signal from the handset microphone and outputs it to the telephone line.

(2) DTMF send and key tone input

The BA8216 has a mute switch which can be switched between a “handset send and receive” mode and a “DTMF send and key tone input” mode by an external logic signal. In the “DTMF send and key tone input” mode, it transmits DTMF signals from the dial pad to the telephone line, and key tones to the handset receiver.

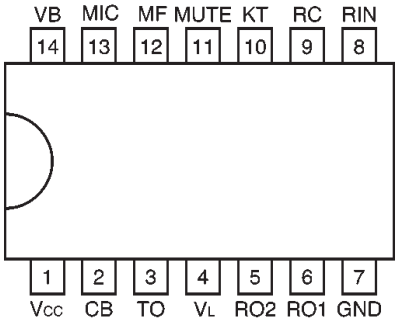
●Operation notes

The maximum power dissipation for the BA8216 is 900mW. Since the maximum power dissipation varies with temperature, the product of the applied voltage V_L and the total current drawn by the IC, after factoring in the temperature, should not exceed the maximum dissipation.

●Pin descriptions

Pin No.	Symbol	Name	Function
1	V_{CC}	Internal power supply pin	Internal power supply pin. Power is supplied from V_L through resistor R_{101}, and is smoothed by capacitor C_{101}.
2	CB	Bypass capacitor connector pin	This is used to connect an AC bypass capacitor to form a DC feedback loop for stabilizing the DC potential of the V_L pin.
3	TO	Transmit/power dissipation resistor connector pin	This is connected between the power dissipation resistor R_{105} and the GND,to eliminate unnecessary power consumption. At the same time, R_{105} determines the gain of the final output stage of the transmission driver. R_{104} and R_{105} form two legs of the side tone suppression bridge, which is also connected to this pin.
4	V_L	V_L pin	This is the power supply pin. The transmit signal is output to the telephone line through this pin. It is connected to the (+) side of the diode bridge.
5	RO_2	Receive output pin	When a piezoelectric receiver is used, connect a 330- protection resistor R_{191} to this pin. When a dynamic receiver is used, R_{191} may be 0Ω.
6	RO_1	Receive output pin	When a piezoelectric receiver is used, connect a 330- protection resistor R_{106} to this pin. C_{191} is shorted. When a dynamic receiver is used, a 4.7 μF DC blocking capacitor (C_{191}) is connected in series with the 680Ω resistor (R_{106}) to this pin.
7	GND	Ground pin	This pin has the lowest potential on the IC. It is connected to the (−) pin of the diode bridge.
8	RIN	Receive input pin	After passing through a side tone suppression circuit, the receive signal frothe telephone line is input to this pin.
9	RC	Receive amplifier bypass capacitor pin	This is connected to the AC bypass capacitor of the reception amplifier.
10	KT	Key tone input pin	When the MUTE pin is low, key tone signals input on this pin are transmitted to the handset speaker.
11	MUTE	Mute input pin	When this is high, hand-set transmission is normal. When this is low, DTMF signals applied at the MF input are output to the telephone line, and key tones applied to the KT pin are transmitted to the hand receiver.
12	MF	DTMF signal input pin	When the MUTE pin is low, DTMF signals input to this pin are output to the telephone line.
13	MIC	Microphone input pin	Used to input signals from the microphone.
14	VB	Bias pin	This is the IC internal bias pin. It is connected to the bypass capacitor C_{111}.

●Pin assignments



Mute control input logic

MUTE	MIC AMP	MF AMP	REC AMP	KT AMP
H	ON	OFF	ON	OFF
L	OFF	ON	OFF	ON

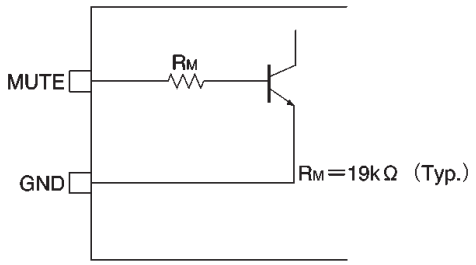


Fig. 15 Mute input equivalent circuit

●Application example

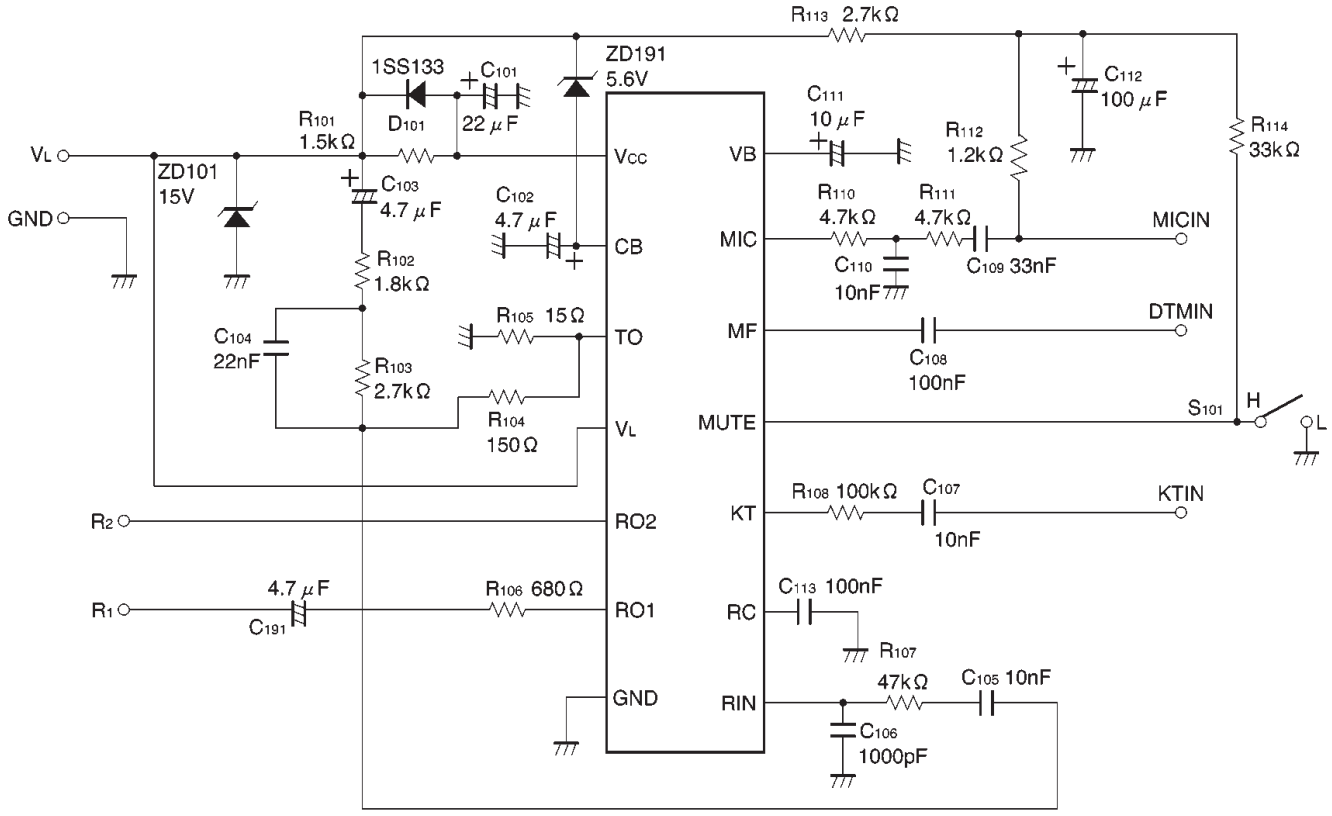


Fig. 16

●External dimensions (Units: mm)