AN5860, AN5860S

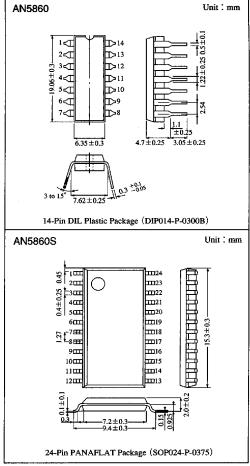
Analog Switch ICs for RGB Interface

Overview

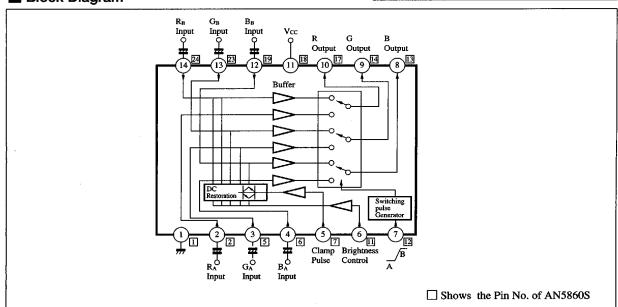
The AN5860 and the AN5860S are the integrated circuits designed for high-speed analog switch circuits for RGB signal processing.

■ Features

- Wide band Characteristics (>20MHz)
- \bullet High speed switching characteristics $(t_{dr~(typ.)}\,,~t_{df~(typ.)}$; 35ns)
- Brightness is DC-controlled



Block Diagram



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■ Pin Descriptions

Pin No.	Pin name	Pin No.	Pin name
1(1)	GND	8(13)	B output
2(2)	R _A input	9(14)	G output
3(5)	G _A input	10(17)	R output
4(6)	B _A input	11(18)	V _{CC}
5(7)	Clamp pulse input	12(19)	B _B input
6(11)	Brightness control	13(23)	G _B input
7(12)	Switching pulse input	14(24)	R _B input

Note: In case of AN5860S, Pin No. 3, 4, 8, 9, 10, 15, 16, 20, 20, 20, are NC

Absolute Maximum Ratings $(Ta=25^{\circ}C)$

	Parameter	Sy	mbol	R	ating	Unit
Voltage	Supply voltage	V _{cc}		13.8(13.0)		V
	Circuit current	V ₁₁₋₁ (V ₁₈₋₁)		0	13.8(13.0)	V
		V _{2,3,4,12,13,14-1} (V _{2,5,6,19,23,24-1})		0	$V_{11-1}(V_{18-1})$	V
		V ₅₋₁ (V ₇₋₁)		-1	6(V ₁₈₋₁)	V
		V ₆₋₁ (V ₁₁₋₁)		3(0)	9	v
		$V_{7-1}(V_{12-1})$		0	6(V ₁₈₋₁)	v
Circuit voltage		I _{8,9,10} (I _{13,14,17})		-10	2	mA
Power dissipation (Ta=70°C)		P _D	AN5860	560 490		
			AN5860S			mW
Temperature	Operating ambient temperature	Topr		-20 to +70		C
	Storage temperature	$T_{ m stg}$	AN5860	-55 to $+150$		°C
			AN5860S	-55 to $+125$		C

() shows the Pin No. of AN5860S

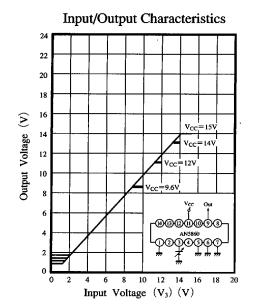
■ Electrical Characteristics $(Ta=25^{\circ}C)$

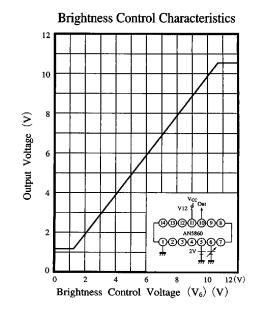
Parameter	Symbol	Condition	min	typ	max	Unit
Operating supply voltage range	$V_{\rm CC(opr.)}$		9.6	12.0	13.8	v
Total circuit current	I _{tot}	$V_{CC} = 12V, V_5$: input pulse $1V_{P-P}$	19	26.5	34	mA
DC voltage difference between outputs	ΔV_{RGB}	$V_{CC} = 12V, V_7 = 1V, 0V$		0	±100	mV
Switching output DC voltage difference	ΔV_{A-B}	$V_{CC} = 12V, V_7 = 1V, 0V$		0	±30	mV
Input/Output dynamic range for signals (upper)	D.R _{max} .	V _{CC} =12V			10.5	v
Input/Output dynamic range for signals (lower)	D.R _{min.}	V _{cc} =12V	1.7			V
Output terminal sink current	I _{SINC}	V _{CC} =12V, input voltage 6V			0.8	mA
Voltage amplification for signals	A_v	$f_{in}=1MHz, 1V_{P-P}$	0.9	1	1.1	times
Frequency characteristics for signals	f _{-3dB}	$e_{in} = 1V_{P-P}$	20			MHz
DC level difference of pedestal level for signals	⊿E _{TO}	Input pulse 1V _{P-P} , V ₆ : 7V		0	±100	mV
Switching crosstalk	CT _{A/B}	$f_{in} = 1$ MHz, 1 V _{P-P}			-40	dB
Signal rise time	t _r	$f_{in} = 1 MHz, 1 V_{P-P}$		20	40	ns
Signal fall time	t _f	$f_{in}=1MHz, 1V_{P-P}$		20	40	ns
Signal rise delay time	t _{dr}	$f_{in} = 1MHz, 1V_{P-P}$	_	10	30	ns
Signal fall delay time	t _{df}	$f_{in}=1MHz, 1V_{P-P}$		10	30	ns
Switching delections	t _{dr(A/B)}	Switching pulse 1V _{P-P}		35	60	ns
Switching delay time	t _{df(A/B)}	Switching pulse 1V _{P-P}	_	35	60	ns
Switching pulse standard input	V _{SWP}	V _{cc} =12V		1		V _{O-P}
Clamp pulse standard input	V_{CLP}	$V_{CC}=12V$		2		V _{O-P}

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