Monolithic Digital IC

LB1651

Dual Bidirectional Motor Driver

Package Dimensions

unit : mm

3037A-DIP20H

Overview

The LB1651 is a dual bidirectional motor driver that is designed to drive motors directly by TTL outputs. It provides the functions of bidirectional motor drive, brake that are determined by two inputs and the inhibit function that brings the output to a high impedance state.

Applications

- Multi DC motor driver
- · Bidirectional motor driver
- · Bipolar stepping motor driver

Features

- High output current (1 A/ch)
- Wide operating voltage range (4.5 to 36 V)
- Inhibit function
- Direct drive made possible by TTL, CMOS IC
- High noise margin

Specifications

Absolute Maximum Ratings at $Ta = 25^{\circ}C$

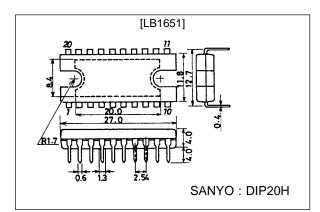
Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} 1		36	V
Logic supply voltage	V _{CC} 2		36	V
Input voltage	V _{IN}		7	V
Inhibit voltage	Vinh		7	V
Peak output current	IOUT	1 ms non-repetitive	2	A
Allowable power dissipation	Pd max		3	W
Operating temperature	Topr		-20 to +80	°C
Storage temperature	Tstg		-40 to +150	°C

Allowable Operating Conditions at Ta = 25°C

Parameter	Symbol	Conditions	Rating	Unit
Supply voltage	V _{CC} 1		4.5 to 36	V
Logic supply voltage	V _{CC} 2		4.5 to 36	V

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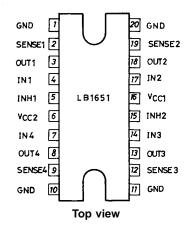
43096HA(II)/D2094TH(KOTO)/22894TS/1300YT/8207TA, TS No.2637-1/4



Parameter	Symbol	Conditions	min	typ	max	Unit
Supply current (Per channel)		$V_{IN} = L, I_O = 0, Vinh = H$			1.5	mA
	I _{CC} 1	$V_{IN} = H, I_O = 0, Vinh = H$			6	mA
		Vinh = L			1	mA
Logic supply current		$V_{IN} = L, I_O = 0, Vinh = H$		44	60	mA
	I _{CC} 2	$V_{IN} = H, I_O = 0, Vinh = H$			22	mA
		Vinh = L			24	mA
Low-level input voltage	VIL		-0.3		+1.5	V
High-level Input Voltage	V	$V_{CC}^{2} \leq 7 V$	2.3		V _{CC} 2	V
	VIH	V _{CC} 2 > 7 V	2.3		7	V
Low-level input current	IL	V _{IN} = L			±10	μA
High-level input current	Чн	V _{IN} = H – 0.3 V		30	100	μA
Low-level inhibit voltage	VinhL		-0.3		+1.5	V
High-level inhibit voltage	VinhH	$V_{CC}^{2} \leq 7 V$	2.3		V _{CC} 2	V
		V _{CC} 2 > 7 V	2.3		7	V
Low-level inhibit current	linhL		-100	-30		μA
High-level inhibit current	linhH				±10	μA
Staturation voltage	V _{CE} (sat)H	$I_{O} = -1 A$		1.4	1.8	V
	V _{CE} (sat)L	I _O = 1 A		1.2	1.8	V
Sensing voltage	V _{SENS}				2	V

Electrical Characteristics at Ta = 25° C, V_{CC}1 = 24 V, V_{CC}2 = 5 V

Pin Assignment

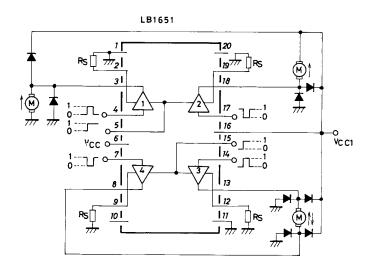


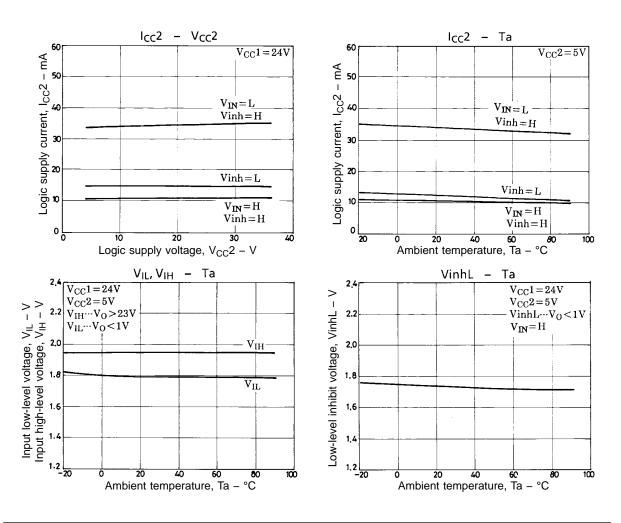
Truth Table

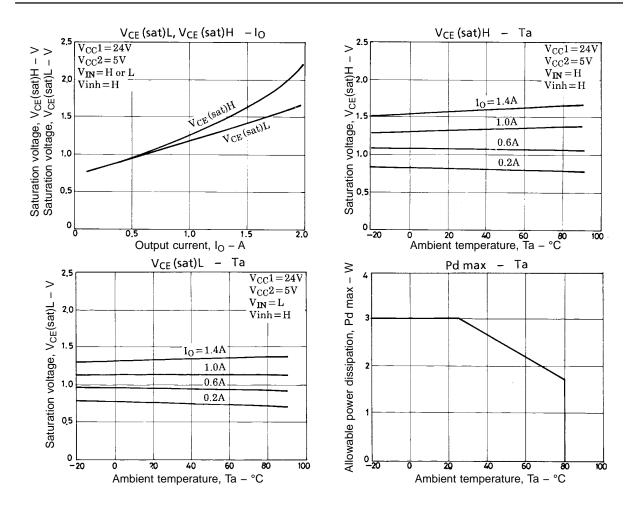
V _{IN} (per CH)	Vinh	Vo
Н	Н	Н
L	Н	L
Н	L	Open*
L	L	Open*

*: High impedance

Sample Application Circuit







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