



# **Level Meter**

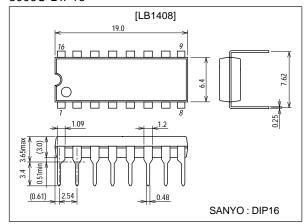
## **Features**

- An input amplifier is built in.
- Minimum number of external parts required.
- Low current dissipation because of series connection of LED's.

# **Package Dimensions**

unit:mm

#### 3006C-DIP16



# **Specifications**

### **Absolute Maximum Ratings** at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max	(Pin 3)	-0.3 to +18.0	V
Maximum input voltage	V <sub>IN</sub> max	(Pin 2)	−0.3 to V <sub>CC</sub>	V
D pin output current	I <sub>D</sub> max	Output transistor ON	0 to +30	mA
D pin output voltage	V <sub>D</sub> max		−0.3 to V <sub>CC</sub>	V
Reference flow-out current	Iref max	(Pin 4)	-0.3 to 0	mA
Allowable power dissipation	Pd max		1.2	W
Operating temperature	Topr		-30 to +80	°C
Storage temperature	Tstg		-40 to +125	°C

### Allowable Operating Ranges at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	VCC		6.7 to 16.0	V

#### **Electrical Characteristics** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Onit
Current drain	Icc	Quiescent, pin 3 3.3kΩ across I <sub>LED1</sub> and Vref		4	8	mA
Input bias current	I <sub>IN</sub>	Pin 2	-10		0	μA
Reference voltage	Vref	Pin 4	4.40	4.85	5.30	V

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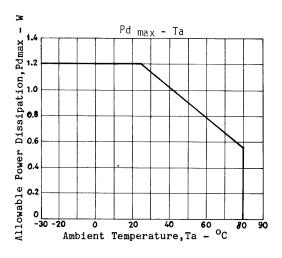
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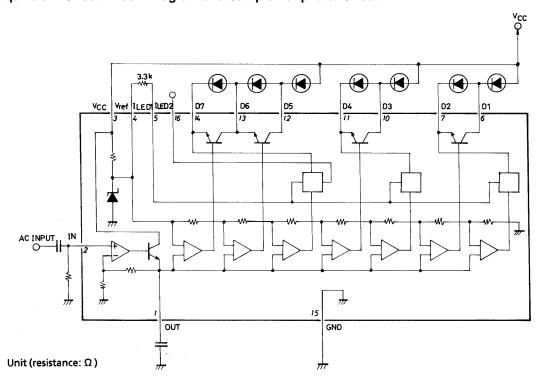
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Parameter	Combal	Conditions	Ratings			Unit
	Symbol		min	typ	max	Unit
D pin current 1	I <sub>D2, 4, 7</sub>	3.3kΩ across I <sub>LED1</sub> and Vref I <sub>LED2</sub> =GND, pins 7, 11, 14	12	16	19	mA
Output saturation voltage	VsatD 1, 3, 5, 6	I <sub>LED2</sub> =GND, pins 6, 10, 12, 13		1.0	1.3	V
D pin current 2	I <sub>D2, 4, 7</sub>	I <sub>LED2</sub> =GND, V <sub>CC</sub> =6.7V, V <sub>D</sub> 1, 3, 6=0.9V, pins 7, 11, 14	12		19	mA
Out pin impedance	R <sub>OUT</sub>	Pin 1	8	12	16	kΩ
Input sensitivity	V <sub>IN5</sub>	Input voltage at which LED of D5 is lighted	119	132	145	mV
Comparator level						
D1	V <sub>T1</sub>	Input voltage at which LED of D5 is lighted is taken as 0dB.	-26	-20	-14	dB
D2	V <sub>T2</sub>	Input voltage at which LED of D5 is lighted is taken as 0dB.	-12	-10	-8	dB
D3	V <sub>T3</sub>	Input voltage at which LED of D5 is lighted is taken as 0dB.	-7	-6	-5	dB
D4	V <sub>T4</sub>	Input voltage at which LED of D5 is lighted is taken as 0dB.	-3.5	-3.0	-2.5	dB
D5	V <sub>T5</sub>	Input voltage at which LED of D5 is lighted is taken as 0dB.	0	0	0	dB
D6	V <sub>T6</sub>	Input voltage at which LED of D5 is lighted is taken as 0dB.	2.5	3.0	3.5	dB
D7	V <sub>T7</sub>	Input voltage at which LED of D5 is lighted is taken as 0dB.	5	6	7	dB
Output leakage current	I <sub>DL1, 3, 5</sub>	V <sub>IN</sub> =0V, pins 6, 10, 12	0		10	μΑ
D pin current 3	I <sub>D7</sub>	3.3kΩ across I <sub>LED1</sub> and Vref I <sub>LED2</sub> =open, pin14	4.5	6.0	8.0	mA
D pin current 4	I <sub>D7</sub>	I <sub>LED2</sub> =open, pin14, V <sub>CC</sub> =6.7V, V <sub>D6</sub> =0.7V, pin14	4.5		8.0	mA

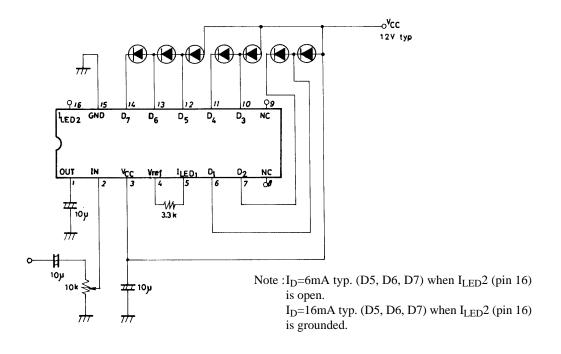


## **Equivalent Circuit Block Diagram and Sample Peripheral Circuit**



## **Sample Application Circuit**

Unit (resistance: Ω, capacitance: F)



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