

#### Features

- . Capable of performing remote controls of 13 kinds due to 13 control outputs
- . Only 2 wires required between set and remote control box
- . On-chip one-shot multivibrator to reject chattering at the time of switch changeover (One-shot time constant is varied externally.)
- . Even if 2 or more switches are pushed simultaneously, first pushed switch's input only is effective because of internal memory.
- . Only one adjustment required
- . Capable of outputting with  $\overline{EN}$  (enable) pin at all times ( $\overline{EN}$ =0V. If one-shot time constant is not required, C pin=0V.)
- . Usable in indicator applications because of output capable of driving LED sufficiently

Absolute Maximum Ratings at Ta Maximum Supply Voltage Output Current 01 to 013	a=25 <sup>0</sup> C V <sub>CC</sub> I <sub>OLO1</sub> to i	Pin No. 21 013 4 to 9 11 to 14 16 to 18	Output ON	-0.3 to +18 30	unit V mA
Output Current C Allowable Power Dissipation Operating Temperature Storage Temperature	I <sub>OLC</sub> Pd max Topr Tstg	19	Output ON Ta=75 <sup>0</sup> C	10 250 -30 to +75 -40 to +125	mA mW °C °C

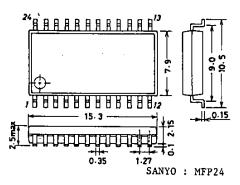
Allowable Operating Conditions at Ta=25°C Pin No. Supply Voltage Range V<sub>CC</sub> 21 Sample Application Circuit 1 8.5 to 16 V (8.0)\* Sample Application Circuit 2 8.0 to 16 V

\*:  $V_{CC}$ =8V applies in case where adjustment is made with semifixed resistor so that  $V_{RO}$ =7.6 is obtained at  $V_{CC}$ =9V in Sample Application Circuit 1.

### Pin Assignment

24	23	22	10	20	19	8	17	Ж	15	14	ß
IN	ĒN	NC	Vcc	Vref	С	ō <sub>13</sub>	ō <sub>12</sub>	ሻከ	NC	74 710	∣ وō
L B 1 4 7 5 M											
	~		π.	×.	π.	Χ.	<b>X</b> .	δ.		Χ.	7.
L FO		NU	ᅳ	-02	<u> 13</u>	-04	45	- 16	100	<u>7</u>	<u>08</u>
	2	3	4	5	6	Z	8	9	0	$\underline{m}$	12

Package Dimensions 3045B unit: mm



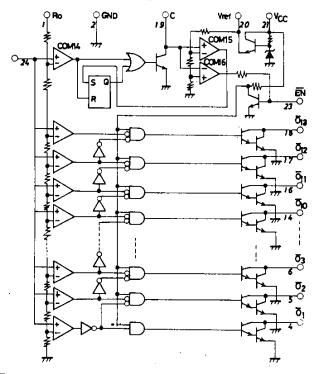
SANYO Electric Co., Ltd. Semiconductor Business Headquarters TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110 JAPAN

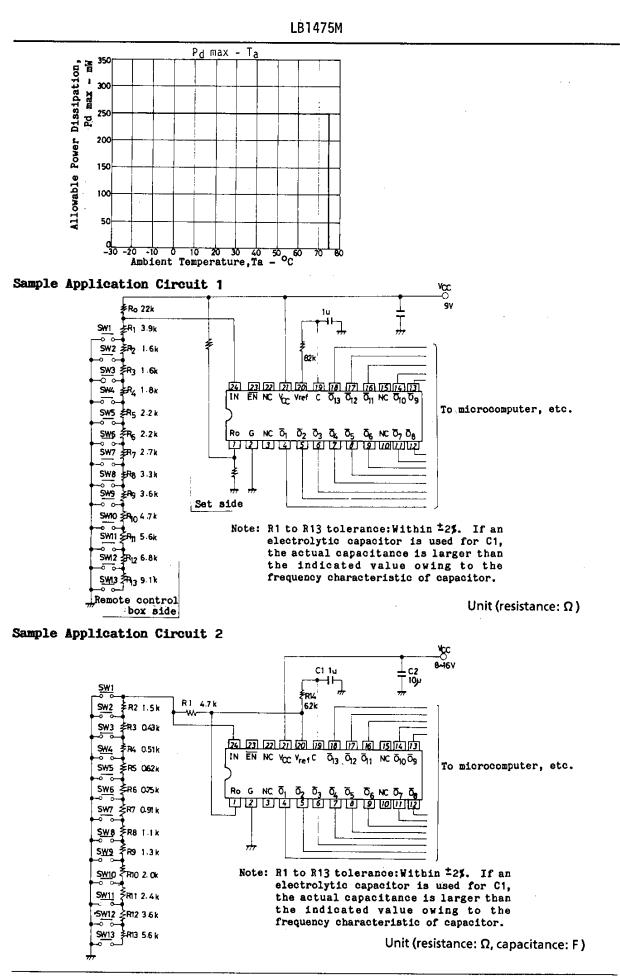
20295HK/7077TA,TS No.2061-1/4

l <b>ectrical Characteris</b> Input Bias Current	<b>VIOD</b> 40	IN N	,1a-2J	°C Pin		V <sub>IN</sub> =OV	min -1	typ	ша.х 0	unit µA
Output Saturation 01 Voltage	to 013	V <sub>sat01</sub>	to 01		to 9 5 14	Output ON		1.2	1.7	
Ħ		Ħ		ויינו		Output ON		0.8	1.2	V
Output Leakage 01 Current	to 013	I <sub>OFF01</sub>	to 01	3	π	U <sub>OLON</sub> =2mA Output OF	F O		10	Aىر
Comparator Level	01	V <sub>TO1</sub>			4	V <sub>RO</sub> =7.6V	1.515	1.6	1.685	v
11	02	V <sub>TO2</sub>			5	n n	1.915		2.085	
U	03	V <sub>T03</sub>			6	U	2.32	2.4	2.48	
13		V <sub>TO4</sub>			7	U	2.72	2.8	2.88	
17	05	V <sub>T05</sub>			8	n	3.125		3.275	
n	06	V <sub>TO6</sub>			9	11	3.525		3.675	
tt	07	VT07			11	11	3.93	4.0	4.07	
Ħ	08	$v_{TO8}^{107}$			12	U	4.33	4.4	4.47	
t	09	V <sub>T09</sub>			13	t	4.735		4.865	
tt	010	V <sub>T010</sub>		,	14	t	5 135		5.265	
tr	011	Veroat			16	t	5.54	5.6	5.66	v
8	012	VT012			17	U	5.94	6.0	6.06	v
17	013	VT012			18	π	6.345		6.455	v
Comparator Level Fall		Vmodu				V <sub>RO</sub> =7.6V	6.7	6.8	6.9	
Reference Voltage	-	V <sub>TC14</sub> Vref			20	RO-1101	5.6	6.3	7.0	
One-shot Multivibrato Threshold Voltage	or	V <sub>TC1</sub>				Vref=7.2V	-	0.5	1.055	v
		V <sub>TC2</sub>				m	3.97		5.03	v
Output Leakage Currer	nt C	I offC			19	Vc=3V	-5		5	μÂ
Output Saturation Vol	tage C	VsatC				I <sub>OLC</sub> =100µ/	•		30	
EN Pin Threshold Volt	age	V <sub>TEN</sub>			23	$V_{IN}^{ULC} = 9V$ $V_{RO}^{ULC} = 7.6V$	0.4	0.6	0.9	V
EN Pin Flow-out Curre	ent	I <sub>OHEN</sub>			23	. KO_1.001	40	80	160	μA
Internal Resistance	-	R <sub>O</sub>			1		6.5	9.5	12.5	kohn
Current Dissipation		I <sub>CC</sub>			21		0.9	5	9	mA

## LB1475M

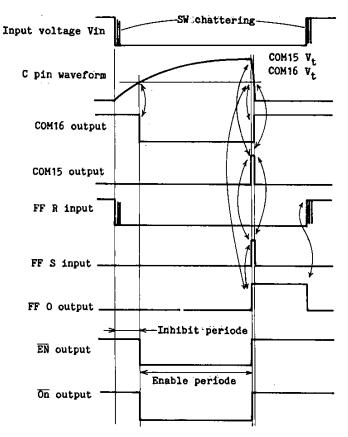
# Equivalent Circuit Block Diagram





Downloaded from Elcodis.com electronic components distributor

### LB1475M Timing Chart



Note: Chattering and switch input not covered by enable period do not appear at output On. In other words, chattering and switch input covered by enable period appear at output On.

