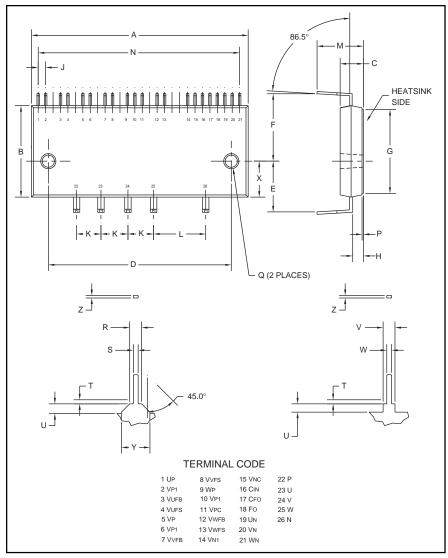


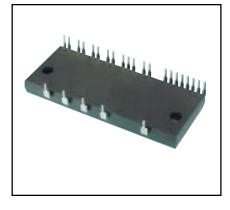
IntellimodTM Module Dual-In-Line Intelligent Power Module 15 Amperes/600 Volts



Outline Drawing and Circuit Diagram

| Dimensions | Inches | Millimeters |
|------------|----------------|---------------|
| Α | 3.07±0.02 | 79.0±0.5 |
| В | 1.22±0.02 | 31.0±0.5 |
| С | 0.32±0.02 | 8.0±0.5 |
| D | 2.64±0.01 | 67.0±0.3 |
| E | 0.53±0.01 Dia. | 13.4±0.2 Dia. |
| F | 0.84±0.02 | 21.4±0.5 |
| G | 1.10±0.02 | 28.0±0.5 |
| Н | 0.15±0.01 | 3.8±0.2 |
| J | 0.11±0.01 | 2.8±0.3 |
| K | 0.39±0.01 | 10.0±0.3 |
| L | 0.79±0.01 | 20.0±0.3 |
| М | 0.50±0.04 | 12.8±1.0 |

| Dimensions | Inches | Millimeters |
|------------|--------------------|--------------|
| N | 2.98 | 75.6 |
| Р | 0.02±0.01 | 0.5±0.2 |
| Q | 0.18±0.01 Dia. | 4.5±0.2 Dia. |
| R | 0.08±0.02 | 1.9±0.05 |
| S | 0.04±0.01 | 1.0±0.2 |
| Т | 0.02 Max. | 0.5 Max. |
| U | 0.02±0.02 | 0.6±0.5 |
| V | 0.07 Max. 1.75 Max | |
| W | 0.03±0.01 0.8±0. | |
| X | 0.45±0.02 11.5±0 | |
| Υ | 0.13 Max. 3.25 Ma | |
| Z | 0.03 | 0.7 |



Description:

DIP and mini-DIP IPMs are intelligent power modules that integrate power devices, drivers, and protection circuitry in an ultra compact dual-in-line transfer-mold package for use in driving small three phase motors. Use of 4th generation IGBTs, DIP packaging, and application specific HVICs allow the designer to reduce inverter size and overall design time.

Features:

- ☐ Compact Packages
- ☐ Integrated HVICs
- ☐ Direct Connection to CPU
- ☐ Optimized for 5kHz Operation

Applications:

- ☐ Washing Machines
- Refrigerators
- ☐ Air Conditioners
- ☐ Small Servo Motors☐ Small Motor Control

Ordering Information:

PS21244-E is a 600V, 15 Ampere DIP Intelligent Power Module.



PS21244-E Intellimod™ Module Dual-In-Line Intelligent Power Module 15 Amperes/600 Volts

Absolute Maximum Ratings, $T_j = 25^{\circ}\text{C}$ unless otherwise specified

| Characteristics | Symbol | PS21244-E | Units |
|--|------------------------|------------|-------|
| Power Device Junction Temperature* | Тј | -20 to 150 | °C |
| Storage Temperature | T _{stg} | -40 to 125 | °C |
| Case Operating Temperature (See T _C Measure Point Illustration) | T _C | -20 to 100 | °C |
| Mounting Torque, M4 Mounting Screws | _ | 13 | in-lb |
| Module Weight (Typical) | _ | 54 | Grams |
| Heatsink Flatness | _ | -50 to 100 | μm |
| Self-protection Supply Voltage Limit (Short Circuit Protection Capability)** | V _{CC(prot.)} | 400 | Volts |
| Isolation Voltage, AC 1 minute, 60Hz Sinusoidal, Connection Pins to Heatsink Plate | V _{ISO} | 1500 | Volts |

^{*}The maximum junction temperature rating of the power chips integrated within the DIP-IPM is 150° C (@T_C $\leq 100^{\circ}$ C). However, to ensure safe operation of the DIP-IPM, the average junction temperature should be limited to T_{|[avg]} $\leq 125^{\circ}$ C (@T_C $\leq 100^{\circ}$ C).

**V_D = V_{DB} = $13.5 \sim 16.5$ V, Inverter Part, T_j = 125° C, Non-repetitive, Less than 2 μ s

IGBT Inverter Sector

| Collector-Emitter Voltage | V _{CES} | 600 | Volts |
|--|------------------------|-----|---------|
| Collector Current, \pm (T _C = 25°C) | IC | 15 | Amperes |
| Peak Collector Current, \pm (T _C = 25°C, Instantaneous Value (Pulse)) | I _{CP} | 30 | Amperes |
| Supply Voltage (Applied between P - N) | V _{CC} | 450 | Volts |
| Supply Voltage, Surge (Applied between P - N) | V _{CC(surge)} | 500 | Volts |
| Collector Dissipation (T _C = 25°C, per 1 Chip) | P _C | 43 | Watts |

Control Sector

| Supply Voltage (Applied between V _{P1} -V _{PC} , V _{N1} -V _{NC}) | V _D | 20 | Volts |
|--|-----------------|----------------------------|-------|
| Supply Voltage (Applied between V _{UFB} -V _{UFS} , V _{VFB} -V _{VFS} , V _{WFB} -V _{WFS}) | V_{DB} | 20 | Volts |
| Input Voltage (Applied between Up, Vp, Wp-VpC, UN, VN, WN-VNC) | VCIN | -0.5 ~ 5.5 | Volts |
| Fault Output Supply Voltage (Applied between F _O -V _{NC}) | V_{FO} | -0.5 ~ V _D +0.5 | Volts |
| Fault Output Current (Sink Current at F _O Terminal) | I _{FO} | 15 | mA |
| Current Sensing Input Voltage (Applied between C _{IN} -V _{NC}) | V _{SC} | -0.5 ~ V _D +0.5 | Volts |

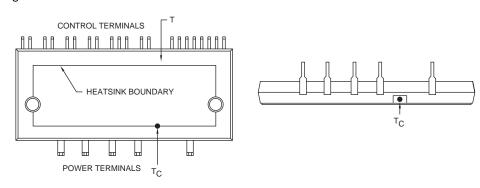


PS21244-E Intellimod™ Module Dual-In-Line Intelligent Power Module 15 Amperes/600 Volts

Electrical and Mechanical Characteristics, T_i = 25°C unless otherwise specified

| Characteristics | Sym | bol Test Conditions | Min. | Тур. | Max. | Units |
|--------------------------------------|----------------------|---|------|------|------|-------|
| IGBT Inverter Sector | | | | | | |
| Collector Cutoff Current | I _{CES} | $V_{CE} = V_{CES}, T_j = 25^{\circ}C$ | _ | _ | 1.0 | mA |
| | | $V_{CE} = V_{CES}$, $T_j = 125$ °C | _ | _ | 10 | mA |
| Diode Forward Voltage | V _{EC} | $T_j = 25^{\circ}C$, $-I_C = 15A$, $V_{CIN} = 5V$ | _ | 2.5 | 3.4 | Volts |
| Collector-Emitter Saturation Voltage | V _{CE(sat)} | $I_C = 15A, T_j = 25^{\circ}C, V_D = V_{DB} = 15V, V_{CIN} = 0V$ | _ | 1.55 | 2.15 | Volts |
| | | $I_C = 15A, T_j = 125^{\circ}C, V_D = V_{DB} = 15V, V_{CIN} = 0V$ | _ | 1.65 | 2.25 | Volts |
| Inductive Load Switching Times | t _{on} | $V_{CC} = 300V, V_D = 15V,$ | 0.1 | 0.7 | 1.2 | μS |
| | t _{rr} | I _C = 15A, | _ | 0.1 | _ | μS |
| | t _{C(on)} | T _j = 125°C, | _ | 0.4 | 0.8 | μS |
| | t _{off} | Inductive Load (Upper-Lower Arm), | _ | 1.5 | 2.6 | μS |
| | t _{C(off)} | V _{CIN} = 5V(off), 0V(on) | _ | 0.9 | 1.8 | μS |

T_C Measure Point





PS21244-E Intellimod™ Module Dual-In-Line Intelligent Power Module 15 Amperes/600 Volts

Electrical and Mechanical Characteristics, $T_j = 25$ °C unless otherwise specified

| Characteristics | Symb | ol Test Conditions | Min. | Тур. | Max. | Units |
|--------------------------------|----------------------|---|------------|------|------|-------|
| Control Sector | | | | | | |
| Supply Voltage | V _D | Applied between V _{P1} -V _{PC} , V _{N1} -V _{NC} | 13.5 | 15.0 | 16.5 | Volts |
| | V _{DB} | Applied between V _{UFB} -V _{UFS} , | 13.5 | 15.0 | 16.5 | Volts |
| | | V_{VFB} - V_{VFS} , V_{WFB} - V_{WFS} | | | | |
| Circuit Current | I _D | $V_D = 15V$, $V_{CIN} = 5V$, $V_{DB} = 15V$, | _ | _ | 8.50 | mA |
| | | Total of V _{P1} -V _{PC} , V _{N1} -V _{NC} | | | | |
| | | $V_D = 15V$, $V_{CIN} = 5V$, $V_{DB} = 15V$, | _ | _ | 1.00 | mA |
| | | $V_{UFB}\text{-}V_{UFS}, V_{VFB}\text{-}V_{VFS}, V_{WFB}\text{-}V_{WFS}$ | | | | |
| Fault Output Voltage | V _{FOH} | V_{SC} = 0V, F_O Circuit: 10k Ω to 5V Pull-up | 4.9 | _ | _ | Volts |
| | V _{FOL} | V_{SC} = 1V, F_O Circuit: 10k Ω to 5V Pull-up | _ | 0.8 | 1.2 | Volts |
| | V _{FO(sat)} | V _{SC} = 1V, I _{FO} = 15mA | 0.8 | 1.2 | 1.8 | Volts |
| PWM Input Frequency | fpWM | $T_{C} \le 100^{\circ}C, T_{j} \le 125^{\circ}C$ | _ | 5 | _ | kHz |
| Allowable Dead Time | t _{DEAD} | Relates to Corresponding Input Signal for | 2.5 | _ | _ | μS |
| | В | Blocking Arm Shoot-through (-20°C \leq T _C \leq 100°C | () | | | |
| Short Circuit Trip Level* | V _{SC(ref)} | $T_j = 25^{\circ}C, V_D = 15V^*$ | 0.45 | 0.5 | 0.55 | Volts |
| Supply Circuit Under-voltage | UV _{DBt} | Trip Level, T _j ≤ 125°C | 10.0 | _ | 12.0 | Volts |
| | UV _{DBr} | Reset Level, T _j ≤ 125°C | 10.5 | _ | 12.5 | Volts |
| | UV _{Dt} | Trip Level, T _j ≤ 125°C | 10.3 | _ | 12.5 | Volts |
| | UV _{Dr} | Reset Level, T _j ≤ 125°C | 10.8 | _ | 13.0 | Volts |
| Fault Output Pulse Width** | t _{FO} | C _{FO} = 22nF | 1.0 | 1.8 | _ | mS |
| ON Threshold Voltage (H-side) | V _{th(on)} | Applied between Up, Vp, Wp-VpC | 0.8 | 1.4 | 2.0 | Volts |
| OFF Threshold Voltage (H-side) | V _{th(off)} | | 2.5 | 3.0 | 4.0 | Volts |
| ON Threshold Voltage (L-side) | V _{th(on)} | Applied between U _N , V _N , W _N -V _{NC} | 0.8 | 1.4 | 2.0 | Volts |
| OFF Threshold Voltage (L-side) | V _{th(off)} | | 2.5 | 3.0 | 4.0 | Volts |

^{*} Short Circuit protection is functioning only at the low-arms. Please select the value of the external shunt resistor such that the SC trip level is less than 25.5A.

^{**}Fault signal is asserted when the low-arm short circuit or control supply under-voltage protective functions operate. The fault output pulse-width tFO depends on the capacitance value of CFO according to the following approximate equation: CFO = (12.2 x 10⁻⁶) x tFO {F}.



PS21244-E Intellimod™ Module Dual-In-Line Intelligent Power Module 15 Amperes/600 Volts

Thermal Characteristics

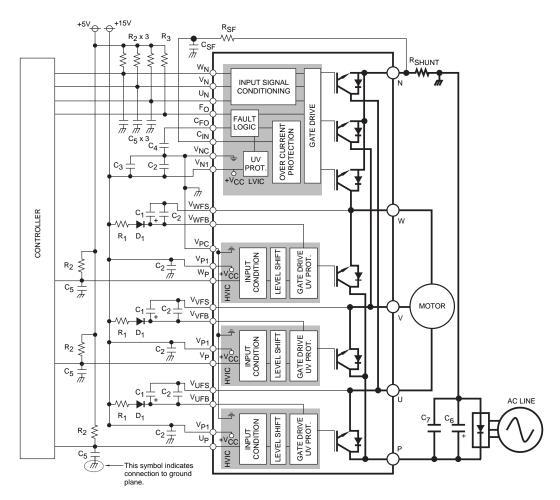
| Characteristic | Symbol | Condition | Min. | Тур. | Max. | Units |
|----------------------------|-----------------------|-------------------------|------|------|-------|---------|
| Junction to Case | R _{th(j-c)Q} | Each IGBT | _ | _ | 2.85 | °C/Watt |
| | R _{th(j-c)D} | Each FWDi | _ | _ | 4.5 | °C/Watt |
| Contact Thermal Resistance | R _{th(c-f)} | Case to Fin Per Module. | _ | _ | 0.067 | °C/Watt |
| | | Thermal Grease Applied | | | | |

Recommended Conditions for Use

| Characteristic | Symbol | Condition | Min. | Тур. | Value | Units |
|---------------------------------|---|---|------|------|-----------|-------|
| Supply Voltage | Vcc | Applied between P-N Terminals | 0 | 300 | 400 | Volts |
| Control Supply Voltage | V _D | Applied between V _{P1} -V _{PC} , V _{N1} -V _{NC} | 13.5 | 15.0 | 16.5 | Volts |
| | V _{DB} | Applied between V _{UFB} -V _{UFS} , | 13.5 | 15.0 | 16.5 | Volts |
| | | V_{VFB} - V_{VFS} , V_{WFB} - V_{WFS} | | | | |
| Control Supply dv/dt | dV _D /dt, dV _{DB} /dt | | -1 | _ | 1 | V/μs |
| Input ON Voltage | V _{CIN(on)} | Applied between Up, Vp, Wp-VpC | | | 0 ~ 0.65 | Volts |
| Input OFF Voltage | V _{CIN(off)} | Applied between U _N , V _N , W _N -V _{NC} | | | 4.0 ~ 5.5 | Volts |
| PWM Input Frequency | f _{PWM} | $T_{C} \le 100^{\circ}C, T_{j} \le 125^{\circ}C$ | _ | 5 | _ | kHz |
| Arm Shoot-through Blocking Time | t _{DEAD} | For Each Input Signal | 2.5 | _ | _ | μS |



PS21244-E Intellimod™ Module Dual-In-Line Intelligent Power Module 15 Amperes/600 Volts



Component Selection:

| Dsgn. | Typ. Value | Description |
|--------------------|------------------|---|
| D ₁ | 1A, 600V | Boot strap supply diode – Ultra fast recovery |
| C ₁ | 10-100uF, 50V | Boot strap supply reservoir – Electrolytic, long life, low Impedance, 105°C (Note 5) |
| C ₂ | 0.22-2.0uF, 50V | Local decoupling/High frequency noise filters - Multilayer ceramic (Note 8) |
| C ₃ | 1-100uF, 50V | Control power supply filter - Electrolytic, long life, low Impedance, 105°C |
| C ₄ | 22nF, 50V | Fault lock-out timing capacitor – Multilayer ceramic (Note 4) |
| C ₅ | 100-1000pF, 50V | Input signal noise filter – Multilayer ceramic (Note 1) |
| C ₆ | 200-2000uF, 450V | Main DC bus filter capacitor - Electrolytic, long life, high ripple current, 105°C |
| C ₇ | 0.1-0.22uF, 450V | Surge voltage suppression capacitor – Polyester/Polypropylene film (Note 9) |
| C _{SF} | 1000pF, 50V | Short circuit detection filter capacitor - Multilayer Ceramic (Note 6, Note 7) |
| R _{SF} | 1.8k ohm | Short circuit detection filter resistor (Note 6, Note 7) |
| R _{SHUNT} | 5-100 mohm | Current sensing resistor - Non-inductive, temperature stable, tight tolerance (Note 10) |
| R ₁ | 1-100 ohm | Boot strap supply inrush limiting resistor (Note 5) |
| R ₂ | 4.7k ohm | Control input pull-up resistor (Note 1, Note 2) |
| R ₃ | 5.1k ohm | Fault output signal pull-up resistor (Note 3) |

Notes:

- 1) To prevent input signal oscillations minimize wiring length to controller (~2cm). Additional RC filtering (C5 etc.) may be required. If filtering is added be careful to maintain proper dead time. See application notes for details
- 2) Internal HVIC provides high voltage level shifting allowing direct connection of all six driving signals to the controller.

- 3) F_O output is an open collector type. This signal should be pulled high with 5.1k ohm resistor (R3). 4) C4 sets the fault output duration and lock-out time. C4 \approx 12.2E⁻⁶ x t_{FO}, 22nF gives \sim 1.8ms 5) Boot strap supply component values must be adjusted depending on the PWM frequency and technique.
- 6) Wiring length associated with R_{SHUNT}, R_{SF}, C_{SF} must be minimized to avoid improper operation of the OC function.
- 7) R_{SF}. C_{SF} set over current protection trip time. Recommend time constant is 1.5us-2.0us. See application notes. 8) Local decoupling/high frequency filter capacitors must be connected as close as possible to the modules pins. 9) The length of the DC link wiring between C6, C7, the DIP's P terminal and the shunt must be minimized to prevent
- excessive transient voltages. In particular C7 should be mounted as close to the DIP as possible
- 10) Use high quality, tight tolorance current sensing resistor. Connect resistor as close as possible to the DIP's N terminal. Be careful to check for proper power rating. See application notes for calculation of resistance value.