# BBS (bass boost system) for radio cassette players BA3870

The BA3870 is a bass boost audio IC for radio cassette players. BBS adjusts the base boost to a level appropriate for the volume, achieving a rich sound without distortion. Frequency characteristics can be set externally, enabling system design to meet the needs of the application.

Applications
Radio cassette players

#### Features

- 1) Adjustment of the base boost to a level appropriate for the volume through BBS.
- Maximum bass boost level can be set externally. (20dB recommended)
- 3) Treble boost circuit (constant at 4dB) included onchip to preserve overall balance.
- 4) BBS on/off "pop" sound almost nonexistent.

#### •Absolute maximum ratings (Ta = $25^{\circ}$ C)

Parameter	Symbol	Limits	Unit
Power supply voltage	Vcc	9.0	V
Power dissipation	Pd	600*	mW
Operating temperature	Topr	-25~+75	Ċ
Storage temperature	Tstg	-55~+125	ĉ

\* Reduced by 6.0mW for each increase in Ta of 1°C over 25°C.

#### • Recommended operating conditions (Ta = $25^{\circ}$ C)

Parameter	Symbol	Limits	Unit
Power supply voltage	Vcc	3.0~7.0	V

#### Block diagram



## Pin descriptions

Pin No.	Pin name	Function		
1	GND	Substrate ground		
2	OUT2	Output 2		
3	BIAS IN	Bias amplifier input		
4	BIAS OUT	Bias amplifier output		
5	DET IN	Detection amplifier positive input		
6	DET NF	Detection amplifier negative input		
7	BBS LEVEL	BBS level setting		
8	BBS 7	BBS time constant setting		
9	OUT1	Output 1		
10	CONT	BBS on/off switch		
11	CONT $\tau$	BBS time constant setting		
12	HP 1	High-range input		
13	IN 1	Input 1		
14	LP 2	Low-range amplifier output		
15	LP 1	Low range amplifier positive input		
16	LP NF	Low range amplifier negative input		
17	N.C.	Not connected to inside of IC		
18	IN 2	Input 2		
19	HP 2	High-range input 2		
20	Vcc	Voltage supply		



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## Audio ICs

#### Input/output circuits









Vcc







•Electrical characteristics (unless otherwise noted,  $Ta = 25^{\circ}C$ , Vcc = 5.0V and f = 1kHz)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition
Quiescent current	la	—	5.0	8.0	mA	BBS ON
Voltage gain	Gv	-2.5	0	2.5	dB	BBS OFF
Total harmonic distortion	THD	_	0.05	0.2	%	BBS ON, Vo=20dBm, DET IN=0Vms
Output noise voltage	VNO	—	-90	-84	dBm	BBS ON, DIN AUDIO, $R_g=2.2k\Omega$
Input resistance	RIN	9	13.5	18	kΩ	_
Channel separation	CS	—	-58	-53	dB	BBS OFF, Vo=0dBm, $R_g=2.2k\Omega$
Bass boost	BB	17	20	23	dB	V <sub>IN</sub> =-50dBm, f=100Hz, DET IN=0V <sub>rms</sub>
Treble boost	ТВ	2.0	4.0	6.0	dB	$V_{IN} = -50 dBm, f = 10 kHz$
Ripple rejection	RR	23	28	_	dB	BBS ON, $V_{RR}$ =-30dBm, f_{RR}=100Hz, $R_g$ =0 $\Omega$
HIGH threshold voltage	Vтн	—	1.1	1.4	V	_
Maximum output voltage	Vом	0.7	1.2	—	Vrms	BBS OFF, THD=1%, R∟=10kΩ



#### Measurement circuit



Fig.1



### Circuit Operation

#### (1) Overview of BBS

BBS is a system which boosts the low end of the audible frequency range and at the same time linearly controls the amount of boost according to the volume to produce a rich sound without distortion through small diameter speakers. It also adjusts the gain of the high-frequency range to attain a balanced sound with natural volume.

- (2) Operation of BBS
- 1) BBS on

The signal system when BBS is on is shown in Fig. 2.



Fig. 2 Signal system when BBS is on.



## Audio ICs

#### 1. Low range signal system

The transmission of low range signals in Fig. 2 is as shown below.



The frequency characteristics of each block are as shown in Fig. 3, and the gain and cutoff frequency of each are given by the following equations.

Cutoff frequency of LPF1 : 
$$f_{CL1} = \frac{1}{2\pi \cdot (R_4//R_5) \cdot C_2}$$
 (Hz)

Attenuation of LPF1 : 
$$G_{LP1} = 20 \log \frac{R_5}{R_4 + R_5}$$
 (dB)

- $\label{eq:Attenuation of ATT1} \quad : G_{\text{ATT1}} = 20 \text{log} \frac{\text{EVR}}{\text{R}_6 + \text{EV}} \qquad (\text{dB})$
- Gain of Amp B :  $G_B = 20 \log \frac{R_7 + R_8}{R_7}$  (dB)
- Cutoff frequency of Amp B:  $f_{CL3} = \frac{1}{2\pi \cdot R_7 \cdot C_3}$  (Hz)
- Cutoff frequency of LPF2: fcL2 =  $\frac{1}{2\pi \cdot R_9 \cdot C4}$  (Hz)
- $Gain \text{ of } Amp \text{ C} \qquad : G_{\text{C}} = 0 \qquad (dB)$
- Gain of Amp D :  $G_{DL} = 20 \log \frac{R_{11}}{R_{10}}$  (dB)

Here,  $R_4 = R_5 = 2R_9$  and  $C_2 = C_4$ , thus  $f_{CL1} = f_{CL2}$ 

and the total frequency shows a -12dB/OCT bass boost as indicated in Fig. 4.

Also, as  $R_{11} = 2R_{10}$ ,

$$G_{LP1} + G_{DL} = -6 + 6$$
  
= 0 (dB)

and thus the total gain during EVR Max. is  $G_{L\,Max.}=G_{LP1}+G_{A}+G_{B}+G_{C}+G_{DL}$ 





2. High-range signal system The transmission of high frequency signals in Fig. 2 is as shown below.

$$\mathsf{IN1} \to \boxed{\mathsf{HPF}} \to \boxed{\mathsf{Amp D}} \to \mathsf{OUT1}$$

The frequency characteristics of each block are shown in Fig. 5, and the gain and cutoff frequency of each are given by the following equations.

Cutoff frequency 1 of HPF: 
$$f_{CH1} = \frac{1}{2\pi \cdot (R_1 + R_2) C_1}$$
 (Hz)  
Cutoff frequency 2 of HPF:  $f_{CH2} = \frac{(R_2 + R_3)}{2\pi \cdot (R_1R_2 + R_2R_3 + R_1R_3) C_1}$  (Hz)

: GHP1 = 20log  $\frac{R_3}{R_2 + R_3}$ Attenuation of HPF in (dB)

Rз Attenuation of HPF in : GHP2 = 20log -(dB)

Gain of Amp D : Gdh

frequency range f < fcH1

frequency range fcH2 < f

= 20log 
$$\frac{R_{10} + R_{11}}{R_{10}}$$
 (dB)

 $R_1//R_2 + R_2$ 

3. Combination of frequency characteristics By combining the low-range characteristics (Fig. 4) and the high-range characteristics (Fig. 6), ideal BBS characteristics can be obtained as shown in Fig. 7.

The total frequency characteristics show a +6dB/OCT treble boost as shown in Fig. 6. Also,  $R_{11} = 2R_{10}$ , thus  $G_{DH} = +9.5 (dB)$ 

and the total gain  $G_{H1}$  in the f < f\_{CH1} frequency range is  $G_{H1} = G_{HP}1 + G_{DH}$  $= G_{HP1} + 9.5 (dB)$ 

The total gain  $G_{H2}$  in the  $f_{CH2} < f$  frequency range is  $G_{H2} = G_{HP2} + G_{DH}$  $= G_{HP2} + 9.5 (dB)$ 



high-range signal system







Fig. 7 Total frequency characteristics when BBS is on



4. Bass boost control system

Bass boost control is as follows in Fig. 8.



Fig. 8 Bass boost control system

The comparator level and gain of each block are given by the following equations. Attenuation of ATT2:

: 
$$G_{ATT2} = 20 \log \frac{R_{13}//R_{14}}{R_{12} + R_{13}//R_{14}}$$
 (dB)

Gain of Amp E:

: 
$$G_E = 20 \log \frac{R_{15} + R_{16}}{R_{16}}$$
 (dB)

Comparator level of COMP:

: 
$$V_{COM} = 20 \log \left( \frac{V_B}{\sqrt{2}} \cdot \frac{R_{17}}{R_{17} + R_{18}} \right)$$
 (dBV)  
V<sub>B</sub>: DC voltage at BIAS OUT pin

If the volume output level at the beginning of control of the low range signal system ATT1 is  $V_{BBS}$  (dBV),

$$V_{BBS} + G_{ATT2} + G_E = V_{COM}$$

In this way, bass boost control is carried out based on the volume as shown in Fig. 9.



Fig. 9 Characteristics of bass boost control based on the volume

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2) BBS off

When BBS is off, the signal system is as shown in Fig. 10 due to the switches in the IC, and signal flow is as follows.

$$IN1 \rightarrow ATT3 \rightarrow Amp D' \rightarrow OUT1$$

The frequency characteristics of each block are shown in Fig. 11, and the gains are given by the following equations:

Attenuation of ATT3:  $G_{ATT3} = 20 \log \frac{R_{20}}{R_{19} + R_{20}}$  (dB)

Gain of Amp D':  $G_D' = 20 \log \frac{R_{21} + R_{22}}{R_{21}}$  (dB)

As  $R_{19} = R_{20}$  and  $R_{21} = R_{22}$ , the total gain GoFF when BBS is off is

 $G_{OFF} = G_{ATT3} + G_D'$ = 0 (dB)

and the characteristics are flat as shown in Fig. 12



Fig. 11 Frequency characteristics of each block when BBS is off



Fig. 10 Signal system when BBS is off



Fig. 12 Total frequency characteristics when BBS is off



#### Application examples



Fig.13







#### Operation notes

(1) External value at  $CONT\tau$  pin

The BBS on/off "pop" sound can be reduced by increasing the value of the capacitor connected to the CONT $\tau$  pin, however, on/off switching time will increase. Select a value appropriate for the application.

#### (2) External value at DET NF pin

The gain  $G_E$  can be decreased by connecting a series RC circuit to the DET NF pin, however, feedback will increase causing greater susceptibility to oscillation. We recommend a gain  $G_E$  of 10dB or more.

(3) External value at BBS LEVEL pin

The comparator level can be increased by reducing the ratio of the resistors connected to the BBS LEVEL pin, however, taking into consideration the power supply voltage characteristics and transient characteristics, we recommend the following:

 $V_{B} > V_{BBS \ LEVEL} \ge V_{B} - 500 mV$   $V_{B} : DC \ voltage \ at \ BIAS \ OUT \ pin$   $( = 1/2 \ V_{CC})$   $V_{BBS \ LEVEL} : DC \ voltage \ at \ BBS \ LEVEL \ pin$ 

#### (4) External value at LP NF pin

The low frequency gain  $G_B$  can be decreased by increasing the value of the resistor in the series RC circuit connected to the LP NF pin, however, feedback will increase causing greater susceptibility to oscillation. We recommend a gain  $G_B$  of 12dB or more.

(5) Coupling capacitors

Decide the polarity of the input and output coupling capacitors based on the electrical potentials of the devices to which they are being connected.



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