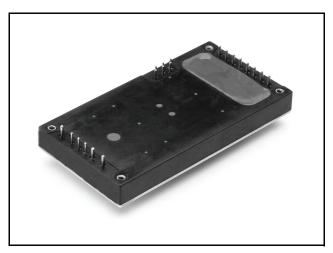
Data Sheet April 02, 2008

DS04-017 ver. 1.1 (Replaces: DS04-017 ver. 1.0)

PDF Name: fc400r1-29.pdf



FC400R1-29 Power Module; dc-dc Converter: 18 Vdc to 36 Vdc Input, 28 Vdc Output; 400 W



The FC400R1-29 Power Module uses advanced, surfacemount technology and delivers high-quality, compact, dc-dc conversion at an economical price.

Applications

- n Redundant and distributed power architectures
- n Computer equipment
- n Communications equipment

Options

n Heat sinks available for extended operation

Features

- ⁿ Size: 61.0 mm x 116.8 mm x 13.5 mm (2.40 in. x 4.60 in. x 0.53 in.)
- Mide input voltage range
- n High efficiency: 85% typical
- n Parallel operation with load sharing
- n Output voltage set-point adjustment (trim)
- n Synchronization
- n Power good signal
- n Output current monitor
- Output overvoltage, overtemperature, and overcurrent protection
- n Constant frequency
- n Case ground pin
- n Input-to-output isolation
- n Remote sense
- n Negative remote on/off logic
- Manufacturing facilities registered against the ISO* 9000 series standards
- UL[†] 60950 Recognized, CSA[‡] C22.2 No. 60950-00 Certified, and VDE § 0805 (IEC**60950) Licensed
- n CE mark meets 73/23/EEC and 93/68/EEC directives^{††}

Description

The FC400R1-29 Power Module is a dc-dc converter that operates over an input voltage range of 18 Vdc to 36 Vdc and provides a precisely regulated dc output. The outputs are fully isolated from the inputs, allowing versatile polarity configurations and grounding connections. The module has a maximum power rating of 400 W at a typical full-load efficiency of 85%.

Two or more modules may be paralleled with forced load sharing for redundant or enhanced power applications. The package, which mounts on a printed-circuit board, accommodates a heat sink for high-temperature applications.

- * ISO is a registered trademark of the International Organization for Standardization.
- † UL is a registered trademark of Underwriters Laboratories, Inc.
- ‡ CSA is a registered trademark of Canadian Standards Association.
- ** IEC is a trademark of International Elecktrotechniker Commission.
- †† This product is intended for integration into end-use equipment. All the required procedures for CE marking of end-use equipment should be followed. (The CE mark is placed on selected products.)

Absolute Maximum Ratings

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. These are absolute stress ratings only. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operations sections of the data sheet. Exposure to absolute maximum ratings for extended periods can adversely affect device reliability.

Parameter	Symbol	Min	Max	Unit
Input Voltage:				
Continuous	Vı	_	80	Vdc
Transient (100 ms)	VI, trans	_	100	Vdc
I/O Isolation Voltage	_	_	1500	Vdc
Operating Case Temperature (See Thermal Considerations section and Figure 16.)	Тс	-40	94	°C
Storage Temperature	Tstg	– 55	125	°C

Electrical Specifications

Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions.

Table 1. Input Specifications

Parameter	Symbol	Min	Тур	Max	Unit
Operating Input Voltage	Vı	18	28	36	Vdc
Maximum Input Current (Vi = 0 V to 36V): FC400R1-29	II, max	_	_	30	Α
Inrush Transient	i ² t	_	_	4.0	A ² s
Input Reflected-ripple Current, Peak-to-peak (5 Hz to 20 MHz, 12 µH source impedance; see Figure 8.)	lı	_	10	_	mAp-p
Input Ripple Rejection (120 Hz)	_	_	60	_	dB

Fusing Considerations

CAUTION: This power module is not internally fused. An input line fuse must always be used.

This encapsulated power module can be used in a wide variety of applications, ranging from simple stand-alone operation to an integrated part of a sophisticated power architecture. To preserve maximum flexibility, internal fusing is not included; however, to achieve maximum safety and system protection, always use an input line fuse. The safety agencies require a fast-acting fuse with a maximum rating of 30 A (see Safety Considerations section). Based on the information provided in this data sheet on inrush energy and maximum dc input current, the same type of fuse with a lower rating can be used. Refer to the fuse manufacturer's data for further information.

Electrical Specifications (continued)

Table 2. Output Specifications

Parameter	Symbol	Min	Тур	Max	Unit
Output Voltage Set Point (VI = 28 V; Io = Io, max; Tc = 25 °C)	Vo, set	27.45	28.0	28.55	Vdc
Output Voltage (Over all operating input voltage, resistive load, and temperature conditions until end of life; see Figure 10 and Feature Descriptions.)	Vo	27.16	_	28.84	Vdc
Output Regulation: Line (VI = 18 V to 36 V) Load (Io = Io, min to Io, max) Temperature (Tc = -40 °C to +100 °C)	_ _ _	_ _ _	0.01 0.05 100	0.1 0.2 300	%Vo %Vo mV
Output Ripple and Noise Voltage (See Figures 4 and 9.): RMS Peak-to-peak (5 Hz to 20 MHz)	_	_	_	50 150	mVrms mVp-p
External Load Capacitance	_	1000	_	*	μF
Output Current (At Io < Io, min, the modules may exceed output ripple specifications.)	lo	0.3	_	14.3	A
Output Current-limit Inception (Vo = 90% of Vo, set; see Feature Descriptions.)	IO, cli	_	_	130 [†]	%IO, max
Output Short-circuit Current (Vo = 1.0 V; indefinite duration, no hiccup mode; see Figure 2.)	_	_	_	150	%IO, max
Efficiency (V _I = 28 V; I _O = I _{O, max} ; T _C = 25 °C; see Figures Figure 3 and 10.)	η	_	85	_	%
Switching Frequency	_	_	475	_	kHz
Dynamic Response $(\Delta Io/\Delta t = 1 \text{ A}/10 \mu\text{s}, \text{ V}_I = 28 \text{ V}, \text{ T}_C = 25 \text{ °C}; \text{ tested}$ with a 1000 μF aluminum and a 1.0 μF ceramic capacitor across the load; see Figures 5 and 6.): Load Change from Io = 50% to 75% of Io, max:					
Peak Deviation Settling Time (Vo < 10% of peak deviation) Load Change from Io = 50% to 25% of Io, max:			400 2000		mV μs
Peak Deviation Settling Time (Vo < 10% of peak deviation)		_ _	400 2000	_ _	mV μs

^{*} Consult your sales representative or the factory. † These are manufacturing test limits. In some situations, results may differ.

Electrical Specifications (continued)

Table 3. Isolation Specifications

Parameter	Min	Тур	Max	Unit
Isolation Capacitance	_	1700	_	pF
Isolation Resistance	10	_	_	MΩ

General Specifications

Parameter	Min	Тур	Max	Unit
Calculated MTBF (Io = 80% of Io, max; Tc = 40 °C)				hours
Weight	_	_	200 (7)	g (oz.)

Feature Specifications

Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions. See the Feature Descriptions section for further information.

Table 4. Feature Specifications

Parameter	Symbol	Min	Тур	Max	Unit
Remote On/Off Signal Interface					
(V _I = 0 V to 36 V; open collector or equivalent					
compatible; signal referenced to V _I (–) terminal):					
Logic Low—Module On					
Logic High—Module Off					
Logic Low:					
At Ion/off = 1.0 mA	Von/off	0	_	1.2	V
At Von/off = 0.0 V	Ion/off	_	_	1.0	mA
Logic High:					
At Ion/off = $0.0 \mu A$	Von/off	_	_	15	V
Leakage Current	Ion/off	_	_	50	μΑ
Turn-on Time		_	100	150	ms
(Io = 80% of Io, max; Vo within ±1% of steady state)					
Output Voltage Overshoot	_		0	5	%Vo, set
Output Voltage Adjustment:					
Output Voltage Remote-sense Range		_	_	0.5	V
Output Voltage Set-point Adjustment Range (trim)	_	60	<u> </u>	102	%Vo, nom
Output Overvoltage Protection	_	30.9*	_	37.0*	V
Output Current Monitor (Io = Io, max, Tc = 70 °C)	IO, mon	_	0.40		V/A

^{*} These are manufacturing test limits. In some situations, results may differ.

Feature Specifications (continued)

Table 4. Feature Specifications (continued)

Parameter	Symbol	Min	Тур	Max	Unit
Synchronization:					
Clock Amplitude	_	4.00	_	5.00	V
Clock Pulse Width	_	0.4	_	_	μs
Fan-out	_	_	_	1	_
Capture Frequency Range	_	425	_	575	kHz
Over temperature Protection	Tc	_	107	_	°C
(See Figure 16.)					
Forced Load Share Accuracy	_	_	10	_	%IO, rated
Power Good Signal Interface:					
Low Impedance—Module Operating	Rpwr/good	_	_	100	Ω
	Ipwr/good	_	_	1	mA
High Impedance—Module Off	Rpwr/good	1	_	_	$M\Omega$
	Vpwr/good	_	_	40	V

Solder, Cleaning, and Drying Considerations

Post solder cleaning is usually the final circuit-board assembly process prior to electrical testing. The result of inadequate circuit-board cleaning and drying can affect both the reliability of a power module and the testability of the finished circuit-board assembly. For guidance on appropriate soldering, cleaning, and drying procedures, refer to the *Board-Mounted Power Modules Soldering and Cleaning* Application Note (AP97-021EPS).

Characteristic Curves

The following figures provide typical characteristics for the power modules.

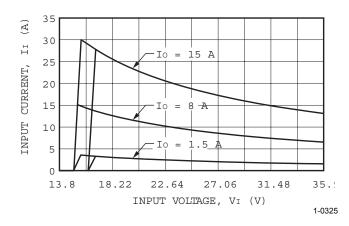


Figure 1. Typical FC400R1-29 Input Characteristics at Room Temperature

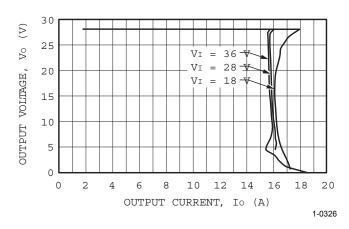


Figure 2. Typical FC400R1-29 Output
Characteristics at Room Temperature

Characteristic Curves (continued)

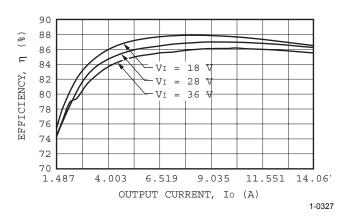
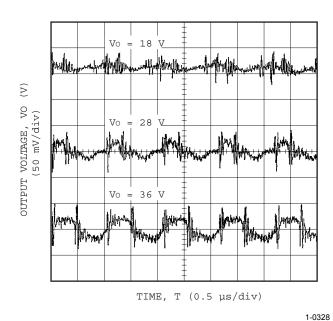
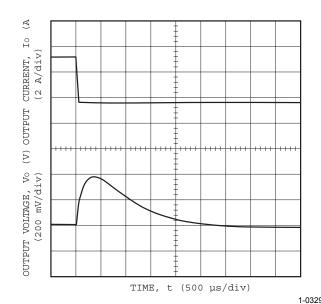


Figure 3. Typical FC400R1-29 Efficiency vs. Output Current at Room Temperature



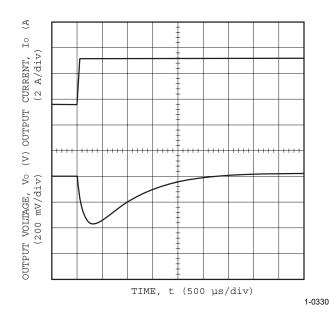
Note: See Figure 9 for test conditions.

Figure 4. Typical FC400R1-29 Output Ripple Voltage at Room Temperature and Full Load Output



Note: Tested with a 1000 μF aluminum and a 1.0 μF ceramic capacitor across the load.

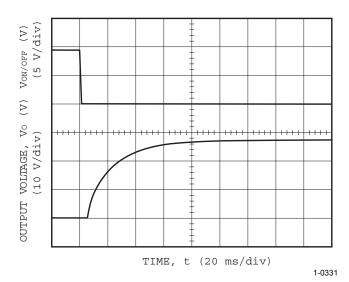
Figure 5. Typical FC400R1-29 Transient Response to Step Decrease in Load from 50% to 25% of Full Load at Room Temperature and 28 V Input (Waveform Averaged to Eliminate Ripple Component.)



Note: Tested with a 1000 μF aluminum and a 1.0 μF ceramic capacitor across the load.

Figure 6. Typical FC400R1-29 Transient Response to Step Increase in Load from 50% to 75% of Full Load at Room Temperature and 28 V Input (Waveform Averaged to Eliminate Ripple Component.)

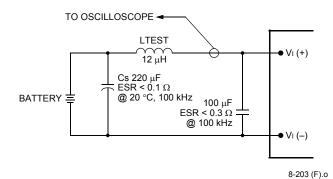
Characteristic Curves (continued)



Note: Tested with a 1000 μF aluminum and a 1.0 μF ceramic capacitor across the load.

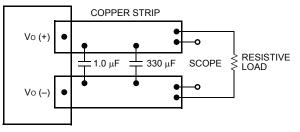
Figure 7. Typical FC400R1-29 Start-Up Transient at Room Temperature, 28 V Input

Test Configurations



Note: Measure input reflected-ripple current with a simulated source inductance (LTEST) of 12 µH. Capacitor Cs offsets possible battery impedance. Measure current as shown above.

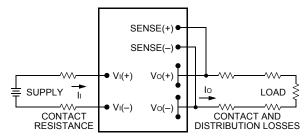
Figure 8. Input Reflected-Ripple Test Setup



8-513 (F).n

Note: Use a 0.1 μ F ceramic capacitor and a 330 μ F aluminum or tantalum capacitor. The 330 μ F capacitor is needed for stability. Scope measurement should be made using a BNC socket. Position the load between 50 mm and 76 mm (2 in. and 3 in.) from the module.

Figure 9. Peak-to-Peak Output Noise Measurement Test Setup



8-683 (F).f

Note: All measurements are taken at the module terminals. When socketing, place Kelvin connections at module terminals to avoid measurement errors due to socket contact resistance.

$$\eta = \left(\frac{[Vo(+) - Vo(-)]Io}{[VI(+) - VI(-)]II}\right) \times 100$$
 %

Figure 10. Output Voltage and Efficiency Measurement Test Setup

Design Considerations

Input Source Impedance

The power module should be connected to a low ac-impedance input source. Highly inductive source impedances can affect the stability of the power module. For the test configuration in Figure 8, a 100 μF electrolytic capacitor (ESR < 0.3 Ω at 100 kHz) mounted close to the power module helps ensure stability of the unit. For other highly inductive source impedances, consult the factory for further application guidelines.

Safety Considerations

For safety-agency approval of the system in which the power module is used, the power module must be installed in compliance with the spacing and separation requirements of the end-use safety agency standard, i.e., *UL*60950, *CSA* C22.2 No. 60950-00, and *VDE* 0805 (IEC60950).

If the input source is non-SELV (ELV or a hazardous voltage greater than 60 Vdc and less than or equal to 75 Vdc), for the module's output to be considered meeting the requirements of safety extra-low voltage (SELV), all of the following must be true:

- The input source is to be provided with reinforced insulation from any hazardous voltages, including the ac mains.
- One V_I pin and one V_O pin are to be grounded, or both the input and output pins are to be kept floating.
- The input pins of the module are not operator accessible.
- Another SELV reliability test is conducted on the whole system, as required by the safety agencies, on the combination of supply source and the subject module to verify that under a single fault, hazardous voltages do not appear at the module's output.

Note: Do not ground either of the input pins of the module without grounding one of the output pins. This may allow a non-SELV voltage to appear between the output pin and ground.

The power module has extra-low voltage (ELV) outputs when all inputs are ELV.

The input to these units is to be provided with a maximum 30 A fast-acting fuse in the ungrounded lead.

Feature Descriptions

Overcurrent Protection

To provide protection in a fault (output overload) condition, the unit is equipped with internal current-limiting circuitry and can endure current limiting for an unlimited duration. At the point of current-limit inception, the unit shifts from voltage control to current control. If the output voltage is pulled very low during a severe fault, the current-limit circuit can exhibit either foldback or tailout characteristics (output-current decrease or increase). The unit operates normally once the output current is brought back into its specified range.

Remote On/Off

To turn the power module on and off, the user must supply a switch to control the voltage between the on/off terminal and the VI(-) terminal (Von/off). The switch can be an open collector or equivalent (see Figure 11). A logic low is Von/off = 0 V to 1.2 V, during which the module is on. The maximum lon/off during a logic low is 1 mA. The switch should maintain a logic-low voltage while sinking 1 mA.

During a logic high, the maximum Von/off generated by the power module is 15 V. The maximum allowable leakage current of the switch at Von/off = 15 V is 50 μ A.

If not using the remote on/off feature, short the ON/OFF pin to VI(-).

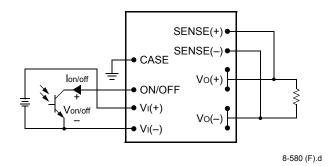


Figure 11. Remote On/Off Implementation

Feature Descriptions (continued)

Remote Sense

Remote sense minimizes the effects of distribution losses by regulating the voltage at the remote-sense connections. The voltage between the remote-sense pins and the output terminals must not exceed the output voltage sense range given in the Feature Specifications table, i.e.:

$$[Vo(+) - Vo(-)] - [SENSE(+) - SENSE(-)] \le 0.5 \text{ V}$$

The voltage between the Vo(+) and Vo(-) terminals must not exceed the minimum value indicated in the output overvoltage shutdown section of the Feature Specifications table. This limit includes any increase in voltage due to remote-sense compensation and output voltage set-point adjustment (trim), see Figure 12.

If not using the remote-sense feature to regulate the output at the point of load, connect SENSE(+) to Vo(+) and SENSE(-) to Vo(-) at the module.

Although the output voltage can be increased by both the remote sense and by the trim, the maximum increase for the output voltage is not the sum of both. The maximum increase is the larger of either the remote sense or the trim. Consult the factory if you need to increase the output voltage more than the above limitation.

The amount of power delivered by the module is defined as the voltage at the output terminals multiplied by the output current. When using remote sense and trim, the output voltage of the module can be increased, which at the same output current would increase the power output of the module. Care should be taken to ensure that the maximum output power of the module remains at or below the maximum rated power.

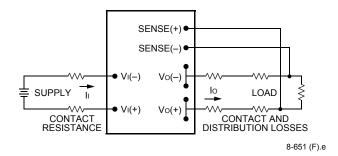


Figure 12. Effective Circuit Configuration for Single-Module Remote-Sense Operation

Output Voltage Set-Point Adjustment (Trim)

Output voltage trim allows the user to increase or decrease the output voltage set point of a module. This is accomplished by connecting an external resistor between the TRIM pin and either the SENSE(+) or SENSE(-) pins. The trim resistor should be positioned close to the module.

If not using the trim feature, leave the TRIM pin open.

With an external resistor between the TRIM and SENSE(–) pins (Radj-down), the output voltage set point (Vo, adj) decreases (see Figure 13). The following equation determines the required external-resistor value to obtain a percentage output voltage change of Δ %.

$$R_{\text{adj-down}} = \left(\frac{205}{\Lambda\%} - 2.255\right) \quad k\Omega$$

With an external resistor connected between the TRIM and SENSE(+) pins (Radj-up), the output voltage set point (Vo, adj) increases (see Figure 14).

The following equation determines the required external-resistor value to obtain a percentage output voltage change of Δ %.

$$L_{adj-up} = \left(\frac{\left(V_{0, nom} \left(1 + \frac{\Delta\%}{100}\right) - 1.225\right)}{\left(1.225\Delta\%\right)} 205 - 2.255\right)$$
 ks

Note: Only trim up to 0.5 V maximum. The output voltage of this module may be increased to a maximum of 0.5 V. The 0.5 V is the combination of both the remote sense and the output voltage set-point adjustment (trim). Do not exceed 28.5 V between the Vo(+) and Vo(-) terminals.

Although the output voltage can be increased by both the remote sense and by the trim, the maximum increase for the output voltage is not the sum of both. The maximum increase is the larger of either the remote sense or the trim. Consult the factory if you need to increase the output voltage more than the above limitation.

Feature Descriptions (continued)

Output Voltage Set-Point Adjustment (Trim) (continued)

The amount of power delivered by the module is defined as the voltage at the output terminals multiplied by the output current. When using remote sense and trim, the output voltage of the module can be increased, which at the same output current would increase the power output of the module. Care should be taken to ensure that the maximum output power of the module remains at or below the maximum rated power.

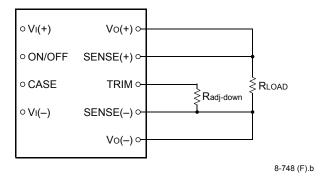


Figure 13. Circuit Configuration to Decrease Output Voltage

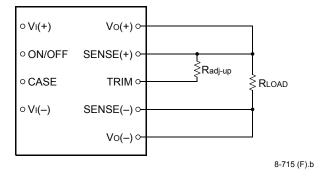


Figure 14. Circuit Configuration to Increase Output Voltage

Output Overvoltage Protection

The output voltage is monitored at the Vo(+) and Vo(-) pins of the module. If the voltage at these pins exceeds the value indicated in the Feature Specifications table, the module will shut down and latch off. Recovery from latched shutdown is accomplished by cycling the dc input power off for at least 1.0 second or toggling the primary referenced on/off signal for at least 1.0 second.

Output Current Monitor

The CURRENT MON pin provides a dc voltage proportional to the dc output current of the module given in the Feature Specifications table. For example, on the FC400R1-29, the V/A ratio is set at 370 mV/A \pm 10% @ 70 °C case. At a load current of 9 A, the voltage on the CURRENT MON pin is 3.33 V. The current monitor signal is referenced to the SENSE(–) pin on the secondary and is supplied from a source impedance of approximately 2 k Ω . It is recommended that the CURRENT MON pin be left open when not in use, although no damage will result if the CURRENT MON pin is shorted to secondary ground. Directly driving the CURRENT MON pin with an external source will detrimentally affect operation of the module and should be avoided.

Synchronization

Any module can be synchronized to any other module or to an external clock using the SYNC IN or SYNC OUT pins. The modules are not designed to operate in a master/slave configuration; that is, if one module fails, the other modules will continue to operate.

SYNC IN Pin

This pin can be connected either to an external clock or directly to the SYNC OUT pin of another FW250x or FW300x module.

If an external clock signal is applied to the SYNC IN pin, the signal must be a 500 kHz (±50 kHz) square wave with a 4 Vp-p amplitude. Operation outside this frequency band will detrimentally affect the performance of the module and must be avoided.

If the SYNC IN pin is connected to the SYNC OUT pin of another module, the connection should be as direct as possible, and the $V_1(-)$ pins of the modules must be shorted together.

Unused SYNC IN pins should be tied to V_I(–). If the SYNC IN pin is unused, the module will operate from its own internal clock.

SYNC OUT Pin

This pin contains a clock signal referenced to the V_I(–) pin. The frequency of this signal will equal either the module's internal clock frequency or the frequency established by an external clock applied to the SYNC IN pin.

Feature Descriptions (continued)

SYNC OUT Pin (continued)

When synchronizing several modules together, the modules can be connected in a daisy-chain fashion where the SYNC OUT pin of one module is connected to the SYNC IN pin of another module. Each module in the chain will synchronize to the frequency of the first module in the chain.

To avoid loading effects, ensure that the SYNC OUT pin of any one module is connected to the SYNC IN pin of only one module. Any number of modules can be synchronized in this daisy-chain fashion.

Overtemperature Protection

To provide protection in a fault condition, the unit is equipped with an over temperature shutdown circuit. The shutdown circuit will not engage unless the unit is operated above the maximum case temperature of approximately 107 °C. This unit is equipped with an automatic restart feature which gets activated when case temperature drops approximately 25 °C below temperature shut down.

Forced Load Sharing (Parallel Operation)

For either redundant operation or additional power requirements, the power modules can be configured for parallel operation with forced load sharing (see Figure 15). For a typical redundant configuration, Schottky diodes or an equivalent should be used to protect against short-circuit conditions. Because of the remote sense, the forward-voltage drops across the Schottky diodes do not affect the set point of the voltage applied to the load. For additional power requirements, where multiple units are used to develop combined power in excess of the rated maximum, the Schottky diodes are not needed.

Good layout techniques should be observed for noise immunity. To implement forced load sharing, the following connections must be made:

- The parallel pins of all units must be connected together. The paths of these connections should be as direct as possible.
- All remote-sense pins should be connected to the power bus at the same point, i.e., connect all SENSE(+) pins to the (+) side of the power bus at the same point and all SENSE(-) pins to the (-) side of the power bus at the same point. Close proximity and directness are necessary for good noise immunity.

When not using the parallel feature, leave the PARALLEL pin open.

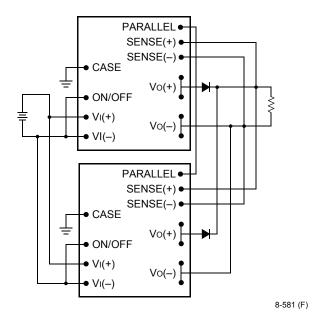


Figure 15. Wiring Configuration for Redundant Parallel Operation

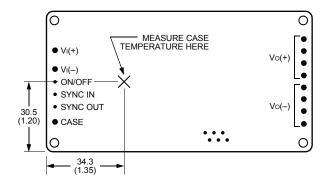
Power Good Signal

The PWR GOOD pin provides an open-drain signal (referenced to the SENSE(–) pin) that indicates the operating state of the module. A low impedance (<100 Ω) between PWR GOOD and SENSE(–) indicates that the module is operating. A high impedance (>1 $M\Omega$) between PWR GOOD and SENSE(–) indicates that the module is off or has failed. The PWR GOOD pin can be pulled up through a resistor to an external voltage to facilitate sensing. This external voltage level must not exceed 40 V, and the current into the PWR GOOD pin during the low-impedance state should be limited to 1 mA maximum.

Thermal Considerations

Introduction

The power modules operate in a variety of thermal environments; however, sufficient cooling should be provided to help ensure reliable operation of the unit. Heat-dissipating components inside the unit are thermally coupled to the case. Heat is removed by conduction, convection, and radiation to the surrounding environment. Proper cooling should be verified by measuring the case temperature. Peak temperature occurs at the position indicated in Figure 16.



8-1303 (F).g

Note: Top view, measurements shown in millimeters and (inches). Pin locations are for reference only.

Figure 16. Case Temperature Measurement Location

Sufficient cooling methods must be designed to hold the maximum case temperature at this location to 94 °C as shown in Figure 17. The maximum case temperature can be limited to a lower value for extremely high reliability. The output power of the module should not exceed the rated power of the module as listed in the Ordering Information table.

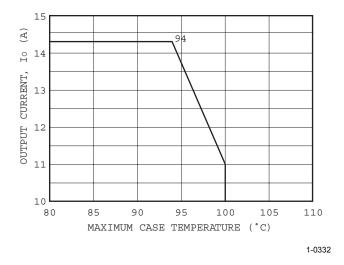


Figure 17. FC400R1-29 Max Case Temperature Using a Cold Plate

Cold Plate Mounting Configuration

This power module may be used in applications where heat must be conducted away from the module by attachment to a metal plate. The derating curve in Figure 17 was obtained from measurements obtained in experimental apparatus shown in Figure 18

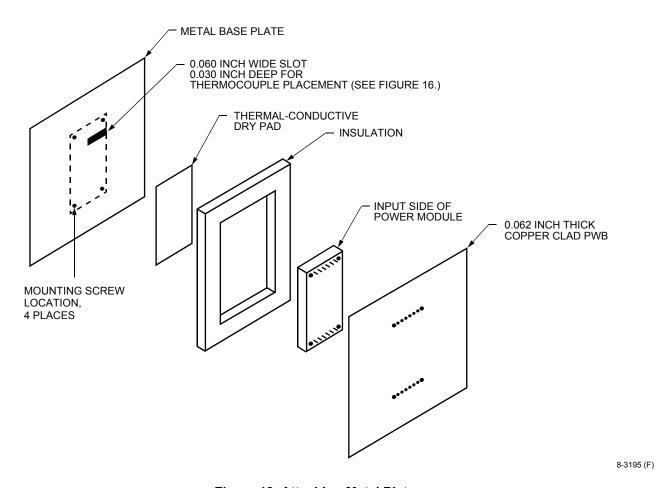


Figure 18. Attaching Metal Plates

Thermal Considerations (continued)

Custom Cold Plates / Heat Sinks

A more detailed model can be used to determine the required thermal resistance of a cold plate / heat sink to provide necessary cooling. The total module resistance can be separated into a resistance from case-to-sink (θ cs) and sink-to-ambient (θ sa) as shown in Figure 19.

$$PD \xrightarrow{T_C} \frac{T_S}{\theta cs} \xrightarrow{\theta sa} \frac{T_A}{\theta sa}$$

8-1304 (F).e

Figure 19. Resistance from Case-to-Sink and Sink-to-Ambient

For a managed interface using thermal grease or foils, a value of θ cs = 0.1 °C/W to 0.3 °C/W is typical. The solution for heat sink resistance is:

$$\theta$$
sa = $\left[\frac{(Tc - TA)}{PD}\right] - \theta cs$

This equation assumes that all dissipated power must be shed by the cold plate / heat sink. Depending on the userdefined application environment, a more accurate model, including heat transfer from the sides and bottom of the module, can be used.

EMC Considerations

For assistance with designing for EMC compliance, please refer to the *FLTR100V10 Filter Module* Data Sheet (DS99-294EPS).

Layout Considerations

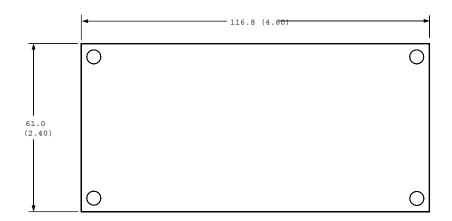
Copper paths must not be routed beneath the power module mounting inserts. For additional layout guidelines, refer to the *FLTR100V10 Filter Module* Data Sheet (FDS01-043EPS).

Outline Diagram

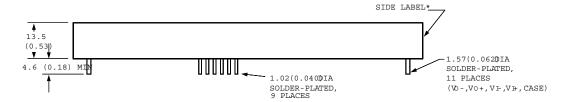
Dimensions are in millimeters and (inches).

Tolerances: x.x mm \pm 0.5 mm (x.xx in. \pm 0.02 in.), x.xx mm \pm 0.25 mm (x.xxx in. \pm 0.010 in.)

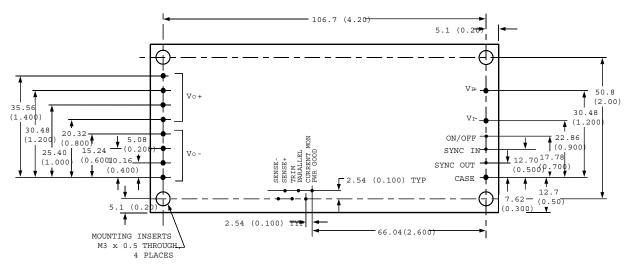
Top View



Side View



Bottom View



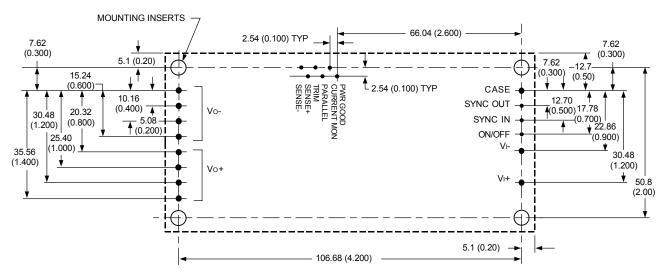
8-1650(C).c

^{*} Side label includes Lineage name, product designation, safety agency markings, input/output voltage and current ratings, and bar code.

Recommended Hole Pattern

Component-side footprint.

Dimensions are in millimeters and (inches).



8-1650(C).c

Ordering Information

Please contact your Lineage Power Account Manager or Field Application Engineer for pricing and availability.

Table 5. Device Codes

Input Voltage	Output Voltage	Output Power	Device Code	Comcode
24 Vdc	28 Vdc	400 W	FC400R1-29	108787524

Optional features can be ordered using the suffixes shown in Table 6. The suffixes follow the last letter of the device code except the "-xx" suffix which are placed at the end of device code. To order more than one option, list device codes suffixes in numerically descending order. For example, the device code for a FC400R61-29 module with the following option is shown below:

FC400R61-29: Standard device code includes option 6 of lead trim.

Table 6

Option	Suffix
Pin Length: 3.68 mm ± 0.25 mm (0.145 in. ± 0.010 in.)	6

Data Sheet April 02, 2008

Notes



World Wide Headquarters
Lineage Power Corporation
30 00 Skyline Drive, Mesquite, TX 75149, USA
+1-800-526-7819
(Outside U.S.A.: +1-972-284-2626)
www.lineagepower.com
e-mail: techsupport1@lineagepower.com

Asia-Pacific Headquarters

Tel: +65 641 6 4283

Europe, Middle-East and Africa Headquarters

Tel: +49 89 6089 286

India Head quarters Tel: +91 80 28411633

Lineage Power reserves the right to make changes to the product(s) or information contained herein without notice. No liability is assumed as a result of their use or application. No rights under any patent accompany the sale of any such product(s) or information.

© 2008 Lineage Power Corporation, (Mesquite, Texas) All International Rights Reserved.

April 02, 2008

DS04-017 ver. 1.1 (Replaces: DS04-017 ver. 1.0)

PDF name: fc400r1-29.pdf