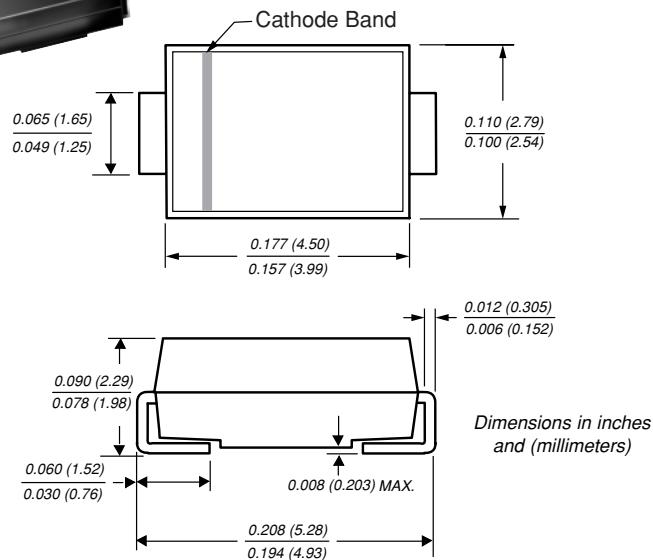
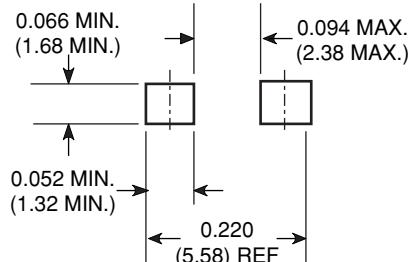


High-Current Density Surface Mount Schottky Rectifier


DO-214AC (SMA)

 Reverse Voltage 30 & 40 V
 Forward Current 2.0 A

Mounting Pad Layout

Features

- Low power loss, high efficiency
- Low profile surface mount package
- Built-in strain relief
- For use in low voltage high frequency inverters, free wheeling, and polarity protection applications
- Guardring for overvoltage protection
- Plastic package has Underwriters Laboratory Flammability Classification 94V-0

Mechanical Data
Case: JEDEC DO-214AC molded plastic body

Terminals: Solder plated, solderable per MIL-STD750, Method 2026

 High temperature soldering guaranteed:
 250°C/10 seconds at terminals

Polarity: Color band denotes cathode end

Weight: 0.002 ounce, 0.064 gram

Maximum Ratings and Thermal Characteristics (TA = 25°C unless otherwise noted)

| Parameter | Symbol | B230LA | B240A | Unit |
|--|------------------------------------|--------------|-------|------|
| Device marking code | | B23 | B24 | V |
| Maximum repetitive peak reverse voltage | V _{RRM} | 30 | 40 | V |
| Maximum RMS voltage | V _{RMS} | 21 | 28 | V |
| Maximum DC blocking voltage | V _{DC} | 30 | 40 | V |
| Maximum average forward rectified current at TL (See Fig. 1) | I _{F(AV)} | 2.0 | | A |
| Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method) | I _{FSM} | 50 | | A |
| Typical thermal resistance ⁽²⁾ | θ _{JA} θ _{JL} | 110 28 | | °C/W |
| Voltage rate of change (rated V _R) | dV/dt | 10,000 | | V/μs |
| Operating junction temperature range | T _J | -65 to + 150 | | °C |
| Storage temperature range | T _{STG} | -65 to + 150 | | °C |

Electrical Characteristics (TA = 25°C unless otherwise noted)

| Parameter | Symbol | Max. | Max. | Unit | |
|--|---------------------|----------------|------|------|----|
| Maximum instantaneous forward voltage at 2.0A ⁽¹⁾ | T _{J=25°C} | V _F | 0.50 | 0.55 | V |
| Maximum DC reverse current at rated DC blocking voltage ⁽¹⁾ | T _{J=25°C} | I _R | 0.5 | 0.5 | mA |

Notes: (1) Pulse test: 300μs pulse width, 1% duty cycle

(2) Aluminum substrate mounted

B230LA & B240A

Vishay Semiconductors
formerly General Semiconductor



Ratings and Characteristic Curves ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Fig. 1 – Forward Current Derating Curve

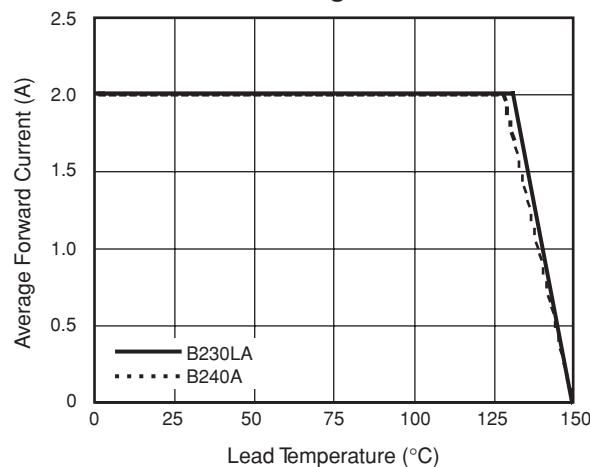


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current

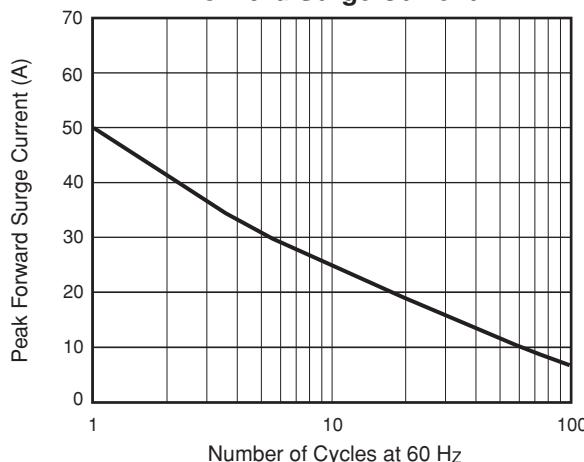


Fig. 3 - Typical Instantaneous Forward Characteristics

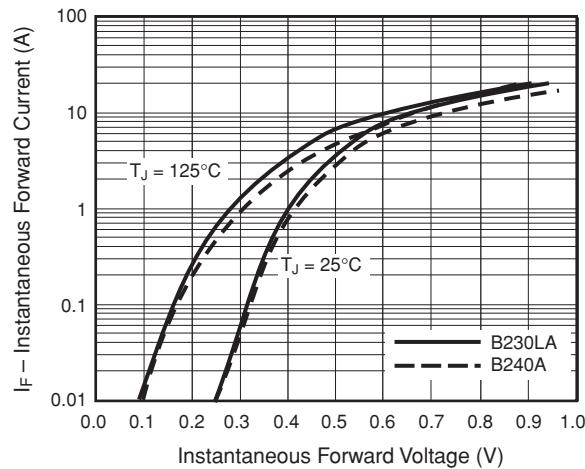


Fig. 4 - Typical Reverse Characteristics

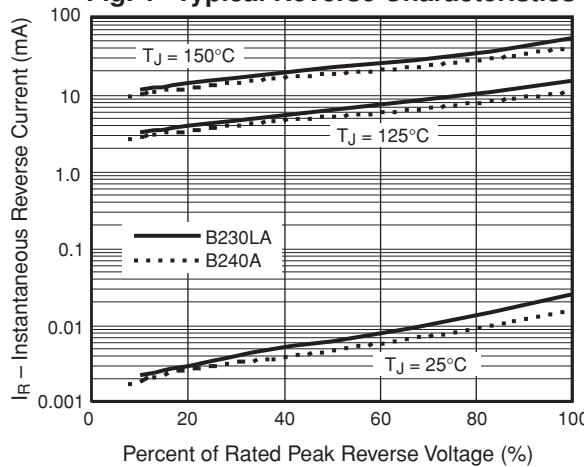


Fig. 5 - Typical Junction Capacitance

