

Features

- RoHS Compliant (with F or G pin option)
- >40 dB ripple attenuation from 60 Hz to 1 MHz
- Integrated OR'ing diode supports N+1 redundancy
- Significantly improves load transient response
- Efficiency up to 98%
- User selectable performance optimization
- · Combined active and passive filtering
- 3 30 Vdc input range
- 20 and 30 Ampere ratings

Product Highlights

Vicor's MicroRAM output ripple attenuation module combines both active and passive filtering to achieve greater than 40 dB of noise attenuation from 60 Hz to 1 Mhz. The MicroRAM operates over a range of 3 to 30 Vdc, is available in either 20 or 30 A models and is compatible with most manufacturers switching converters including all Vicor DC-DC converter models.

The MicroRAM's closed loop architecture greatly improves load transient response and with dual mode control, insures precise point of load voltage regulation, The MicroRAM supports redundant and parallel operation with its integrated OR'ing diode function.

It is available in Vicor's standard Micro package (quarter brick) with a variety of terminations for through hole, socket or surface mount applications.

Data Sheet **MicroRAM**TM



Output Ripple Attenuation Module



Actual size: 2.28 x 1.45 x 0.5 in 57,9 x 36,8 x 12,7 mm

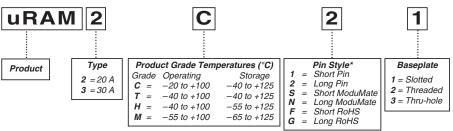
Absolute Maximum Ratings

Parameter	Rating	Unit	Notes
+ln to -ln	30	Vdc	Continuous
	40	Vdc	100ms
Load current	40	Adc	Continuous
Ripple Input (Vp-p)	100	mV	60 Hzc 100 kHz
	500	mV	100 kHz – 2 MHz
Mounting torque	4 – 6 (0.45 – 0.68)	In. lbs (Nm)	6 each, 4-40 screw
Din coldoring tomporature	500 (260)	°F (°C)	<5 sec; wave solder
Pin soldering temperature	750 (390)	°F (°C)	<7 sec; wave solder

Thermal Resistance

Parameter	Тур	Unit	
Baseplate to sink			
flat, greased surface	0.16	°C/Watt	
with thermal pad (P/N 20265)	0.14	°C/Watt	
Baseplate to ambient			
free convection	8.0	°C/Watt	
1000 LFM	1.9	°C/Watt	

Part Numbering



*Pin styles S & N are compatible with the ModuMate interconnect system for socketing and surface mounting.

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ELECTRICAL CHARACTERISTICS

Electrical characteristics apply over the full operating range of input voltage, output power and baseplate temperature, unless otherwise specified. All temperatures refer to the operating temperature at the center of the baseplate.

■ µRAM MODULE SPECIFICATIONS (-20°C to +100°C baseplate temperature)

Parameter	Min	Тур	Max	Unit	Notes	
Operating current range					No internal current limiting. Converter input must be	
μRAM2xxx	0.02		20	Α	properly fused such that the µRAM output current	
µRAM3xxx	0.02		30	Α	does not exceed the maximum operating current	
					rating by more than 30% under a steady state condition.	
Operating input voltage	3.0		30	Vdc	Continuous	
Transient output response	ient output response 50 mVp-p		Step load change;			
Load current step <1A/µsec			30	шур-р	see Figures 9, 12, & 15, pp. 6-7	
Transient output response					Optional capacitance CTRAN can be used	
Load current step <1A/µsec			50	mVp-p	to increase transient current capability; See Figures	
(CTRAN = 820 μF)				1 & 2 on p. 3 and Figures 10, 13, & 16 on pp. 6-7		
VHR headroom voltage range ¹	005		405	mV	See Figures 5, 6 & 7	
@ 1A load	325		425		See Table 1 for headroom setting resistor values	
Output ripple Input Vp-p = 100 mV			10	mVp-p	Ripple frequency 60 Hz to 100 kHz; optional capacitor	
			5	mVrms	CHR = 100 μF required to increase low frequency	
					attenuation as shown in Figures 3a and 3b	
					see Figures 8, 11, & 14, pp. 6 – 7	
Output ripple			10	mVp-p	Ripple frequency 100 kHz to 2 MHz;	
Input Vp-p = 500 mV			5	mVrms	see Figures 8, 11, & 14, pp. 6-7	
SC output voltage ²	1.23			Vdc	See Table 1 RSC value	
OR'ing threshold		10		mV	Vin – Vout	
μRAM bias current			60	mA		
Power dissipation						
μRAM2xxx VHR = 380 mV@1 A		7.5		W	Vin = 28 V; lout = 20 A	
µRAM3xxx VHR = 380 mV@1 A		11.5		W	Vin = 28 V; lout = 30 A	

¹ Headroom is the voltage difference between the +Input and +Output pins.

 $RHR = (\mu RAM + Out/VHR) \times 2.3 \text{ k (see Table 1 for example values)}$

$$Rsc = ((\mu RAM + Out)/1.23 V x 1k) - 2 k$$

μRAM Out	VHR @ 1A	Rhr Value (ohms)	Rsc Value (ohms)
3.0 V	375 mV	18.4 k	0.439 k
5.0 V	375 mV	30.6 k	2.07 k
12.0 V	375 mV	73.6 k	7.76 k
15.0 V	375 mV	92.0 k	10.20 k
24.0 V	375 mV	147.2 k	17.50 k
28.0 V	375 mV	171.7 k	20.76 k

 $Table\ 1$ – RHR and RSC are computed values for a 375 mV case. To compute different headroom voltages, or for standard resistor values and tolerances, use Notes 1 and 2.

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² SC resistor is required to trim the converter output up to accommodate the headroom of the μRAM module when remote sense is not used. This feature can only be used when the trim reference of the converter is in the 1.21 to 1.25 Volt range. (see Table 1 with calculated Rsc resistor values)

ELECTRICAL CHARACTERISTICS (CONT.)

APPLICATION SCHEMATIC DRAWINGS USING VICOR CONVERTERS AND THE μRAM

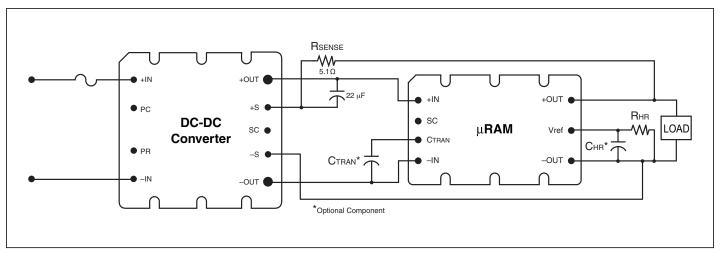


Figure 1 — Typical Configuration using Remote Sensing

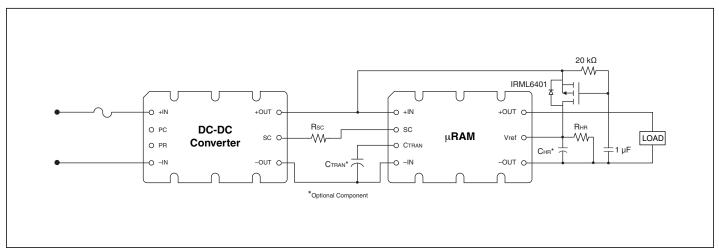


Figure 2 — Typical Configuration using SC Control (Oppional CHR 25µF maximum in SC configuration.)

FUNCTIONAL DESCRIPTION

The MicroRAM has an internal passive filter that effectively attenuates ripple in the 50 kHz to 1 MHz range. An active filter provides attenuation from low frequency up to the 1 MHz range. The user must set the headroom voltage of the active block with the external Rhr resistor to optimize performance. The MicroRAM must be connected as shown in Figures 1 or 2 depending on the load sensing method. The transient load current performance can be increased by the addition of optional Ctran capacitance to the Ctran pin. The low frequency ripple attenuation can be increased by addition of optional Chr capacitance to the Vref pin as shown in Figures 3a and 3b, on p. 5.

Transient load current is supplied by the internal CTRAN capacitance, plus optional external capacitance, during the time it takes the converter loop to respond to the increase in load. The MicroRAM's active loop responds in roughly one microsecond to output voltage perturbations. There are limitations to the magnitude and the rate of change of the transient current that the MicroRAM can sustain while the converter responds. See Figures 8 – 16, on pp. 6 and 7, for examples of dynamic performance. A larger headroom voltage setting will provide increased transient performance, ripple attenuation and power dissipation while reducing overall efficiency (see Figures 4a, 4b, 4c and 4d on p. 5).

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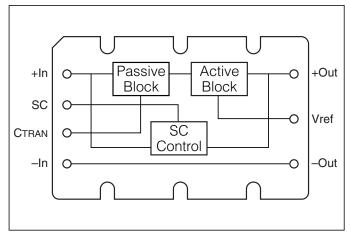
FUNCTIONAL DESCRIPTION (CONT.)

The active loop senses the output current and reduces the headroom voltage in a linear fashion to approximate constant power dissipation of MicroRAM with increasing loads (see Figures 5, 6 & 7, p. 6). The headroom setting can be reduced to decrease power dissipation where the transient requirement is low and efficient ripple attenuation is the primary performance concern.

The active dynamic headroom range is limited on the low end by the initial headroom setting and the maximum expected load. If the maximum load in the application is 10 Amps, for example, the 1 Amp headroom can be set 75mV lower to conserve power and still have active headroom at the maximum load current of 10 Amps. The high end or maximum headroom range is limited by the internal OR'ing diode function.

The SC or trim-up function can be used when remote sensing is not available on the source converter or is not desirable. It is specifically designed for converters with a 1.23 Volt reference and a 1k ohm input impedance like Vicor Maxi, Mini, Micro converters. In comparison to remote sensing, the SC configuration will have an error in the load voltage versus load current. It will be proportional to the output current and the resistance of the load path from the output of the MicroRAM to the load.

The OR'ing feature prevents current flowing from the output of the MicroRAM back through it's input terminal in a redundant system configuration in the event that a converter output fails. When the converter output supplying the MicroRAM droops below the OR'ed output voltage potential of the redundant system, the input of the MicroRAM is isolated from it's output. Less than 50mA will flow out of the input terminal of the MicroRAM over the full range of input voltage under this condition.



Block Diagram

APPLICATION NOTES

Load capacitance can affect the overall phase margin of the MicroRAM active loop as well as the phase margin of the converter loop. The distributed variables such as inductance of the load path, the capacitor type and value as well as its ESR and ESL also affect transient capability at the load. The following guidelines should be considered when point of load capacitance is used with the MicroRAM in order to maintain a minimum of 30 degrees of phase margin.

- Using ceramic load capacitance with <1milliohm ESR and <1nH ESL:
 - (a) 20 μ F to 200 μ F requires 20 nH of trace/wire load path inductance
 - (b) 200 μ F to 1,000 μ F requires 60 nH of trace/wire load path inductance

- 2) For the case where load capacitance is connected directly to the output of the MicroRAM, i.e. no trace inductance, and the ESR is >1 milliohm:
 - (a) 20 μF to 200 μF load capacitance needs an ESL of >50 nH
 - (b) 200 μ F to 1,000 μ F load capacitance needs an ESL of >5 nH
- Adding low ESR capacitance directly at the output terminals of MicroRAM is not recommended and may cause stability problems.
- 4) In practice the distributed board or wire inductance at a load or on a load board will be sufficient to isolate the output of the MicroRAM from any load capacitance and minimize any appreciable effect on phase margin.

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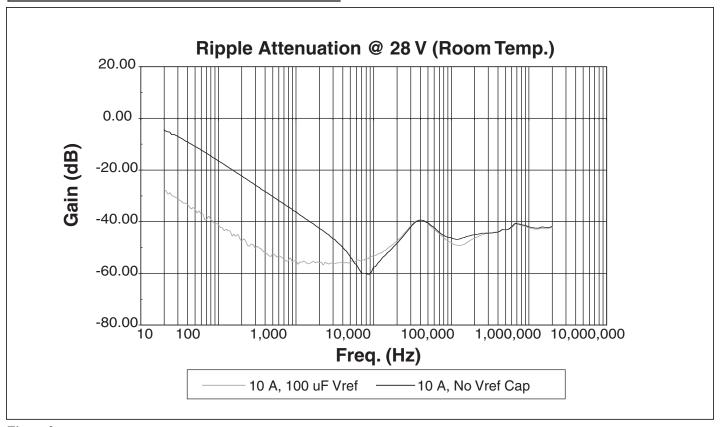


Figure 3a

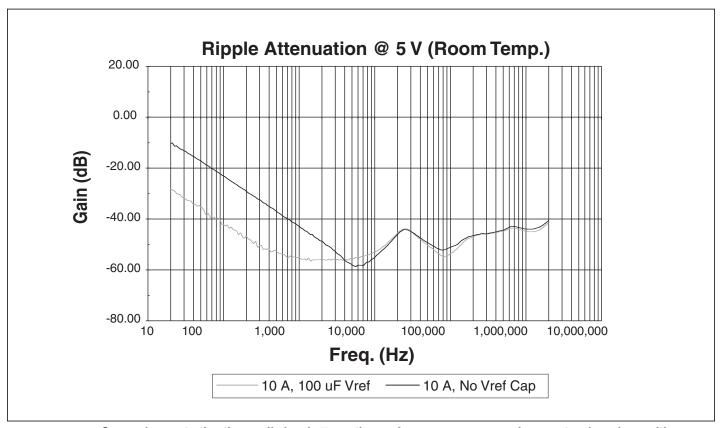


Figure 3a, 3b — Curves demonstrating the small signal attenuation performance as measured on a network analyzer with a typical module at (a) 28 V and 10 A output and (b) 5 V and 10 A. The low frequency attenuation can be enhanced by connecting a 100 μ F capacitor, CHR, to the VREF pin as shown in Figures 1 and 2.

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μRAM2xxx

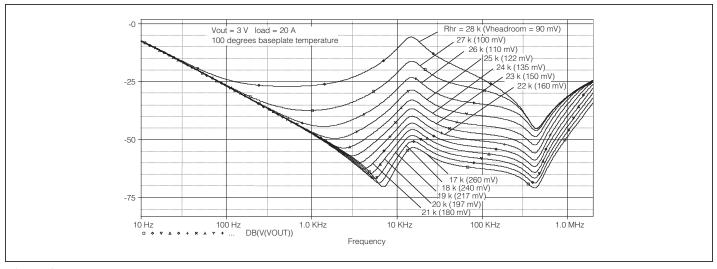


Figure 4a

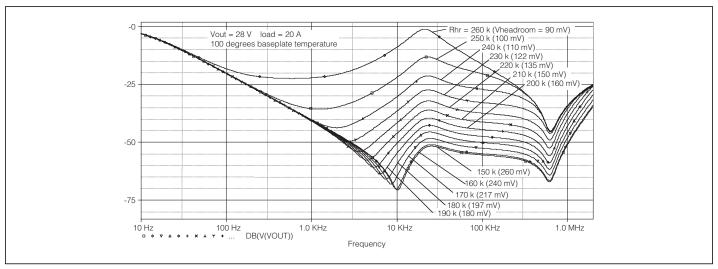


Figure 4a - 4b — Simulated graphs demonstrating the tradeoff of attenuation versus headroom setting at 20 Amps and an equivalent 100°C baseplate temperature at 3 V and 28 V.

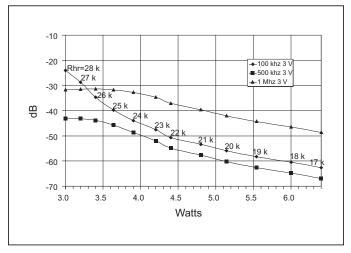


Figure 4c

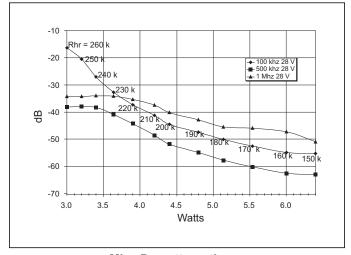


Figure 4c - 4d — MicroRam attenuation vs. power dissipation at 3 V 20 A, and 28 V 20 A.

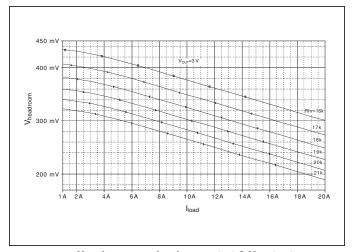


Figure 5 — Headroom vs. load current at 3 V output.

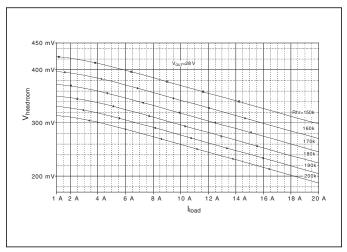


Figure 7 — Headroom vs. load current at 28 V output.

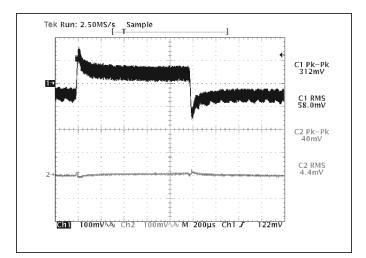


Figure 9 — V375A28C600B and μ RAM; Input and output dynamic response no added Ctran; 20% of 20 A rating load step of 4 A (10 A – 14 A);RHR = 178 k (Configured as in Figs. 1 & 2)

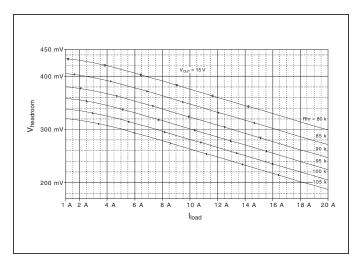


Figure 6 — Headroom vs. load current at 15 V output.

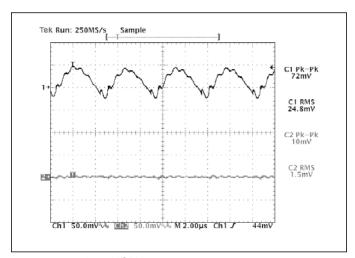


Figure 8 — V375A28C600B and μ RAM; Input and output ripple @50% (10 A) load CH1=Vi; CH2=Vo; Vi-Vo=332 mV; RHR=178 k

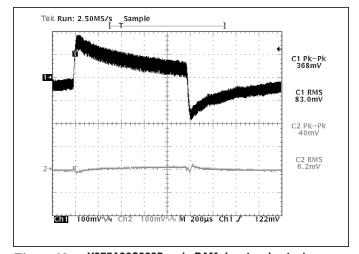


Figure 10 — V375A28C600B and μ RAM; Input and output dynamic response Ctran=820 μ F Electrolytic; 32.5% of load step of 6.5 A (10 A – 16.5 A);RHR=178 k (Configured as in Figs. 1 & 2)

Notes: The measurements in Figures 8-16 were taken with a µRAM2C21 and standard scope probes with a 20MHz bandwidth scope setting. The criteria for transient current capability was as follows: The transient load current step was incremented from 10A to the peak value indicated, then stepped back to 10A until the resulting output peak to peak was around 40mV.

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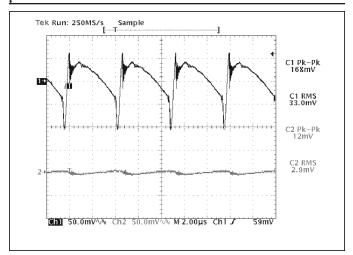


Figure 11 — V375B12C250B and μ RAM; Input and output ripple@50% (10 A) load CH1=Vi; CH2=Vo; Vi-Vo=305mV; RHR=80k (Configured as in Figs. 1 & 2)

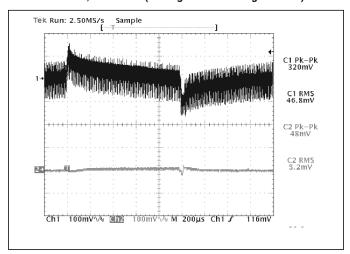


Figure 13 — V300B12C250B and μ RAM; Input and output dynamic response Ctran = 820 μ F Electrolytic; 30% of load step of 6 A (10 A – 16 A);RHR=80 k (Configured as in Figs. 1 & 2)

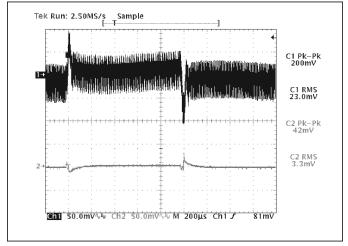


Figure 15 — V48C5C100B and μ RAM; Input and output dynamic response no added Ctran; 22.5% of 20 A rating load step of 4.5 A (10 A – 14.5 A);RHR=31k (Configured as in Figs. 1 & 2)

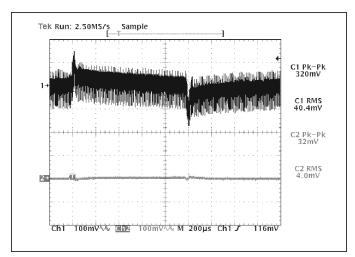


Figure 12 — V300B12C250B and μ RAM; Input and output dynamic response no added Ctran; 17.5% of 20 A rating load step of 3.5 A (10 A – 13.5 A); RHR=80 k (Configured as in Figs. 1 & 2)

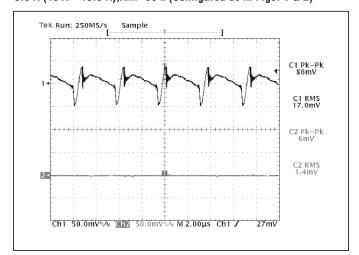


Figure 14 — V48C5C100B and μRAM; Input and output ripple @50% (10 A) load CH1=Vi; CH2=Vo; Vi-Vo=327mV; R_{HR}=31k (Configured as in Figs. 1 & 2)

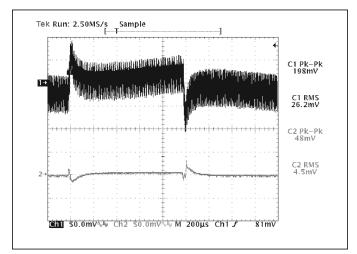


Figure 16 — V48C5C100B and μ RAM; Input and output dynamic response Ctran=820 μ F Electrolytic; 35% of load step of 7 A (10 A – 17 A);RHR=31 k (Configured as in Figs. 1 & 2)

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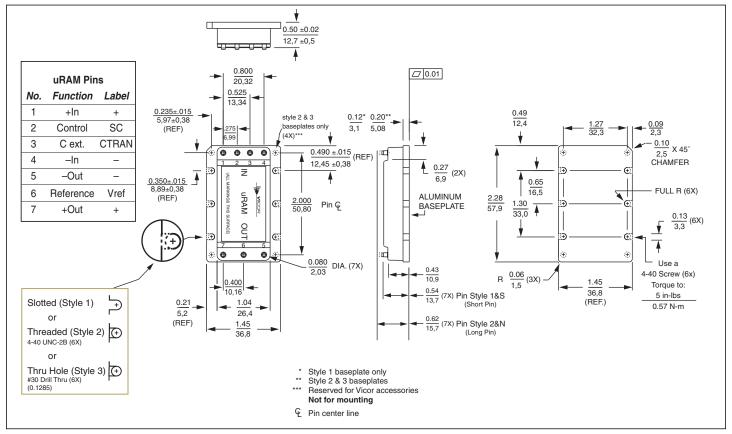


Figure 17 — Module outline

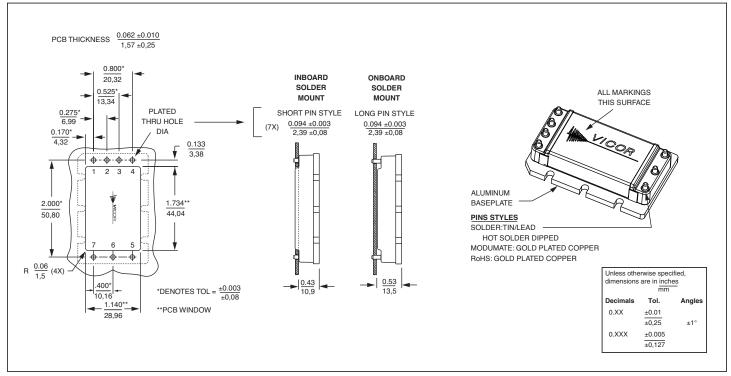


Figure 18 — PCB mounting specifications

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Vicor Corporation

25 Frontage Road Andover, MA, USA 01810 Tel: 800-735-6200

Fax: 978-475-6715

email

Customer Service: custserv@vicorpower.com Technical Support: apps@vicorpower.com