TOSHIBA Intelligent Power Module Silicon N Channel IGBT

MIG50J101H

High Power Switching Applications Motor Control Applications

- Integrates inverter & control circuits (IGBT drive units, protection units for over-current, under-voltage & over-temperature) in one package.
- The electrodes are isolated from case.

7. GND (W)

13.IN(X)

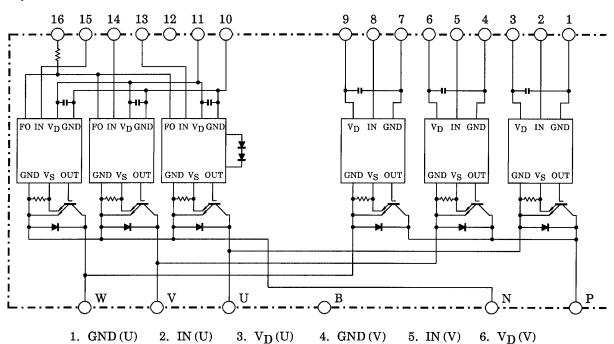
• High speed type IGBT : $V_{CE (sat)} = 2.5 \text{ V (max)}$

 $t_{off} = 3.0 \mu s \text{ (max)}$ $t_{rr} = 0.30 \mu s \text{ (max)}$

• Package dimensions : TOSHIBA 2-110A1A

• Weight: 520 g

Equivalent Circuit



10.GND(L)

16.FO

9. $\widetilde{V_D}(W)$

15.IN(Z)

8. IN(W)

14.IN(Y)

 $11.V_D(L)$

12.0PEN



Maximum Ratings ($T_j = 25$ °C)

Stage	Characteristic	Condition	Symbol	Ratings	Unit
Inverter	Supply voltage	P-N power terminal	Vcc	450	V
	Collector-emitter voltage	_	V _{CES}	600	V
	Collector current	Tc = 25°C, DC	Ic	50	Α
ilivertei	Forward current	Tc = 25°C, DC	IF	50	Α
	Collector power dissipation	Tc = 25°C	PC	150	W
	Junction temperature	_	Tj	150	°C
Control	Control supply voltage	V _D -GND terminal	V_{D}	20	V
	Input voltage	IN-GND terminal	V _{IN}	20	V
	Fault output voltage	FO-GND (L) terminal	V _{FO}	20	V
	Fault output current	FO sink current	I _{FO}	14	mA
	Operating temperature	_	TC	-20 ~ +100	°C
Madula	Storage temperature range	_	T _{stg}	-40 ~ +125	°C
Module	Isolation voltage	AC 1 minute	V _{ISO}	2500	V
	Screw torque	M5	_	3	Nm

Electrical Characteristics $(T_j = 25^{\circ}C)$

a. Inverter Stage

Characteristic	Symbol	Test Condition		Min	Тур.	Max	Unit
Collector cut-off current	lory	$V_{CE} = 600V$ $T_{j} = 25^{\circ}C$ $T_{j} = 125^{\circ}C$	T _j = 25°C	_	_	1	- mA
Collector curent	ICEX		T _j = 125°C	_	_	20	
Collector-emitter saturation voltage	V05 (1)	15 17 10 00 11	T _j = 25°C	_	2.0	2.5	V
Collector-entitler saturation voltage	V _{CE} (sat)		T _j = 125°C	_	2.0	_	
Forward voltage	V _F	I _F = 50A		_	2.1	3.0	٧
	t _{on}	V_{CC} = 300 V, I_{C} = 50 A V_{D} = 15 V, V_{IN} = 15 V \leftrightarrow 0 V Inductive load (Note 1)		1	0.8	2.0	- µs
Switching time	t _{off}			I	1.2	3.0	
Switching time	t _f				0.25	0.5	
	t _{rr}	(Note 1)	_	0.1	0.3		

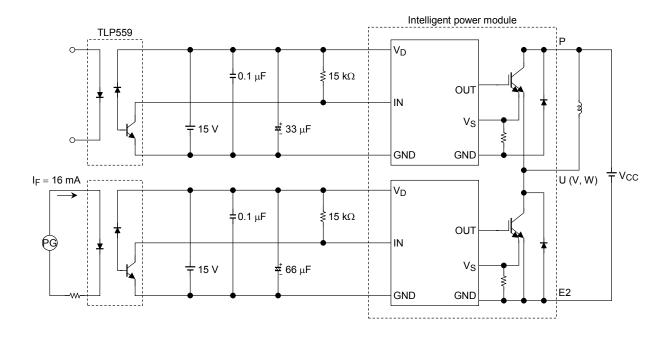
b. Control Stage $(T_j = 25^{\circ}C)$

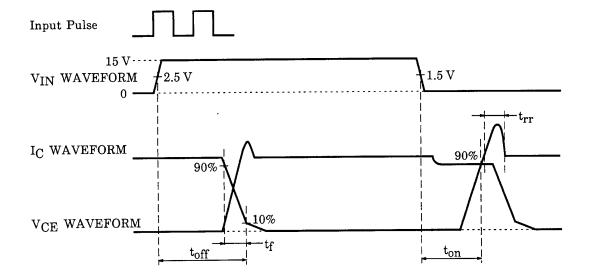
	,						
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Control circuit current	High side	I _{D (H)}	- V _D = 15 V	_	8	_	mA
	Low side	I _{D (L)}		_	24	_	
Input-on signal voltage		V _{IN (on)}	V _D = 15 V, I _C = 50 mA	1.3	1.5	1.7	V
Input-off signal voltage		V _{IN (off)}	V _D = 15 V, I _C = 50 mA	2.2	2.5	2.8	V
Fault output current	Protection	I _{FO (on)}	_	8	10	12	- mA
	Normal	I _{FO (off)}		_	_	1	
Over current protection trip level	Inverter	ОС	V _D = 15 V, T _j = 125°C	75	100	_	А
Short circuit protection trip level	Inverter	ОС	V _D = 15 V, T _j = 125°C	110	150	_	А
Over current cut-off time		t _{off (OC)}	V _D = 15 V	_	5	_	μs
Over temperature protection	Trip level	OT		110	118	125	
	Reset level	OTr	Case temperature	_	98	_	°C
Control supply under voltage protection	Trip level	UV		11.0	12.0	12.5	
	Reset level	UVr] –	_	12.5	_	V
Fault output pulse width		t _{FO}	V _D = 15 V	1	2	3	ms

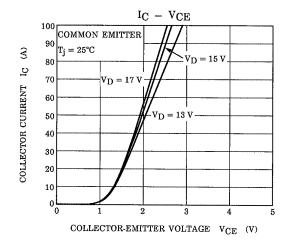
c. Thermal Resistance $(T_j = 25^{\circ}C)$

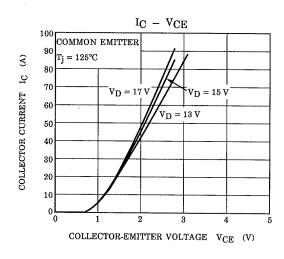
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Junction to case thermal resistance	P., ., .	Inverter IGBT stage	_	_	0.833	°C/W
duriction to case thermal resistance	R _{th (j-c)}	Inverter FRD stage —	_	2.000]	
Case to fin thermal resistance	R _{th (c-f)}	Compound is applied	1	0.05	-	°C/W

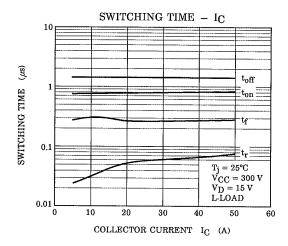
Note 1: Switching time test circuit & timing chart

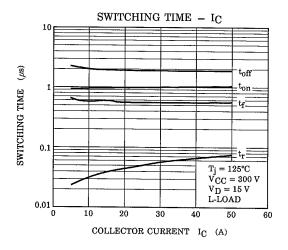


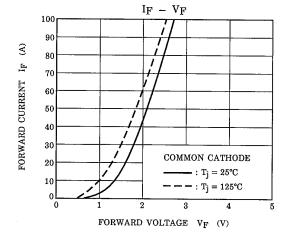


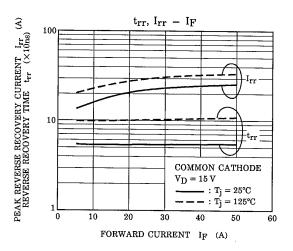


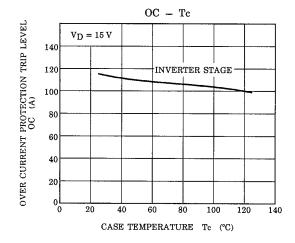


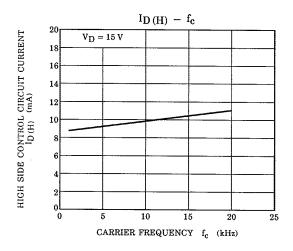


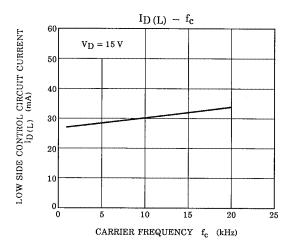


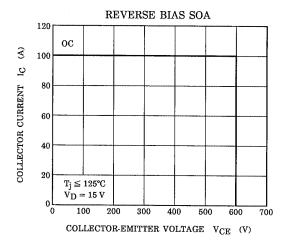


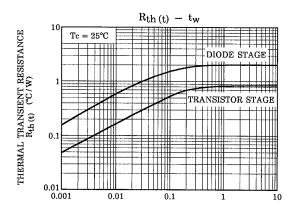






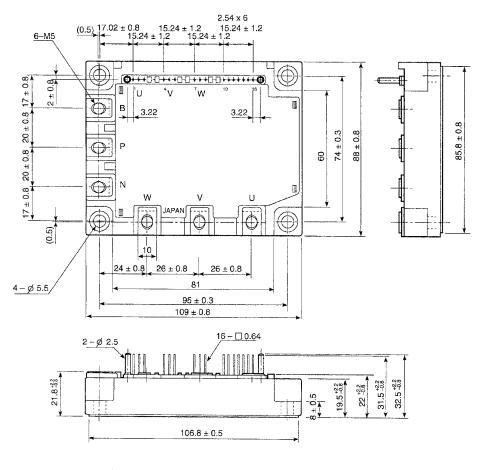






Package Dimensions: TOSHIBA 2-110A1A

Unit: mm



GNDIN VDGNDIN VDGNDIN VDGNDVD IN IN IN IN FO (U) (V) (W) (B) (X) (Y) (Z) , 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 Signal Terminal

RESTRICTIONS ON PRODUCT USE

000707EAA

- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc..
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk.
- The information contained herein is presented only as a guide for the applications of our products. No
 responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other
 rights of the third parties which may result from its use. No license is granted by implication or otherwise under
 any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.