

THYRISTOR MODULE

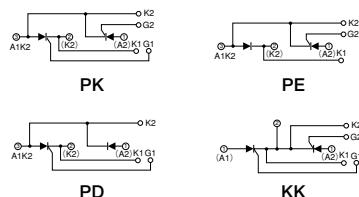
PK(PD,PE,KK)25GB

UL:E76102(M)

Power Thyristor/Diode Module **PK25GB** series are designed for various rectifier circuits and power controls. For your circuit application, following internal connections and wide voltage ratings up to 800V are available. and electrically isolated mounting base make your mechanical design easy.

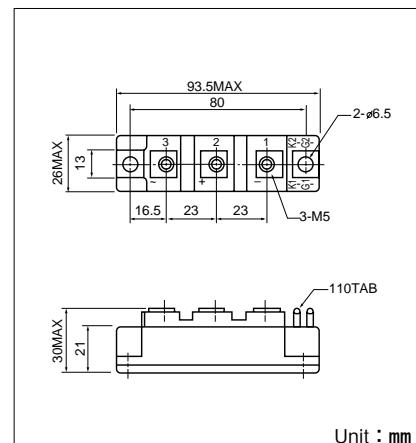
- $I_{T(AV)}$ 25A, $I_{T(RMS)}$ 39A, I_{TSM} 500A
- di/dt 100 A/ μ s
- dv/dt 500V/ μ s

Internal Configurations



(Applications)

Various rectifiers
AC/DC motor drives
Heater controls
Light dimmers
Static switches



■ Maximum Ratings

Symbol	Item	Ratings				Unit
		PK25GB40	PD25GB40	PK25GB80	PD25GB80	
V _{RRM}	* Repetitive Peak Reverse Voltage	400		800		V
V _{RSM}	* Non-Repetitive Peak Reverse Voltage	480		960		V
V _{DRM}	Repetitive Peak Off-State Voltage	400		800		V
Symbol	Item	Conditions			Ratings	Unit
I _{T(AV)}	* Average On-State Current	Single phase, half wave, 180° conduction, T _c : 97°C			25	A
I _{T(RMS)}	* R.M.S. On-State Current	Single phase, half wave, 180° conduction, T _c : 97°C			39	A
I _{TSM}	* Surge On-State Current	1/2cycle, 50Hz/60Hz, peak Value, non-repetitive			450/500	A
I ² t	* I ² t	Value for one cycle of surge current			1000	A ² S
PGM	Peak Gate Power Dissipation				10	W
PG(AV)	Average Gate Power Dissipation				1	W
I _{FGM}	Peak Gate Current				3	A
V _{FGM}	Peak Gate Voltage (Forward)				10	V
V _{RGM}	Peak Gate Voltage (Reverse)				5	V
di/dt	Critical Rate of Rise of On-State Current	I _G =100mA, T _j =25°C, V _D =1/2V _{DRM} , dI _G /dt=0.1A/ μ s			100	A/ μ s
V _{ISO}	* Isolation Breakdown Voltage (R.M.S.)	A.C. 1 minute			2500	V
T _j	* Operating Junction Temperature				-40 to +125	°C
T _{stg}	* Storage Temperature				-40 to +125	°C
Mounting Torque	Mounting (M5)	Recommended Value 2.5-3.9 (25-40)			4.7 (48)	N·m (kgf·cm)
	Terminal (M5)	Recommended Value 1.5-2.5 (15-25)			2.7 (28)	
Mass					170	g

■ Electrical Characteristics

Symbol	Item	Conditions	Ratings	Unit
I _{DRM}	Repetitive Peak Off-State Current, max.	at V _{DRM} , single phase, half wave, T _j =125°C	4	mA
I _{RRM}	* Repetitive Peak Reverse Current, max.	at V _{DRM} , single phase, half wave, T _j =125°C	4	mA
V _{TM}	* Peak On-State Voltage, max.	On-State Current 75A, T _j =125°C Inst. measurement	1.50	V
I _{GT} / _{V_{GT}}	Gate Trigger Current/Voltage, max.	T _j =25°C, I _t =1A, V _D =6V	50/3	mA/V
V _{GD}	Non-Trigger Gate, Voltage. min.	T _j =125°C, V _D =1/2V _{DRM}	0.25	V
t _{gt}	Turn On Time, max.	I _t =25A, I _G =100mA, T _j =25°C, V _D =1/2V _{DRM} , dI _G /dt=0.1A/ μ s	10	μ s
dv/dt	Critical Rate of Rise of Off-State Voltage, min.	T _j =125°C, V _D =2/3V _{DRM} , Exponential wave.	500	V/ μ s
I _H	Holding Current, typ.	T _j =25°C	50	mA
I _L	Latching Current, typ.	T _j =25°C	100	mA
R _{th(j-c)}	* Thermal Impedance, max.	Junction to case	0.80	°C/W

* mark : Thyristor and Diode part. No mark : Thyristor part

