



Vishay General Semiconductor

Surface Mount Glass Passivated Rectifier



DO-214AB (SMC)

PRIMARY CHARACTERISTICS							
I _{F(AV)} 3.0 A							
V _{RRM}	50 V to 1000 V						
I _{FSM}	100 A						
I _R	10 μΑ						
V _F	1.15 V						
T _J max.	150 °C						

FEATURES

- · Low profile package
- · Ideal for automated placement
- · Glass passivated chip junction
- · Low forward voltage drop
- · Low leakage current
- High forward surge capability
- · Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- · AEC-Q101 qualified
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

TYPICAL APPLICATIONS

For use in general purpose rectification of power supplies, inverters, converters and freewheeling diodes for consumer, automotive and telecommunication.

MECHANICAL DATA

Case: DO-214AB (SMC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS compliant, commercial grade Base P/NHE3 - RoHS compliant, AEC-Q101 qualified

Terminals: Matte tin plated leads, solderable per J-STD-002

and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test, HE3 suffix

meets JESD 201 class 2 whisker test

Polarity: Color band denotes cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)									
PARAMETER	SYMBOL	S3A	S3B	S3D	S3G	S3J	S3K	S3M	UNIT
Device marking code		SA	SB	SD	SG	SJ	SK	SM	
Maximum recurrent peak reverse voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V _{RMS}	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum average forward rectified current at T _L = 103 °C	I _{F(AV)}	3.0						Α	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	100					Α		
Operating junction and storage temperature range	T _J , T _{STG}	- 55 to + 150							°C

S3A thru S3M

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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)											
PARAMETER	TEST CONDITIONS		SYMBOL	S3A	S3B	S3D	S3G	S3J	S3K	S3M	UNIT
Maximum instantaneous forward voltage	2.5 A		V _F	1.15					٧		
Maximum DC reverse current at rated		T _A = 25 °C	I_	10							μΑ
DC blocking voltage		T _A = 125 °C	IR	250							
Typical reverse recovery time	$I_F = 0.5$ $I_{rr} = 0.2$	A, I _R = 1.0 A, 5 A	t _{rr} 2.5			μs					
Typical junction capacitance	4.0 V, 1	MHz	CJ	C _J 60					pF		

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)									
PARAMETER	SYMBOL	L S3A S3B S3D S3G S3J S3K S3M					S3M	UNIT	
Typical thermal resistance (1)	$R_{\theta JA}$	47							°C/W
Typical thermal resistance (7)	$R_{ heta JL}$	13						C/VV	

Note

⁽¹⁾ Thermal resistance from junction to ambient and from junction to lead mounted on PCB. with 0.3" x 0.3" (8.0 mm x 8.0 mm) copper pad area

ORDERING INFORMATION (Example)								
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE				
S3J-E3/57T	0.211	57T	850	7" diameter plastic tape and reel				
S3J-E3/9AT	0.211	9AT	3500	13" diameter plastic tape and reel				
S3JHE3/57T (1)	0.211	57T	850	7" diameter plastic tape and reel				
S3JHE3/9AT (1)	0.211	9AT	3500	13" diameter plastic tape and reel				

Note

RATINGS AND CHARACTERISTICS CURVES

(T_A = 25 °C unless otherwise noted)

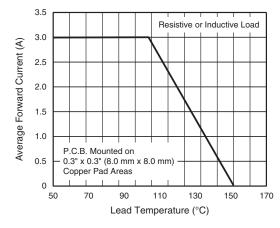


Fig. 1 - Forward Current Derating Curve

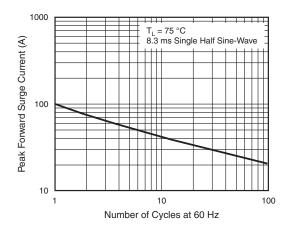


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current

⁽¹⁾ AEC-Q101 qualified



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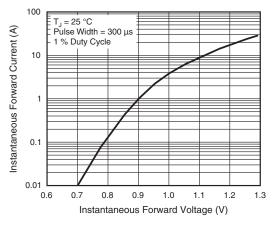


Fig. 3 - Typical Instantaneous Forward Characteristics

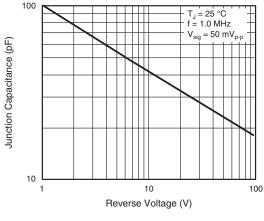


Fig. 5 - Typical Junction Capacitance

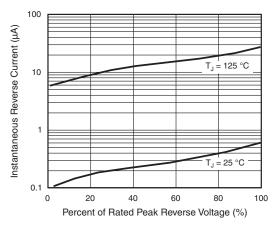


Fig. 4 - Typical Reverse Characteristics

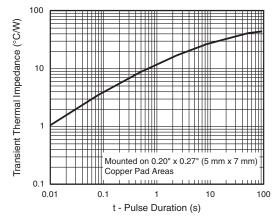
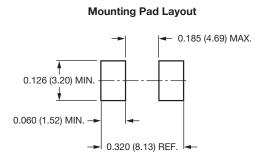


Fig. 6 - Typical Transient Thermal Impedance

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

DO-214AB (SMC) Cathode Band 0.246 (6.22) 0.126 (3.20) 0.220 (5.59) 0.114 (2.90) 0.280 (7.11) 0.260 (6.60) 0.012 (0.305) 0.006 (0.152) 0.103 (2.62) 0.079 (2.06) 0.008 (0.2) 0.060 (1.52) 0.030 (0.76) 0 (0) 0.320 (8.13) 0.305 (7.75)



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