HITACHI

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FOR	MESSRS.	
	IVIECUI VO.	

DATE. Jun.04,2004

CUSTOMER'S ACCEPTANCE SPECIFICATIONS

SP14Q003 CONTENTS

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* When products will be discontinued, customers will be informed by HITACHI with twelve months prior announcement.

PROPOSED BY; Jimmy, HO

KAOHSIUNG HITACHI	Sh.	7B64PS 2701- SP14Q003-3	PAGE	1-1/1
ELECTRONICS CO.,LTD.	No.	10041 0 2101-01 140000-0	I AGE	1-1/1

RECORD OF REVISION

DATE	SHEET No.	SUMMARY									
Mar.12,'04	7B64PS 2708- SP14Q003-2 Page 8-3/3	8.3 TIMING OF POWER SUPPLY AND INTERFACE SIGNAL Added tDLD min. → 50 Revised tCH max. 50 → 30									
Jun.04,'04	7B64PS 2705- SP14Q003-3	5.1 ELECTRICAL CHARACTERISTICS Added									
	Page 5-1/1	ITEM SYMBOL MIN. TYP. MAX									
	Page 5-1/1	Power Supply Voltage Logic VDD-VSS 3.2 3.3 3.4									
		22.5 23.5 24.5									
		Recommend LC Driving Voltage VDD-V0 21.3 22.3 23.3									
		20.6 21.6 22.6									
	7B64PS 2706-	5.2 ELECTRICAL CHARACTERISTICS OF BACKLIGHT Added Note1~4 6.2 OPTICAL CHARACTERISTICS OF BACKLIGHT									
		Added									
	SP14Q003-3 Page 6-2/2	The LCD driving voltage should be adjusted at the voltage where the peak contrast is obtained.									
	7B64PS 2710- SP14Q003-3 Page 10-1/3	10.1 APPEARANCE INSPECTION CONDITION Revised 45°→25°									

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KAOHSIUNG HITACHI	D 4 TF	1 04104	Sh.	7D04D0 0700 0D440000 0	DAGE	0.414
ELECTRONICO CO LER	DATE	Jun.04,'04	N =	7B64PS 2702-SP14Q003-3	PAGE	2-1/1
ELECTRONICS CO.,LTD.			No.			

3. GENERAL SPECIFICATIONS

(1) Part Name SP14Q003

(2) Outer Dimensions 167.0(W)mm x 109.0(H)mm x 10.0(D)mm(max.)

(3) Effective Display Area 120 mm min. x 89 mm min.

(4) Dot Size 0.345(W)min. x 0.345(H)min.

(5) Dot Pitch 0.360(W)mm x 0.360(H)mm

(6) Dot Number (Resolution) 320 (W) x 240 (H)

(7) Duty Ratio 1/240

(8) LCD Type Blue type (negative type)

The upper polarizer is anti-glare type.

The bottom polarizer is transmissive type.

(9) Viewing Direction 6 O'clock

(10) Backlight Type Cold cathode fluorescent lamp.

KAOHSIUNG HITACHI
ELECTRONICS CO.,LTD.

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4. ABSOLUTE MAXIMUM RATINGS

4.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS.

VSS=0V:STANDARD

ITEM	SYMBOL	MIN.	MAX.	UNIT	COMMENT
Power Supply for Logic	VDD-VSS	0	6	V	
Power Supply for LC Driving	VDD-VEE	0	27.5	V	
Input Signal Voltage	Vi	-0.3	VDD+0.3	V	(Note 1)
Input Signal Current	li	0	1	Α	
Static Electricity	-	-	100	_	(Note 2)

Note 1: DOFF, FLM, LOAD, CP, D0~D3.

Note 2: Make certain you are grounded when handling LCM.

4.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS.

ITEM	OPER	OPERATING		RAGE	OMMNT
	MIN.	MAX.	MIN.	MAX.	
Ambient Temperature	0℃	50 ℃	-20 ℃	60℃	(Note 2,3,6)
		(Note 5)			
Humidity	(No			te 1)	Without Condensation
		2.45m/s ²		11.76m/s ²	
Vibration	-	(0.25G)	-	(1.2G)	(Note 4)
				(Note 5)	
		29.4m/s ²		490.0m/s ²	
Shock	-	(3 G)	-	(50 G)	XYZ Directions
				(Note 5)	
Corrosive Gas	Not Acce	ptable	Not Acce	ptable	

Note 1 : Ta ≤ 40°C : 85%RH max.

 $Ta{>}40\%$: Absolute humidity must be lower than the humidity of 85%RH at 40%

Note 2 : Ta at -20° C < 48h, at 60° C < 168h.

Note 3: Background color changes slightly depending on ambient temperature.

The phenomenon is reversible.

Note 4:5Hz~100Hz (Except resonance frequency and X,Y,Z each direction within 1h)

Note 5: This module should be operated normally after finish the test.

Note 6: When LCM will be operated at 0°C, the life time of CFL will be reduced.

Please make sure that characteristics of the inverter meet the CFL specification.

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5. ELECTRICAL CHARACTERISTICS

5.1 ELECTRICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Power Supply Voltage	VDD-VSS	-	4.75	5.0	5.25	V
for Logic			3.2	3.3	3.4	
Power Supply Voltage	VEE-VSS	-	-23.1	-22.0	-20.9	٧
for LC Driving						
Input Signal Voltage	Vi	H LEVEL	0.8VDD	· -	VDD	V
(Note 1)		L LEVEL	0	•	0.2VDD	V
Power Supply Current	IDD	VDD-VSS=5.0V	-	6.0	-	mΑ
for Logic (Note 2)		VEE-VSS= -22.0V				
Power Supply Voltage	IEE	VDD-VSS=5.0V	_	5.0	-	mA
for LC Driving (Note 2)		VEE-VSS= -22.0V				
Recommended LC		Ta= 0°C , φ= 0°	22.5	23.5	24.5	V
Driving Voltage	VDD-V0	Ta=25°C , <i>φ</i> = 0°	21.3	22.3	23.3	V
(Note 3)		Ta=50°C , <i>φ</i> = 0°	20.6	21.6	22.6	V
Frame Frequency (Note 4)	fFLM	-	70	75	80	Hz

Note 1: DOFF, FLM, LOAD, CP, D0~D3.

Note 2: FLM=75Hz, test pattern is all "Q".

VDD-V0=22.3V , Ta=25℃

Note 3 : Recommended LC driving voltage may fluctuate about ±1.0V by each module. Test pattern is all "Q"

Note 4: Please set the frame frequency so as to avoid flicker and rippling on the display.

5.2 ELECTRICAL CHARACTERISTICS OF BACKLIGHT

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTE
Lamp Voltage	VL	-	300	-	Vrms	T a=25 ℃
Frequency	FL	-	70	85	kHz	Ta =25 ℃
Lamp Current	IL	4	5	6	mArms	Ta=25°C
Starting Discharge Voltage	vs	(1000)	-	-	Vrms	Ta=25℃

- Note 1: Please make sure that your inverter is designed to meet the above specifications.
- Note 2: Starting discharge voltage is increased when LCM is operating at lower temperature, please check the characteristics of your inverter, so as to ensure discharge at low temperature.
- Note 3 :Average life time of CFL will be decreased when LCM is operating at lower temperature.
- Note 4: Lower driving frequency of CFL inverter may cause mechanical noise of the backlight system.

 Before designing the inverter, please consider the driving frequency of noise.

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6. OPTICAL CHARACTERISTICS

6.1 OPTICAL CHARACTERISTICS

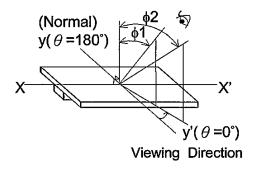
Ta=25°C (BACKLIGHT ON)

ITEM	SYMBOL	CONDITIONAL	MIN.	TYP.	MAX.	UNIT	NOTE
Viewing Area	φ2-φ1	K>=2.0	-	40	-	deg	1,2
Contrast Ratio	K	ϕ =0° , θ =0°	-	6	- ;	-	3
Response Time (Rise)	tr	φ=0°, θ=0°	-	120	-	ms	4
Response Time (Fall)	tf	φ=0°, θ=0°	-	150	-	ms	4

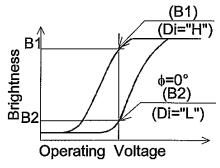
(Measure condition by HITACHI)

Note 3: Definition of contrast "K"

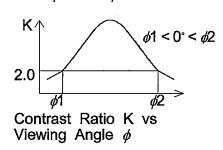
Note 1 : Definition of θ and ϕ

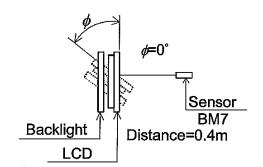


K= Brightness on Selected Dot (B1)
Brightness on Non-Selected Dot (B2)

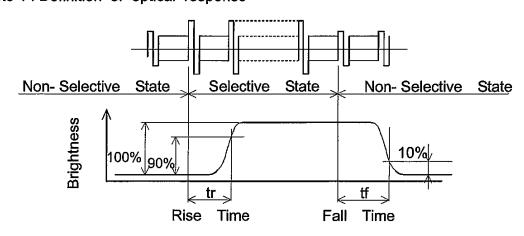


Note 2 : Definition of viewing angle $\phi 1$ and $\phi 2$.





Note 4: Definition of optical response



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ELECTRONICS CO.,LTD.

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6.2 OPTICAL CHARACTERISTICS OF BACKLIGHT

ITEM	MIN.	TYP.	MAX.	UNIT	NOTE
Brightness	_	100	-	cd/m²	IL=5mA
		100			(Note 1,2)
Rise Time	-	5	-	minute	lL=5mA
					Brightness 80%
Brightness Uniformity	-	-	±30	%	(Note 1,3)

CFL: Initial, Ta=25℃

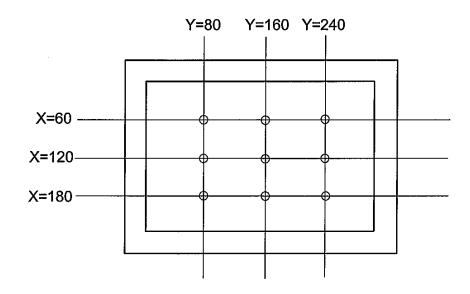
Display data should be all "ON".

The LCD driving voltage should be adjusted at the voltage where the peak contrast is obtained.

Note 1: Measurement after 10 minutes of CFL operating.

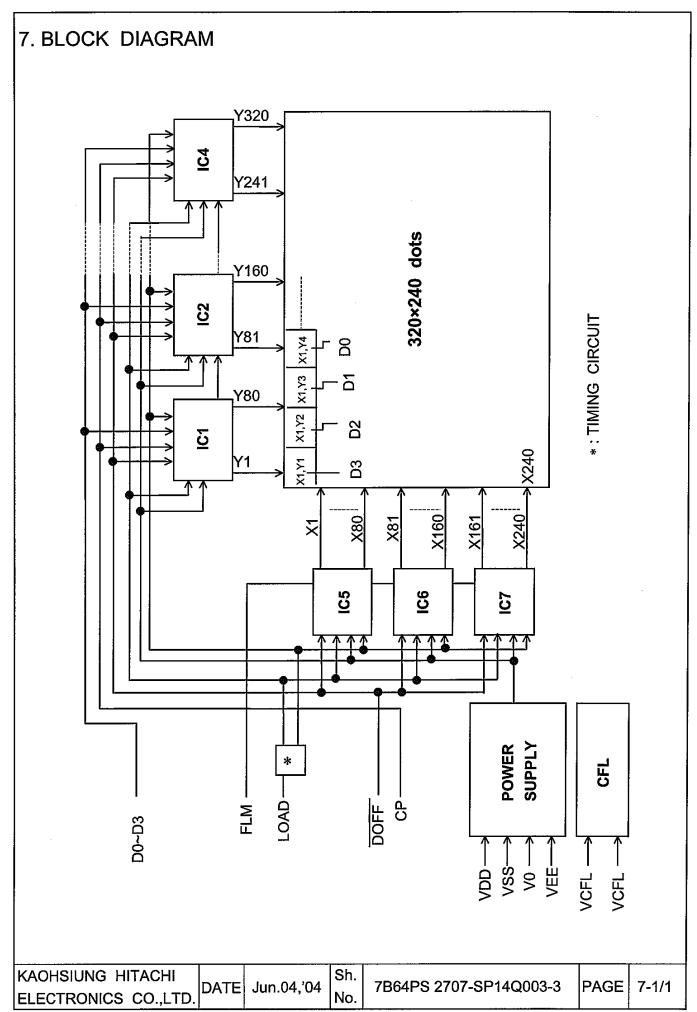
Note 2: Brightness control: 100%

Note 3: Measure of the following 9 places on the display. Definition of the brightness tolerance.



Definition of the brightness tolerance.

KAOHSIUNG HITACHI	D 4 TE	In 0.4.10.4	Sh.	7DC4DC 070C CD440002 2	DAGE	0.00
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8. INTERFACE TIMING CHART 8.1 INTERFACE TIMING CHART $52.1\mu S \le T \le 59.5\mu S$ LOAD CP X1 X240. D3 Y1 X Y5 $\overline{Y2} \times \overline{Y6}$ D2 D1 $\langle \overline{Y4} \rangle \langle \overline{Y8} \rangle$ D0 FLM LOAD 240×T FLM $X1 \times X2$ D0~D3 $\langle x_{239} \rangle x_{240} \rangle$

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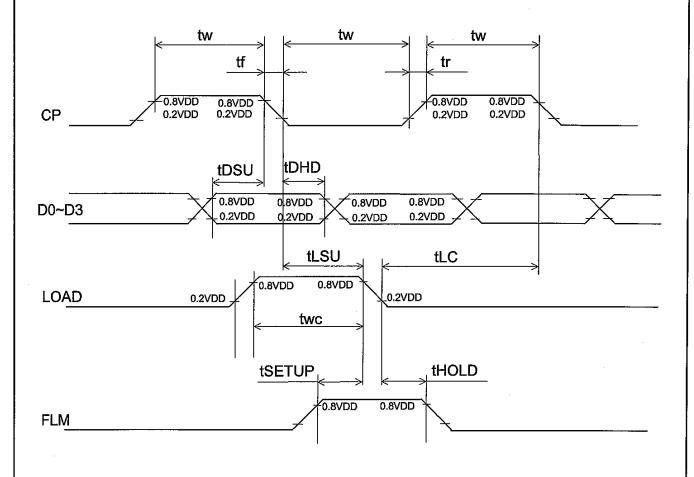
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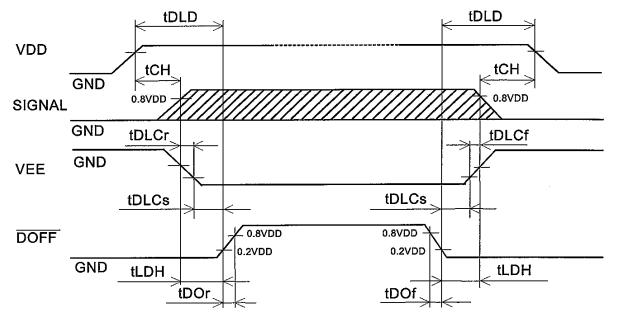
8.2 TIMING CHARACTERISTICS

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
Clock Frequency	fCP		-	6.5	MHz
Clock Pulse Width	tW	63	-	-	ns
Clock Rise, Fall Time	tr,tf	-	-	20	ns
Data Set Up Time	tDSU	50	ı	•	ns
Data Hold Time	tDHD	50	1	ı	ns
Load Set Up Time	tLSU	80	ı	-	ns
Load Clock Time	tLC	80	-	-	ns
"Frame" Set Up Time	tSETUP	100	1	•	ns
"Frame" Hold Time	tHOLD	100	1	1	ns
"Load" Pulse Width	tWC	125		-	ns



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8.3 POWER ON/OFF TIMING SEQUENCE



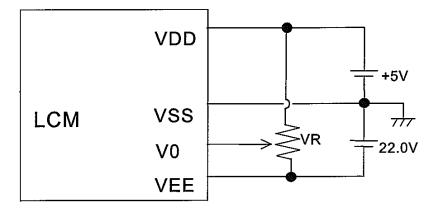
SYMBOL	MIN.	MAX.	UNIT	COMMENT
tDLD	50	-	ms	
tCH	0	30	ms	(Note 1)
tLDH	0	-	ms	
tDOr	-	100	ns	
tDOf	-	100	ns	
tDLCr	0	_	ms	(Note 2)
tDLCf	0	-	ms	
tDLCs	20	-	ms	

Note 1 Please keep the specified sequence because wrong sequence may cause permanent damage to the LCD panel.

Note 2 HITACHI recommends you to use DOFF function.

Display quality may deteriorate if you don't use DOFF function.

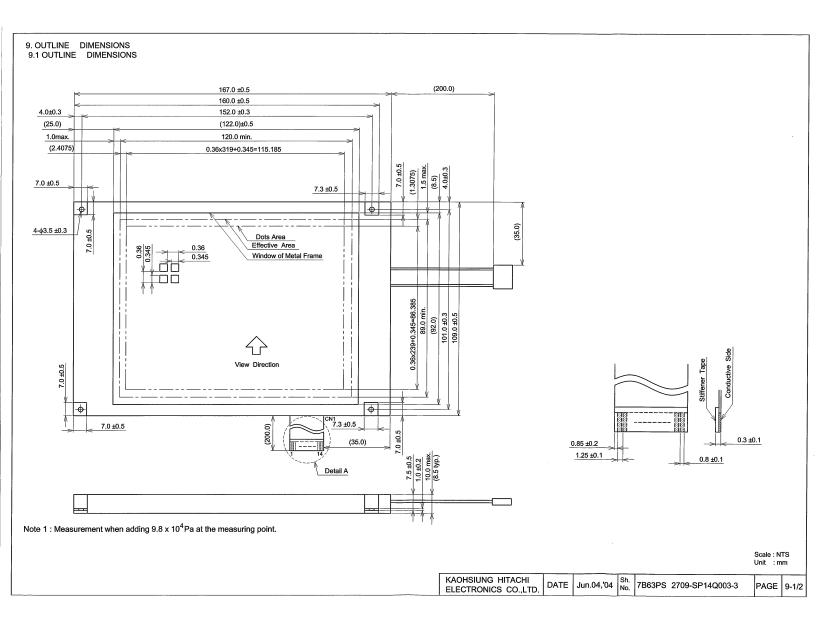
8.4 POWER SUPPLY FOR LCM



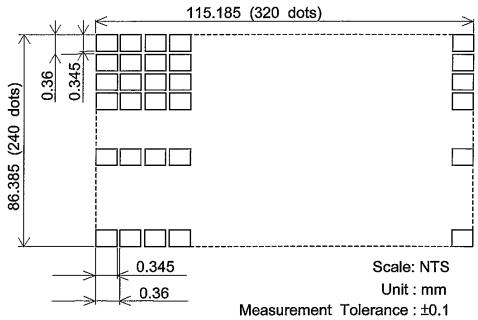
Note 1: VR: 10kOHM

Note 2: We recommend to ADD fuse (1A) to VDD line.

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9.2 DISPLAY PATTERN



9.3 INTERFACE PIN CONNECTION

FFC: PITCH 1.25mm 14 PINS

INTER	FACE	PIN No.	SIGNAL	LEVEL	FUNCTION
LCM	CN1	1	D0	H/L	Display data
		2	D1		
		3	D2		
		4	D3		
		5	DOFF	H/L	H:ON / L:OFF
		6	FLM	Н	First line marker
		7	N.C	•	-
		8	LOAD	H→L	Data latch
		9	CP	H→L	Data shift
		10	VDD	_	Power supply for logic
		11	VSS	-	GND
		12	VEE	-	Power supply for LC
		13	V0	-	Operating voltage LC driving
		14	VSS .	-	GND

RECOMMEND SUITABLE CONNECTOR: (MOLEX) 5597-14APB

INTER	RFACE	PIN No.	SIGNAL	LEVEL	FUNCTION
CFL	CN2	1	VCFL	-	Power supply for CFL
	[2	N.C	-	
		3	N.C	-	-
		4	VCFL	-	CFL GND

CFL I/F: J.A.E./ IL - G - 4S - S3C2

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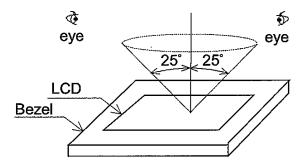
10. APPEARANCE STANDARD

10.1 APPEARANCE INSPECTION CONDITIONS

Visual inspection should be done under the following condition.

- (1) The inspection should be done under in the dark room.
- (2) The CFL should be lighted with the prescribed inverter.
- (3) The distance between eyes of an inspector and the LCD module is 25cm.
- (4) The viewing zone is shown the figure.

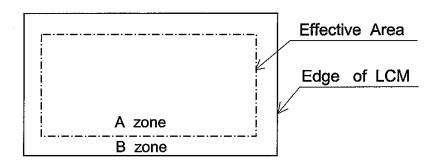
Viewing angle ≤25°



10.2 DEFINITION OF EACH ZONE

A zone: Within the viewing area specified at page 9-1/2 of this document.

B zone: Area between the edge line of LCD glass and the viewing area line specified at page 9-1/2 of this document.



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10.3 APPEARANCE SPECIFICATION

*) If a problem occurs in respect to any of these items, both parties (Customer and HITACHI) will discuss in more detail.

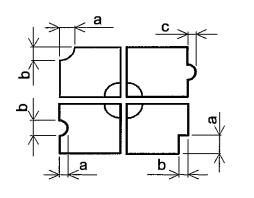
No.	ITEM		CF	RITE	RIA			Α	В
	Scratches	Distinguished on						*	-
1		(to be judged b	y HITACI	<u> 11 lii</u>	mit samp	ole)			
	Dent	Same as Above						*	_
	Wrinkles in Polarizer	Same as Above)					*	-
	Bubbles	Average Di					n Number		
		D(mm				-	otable		
	Wrinkles in Polarizer		0.2			lgn			
		0.2 <d≦< td=""><td></td><td></td><td></td><td></td><td>2</td><td>O</td><td> -</td></d≦<>					2	O	-
		0.3 <d≦< td=""><td></td><td></td><td></td><td>3</td><td></td><td>ļ</td><td></td></d≦<>				3		ļ	
		0.5 <d< td=""><td colspan="3"></td><td>No</td><td>ne</td><td></td><td></td></d<>				No	ne		
	Stains,		Filament				<u> </u>		
	Foreign	LENGTH		DTH			mum Number	0	-
	Materials,	L(mm)		mm)		. A	cceptable	ł	
	Dark Spot	L≦2.0		<u>≤0.</u> 0			Ignore	-	
ΙL		L≦3.0	0.03 < V				<u>6</u> 1	<u> </u>	
_		L <u>≥ 2.5</u>	L≦2.5 0.05 <w≦0.1 <br="">Round</w≦0.1>						
		Average Diamet			Number Minimum				
	•	D(mm)			table		Space		
C		D<.2		Ignore			-		_
		0.2 ≦D<0.33		8			10mm		
		0.33≦D		None			-		
		The whole number	ber Filam			Round = 10			
ן ר		Those wiped ou						0	0
	Color Tone	To be judged by						0	-
	Color Uniformity	Same as Above			•			0	-
	Pinhole	Average Di	ameter		Maxi	mum	Number		
		D(mm			P	Ассер	otable		
		D ≦ 0.		_		Igno	· · · · · · · · · · · · · · · · · · ·		
		0.15 <d≦0< td=""><td>).3</td><td></td><td>***</td><td>10</td><td>0</td><td></td><td></td></d≦0<>).3		***	10	0		
		D≦0.	015			Igno	ore		
	Contrast	Average	Contrast	:	Maximu	4	Minimum	О	-
	Irregularity	Diameter			Numbe		Space		
	(Spot)	D(mm)		-	Accepta				
		D≦0.25	To be	-	Ignore	•	-		
1			judged by	_	10		20mm		
		0.35 <d≦0.5< td=""><td>HITACHI</td><td>\ </td><td>4</td><td></td><td>20mm</td><td></td><td></td></d≦0.5<>	HITACHI	\ 	4		20mm		
		0.5 < D			None				

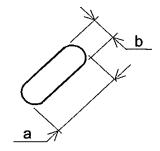
KAOHSIUNG HITACHI DATE Jun.04,'04 Sh. No.	7B64PS 2710-SP14Q003-3	PAGE	10-2/3

No.	ITEM		CRITERIA						
	Contrast Irregularity (Line)	Width D(mm)	Length L(mm)	Maximum Number Acceptable	Minimum Space				
L	(Filamentous)	W≦0.25	L≦1.2	2	20mm] _			
С		W≦0.2	L≦1.5	3	20mm	0	[-		
D		W≦0.15	L≦2.0	3	20mm				
		W≦0.1	L≦3.0	4	20mm				
		To	tal	6					
	Rubbing Scratch	To be judged	by HITACHI	standard		О			

No.	ITEM		ERIA	
С	Dark Spots, White Spots	D≦	0.4	Ignore
F	Foreign Materials (Spot)	D>	0.4	None
L		W≦0.2	L<2.5	<u>≦</u> 1
	Foreign Materials (Line)	W≦0.2	L>2.5	None
B		W>	0.2	None
/		W≦	0.1	lgnore
L Scratches	Caratabaa	0.1 <w≦0.2< td=""><td>L≦11.0</td><td><u>≦</u>1</td></w≦0.2<>	L≦11.0	<u>≦</u> 1
	Scratches	0.1 <w≦0.2< td=""><td>L≧11.0</td><td>None</td></w≦0.2<>	L≧11.0	None
		W>	0.2	None

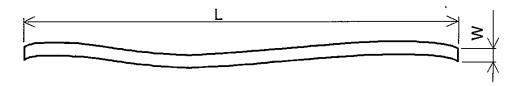
Note 1:





a+b =D Average diameter c Salient

Note 2 : Definition of length L and width W



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11. PRECAUTION IN DESIGN

11.1 LC DRIVING VOLTAGE (VEE) AND VIEWING ANGLE RANGE.

Setting VEE out of the recommended condition will be a cause for a change of viewing angle range.

11.2 PRECAUTIONS AGAINST STATIC CHARGE

As this module contains C-MOS LSIs, it is not strong against electrostatic discharge. Make certain that the operator's body is connected to the ground through a list band etc. And don't touch I/F pins directly.

11.3 POWER ON SEQUENCE

Input signals should not be applied to LCD module before power supply voltage is applied and reaches to specified voltage (VDD).

If above sequence is not kept, C-MOS LSIs of LCD modules may be damaged due to latch up problem.

11.4 PACKAGING

- (1) No. leaving product is preferable in the place of high humidity for a long period of time. For their storage in the place where temperature is 35 ℃ or higher, special care to prevent them from high humidity is required. A combination of high temperature and high humidity may cause them polarization degradation as well as bubble generation and polarizer peel-off. Please keep the temperature and humidity within the specified range for use and storage.
- (2) Since polarizers tend to be easily damaged, they should be handled full with care so as not to get them touched, pushed or rubbed.
- (3) As the adhesives used for adhering polarizers are made of organic substances which will be deteriorated by a chemical reaction with such chemicals as acetone, toluene, ethanol and isopropyl alcohol. The following solvents are recommended for use: normal hexane

Please contact us when it is necessary for you to use chemicals.

- (4) Lightly wipe to clean the dirty surface with absorbent cotton waste or other soft material like chamois, soaked in the chemicals recommended without scrubbing it hardly. To prevent the display surface from damage and keep the appearance in good state, it is sufficient, in general, to wipe it with absorbent cotton.
- (5) Immediately wipe off saliva or water drop attached on the display area because its long period adherence may cause deformation or faded color on the spot.
- (6) Foggy dew deposited on the surface due to coldness will be caused for polarizer damage, stain and dirt on product. When necessary to take out the products from some place at low temperature for test, etc. It is required for them to be warmed up in a container once at the temperature higher than that of room.
- (7) Touching the display area and contact terminals with bare hands and contaminating them are prohibited, because the stain on the display area and poor insulation between terminals are often caused by being touched by bare hands. (Some cosmetics are detrimental to polarizers.)

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(8) In general the quality of glass is fragile so that it tends periphery. Be careful not to give it sharp shock caused by dropping down, etc.

11.5 CAUTION FOR OPAERATION

- (1) It is an indispensable condition to drive LCDs within the specified voltage limit since the higher voltage than the limit causes the shorter LCD life. An electrochemical reaction due to direct current causes LCDs undesirable deterioration, so that the use of direct current driver should be avoided.
- (2) Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCDs show dark blue color in them. However those phenomena do not mean malfunction or out of order with LCDs which will come back in the specified operating temperature range.
- (3) If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- (4) A slight dew depositing on terminals is a cause for electrochemical reaction resulting in terminal open circuit. Usage under the relative condition of 40℃ 50%RH or less is required.

11.6 STORAGE

In case of storing for a long period of time (for instance, for years) for the purpose of replacement use, the following ways area recommended.

- (1) Storage in a polyethylene bag with the opening sealed, so the fresh air will not be entered from outside.
- (2) Placing in a dark place where neither exposure to direct sunlight nor light is , keeping temperature in the range from 0 $^{\circ}$ C to 35 $^{\circ}$ C.
- (3) Storing with no touch on polarizer surface by anything else. (It is recommended to store them as they have been contained in the inner container at the time of delivery from us.)

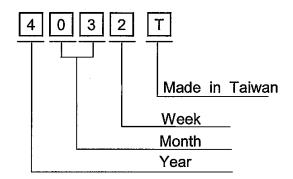
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11.7 SAFETY (1) It is recommendable to crash damaged or unnecessary LCDs into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
(2) When any liquid leaked out of a damaged glass cell comes in contact with your hands , please wash it off well with soap and water.
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12. DESIGNATION OF LOT MARK

LOT MARK

Lot mark is consisted of 4 digital number.



YEAR	FIGURE IN
	LOT MARK
2004	4
2005	5
2006	6
2007	7
2008	8

Note 1: Some products have alphabet at the end or the first.

	FIGURE IN		FIGURE IN
монтн	LOT MARK	MONTH	LOT MARK
Jan.	01	Jul.	07
Feb.	02	Aug.	08
Mar.	03	Sep.	09
Apr.	04	Oct.	10
May	05	Nov.	11
Jun.	06	Dec.	12

WEEK	FIGURE IN
(DAY IN	LOT MARK
CALENDAR	
01~07	1
08~14	2
15~21	3
22~28	4
29~31	5

Location of lot mark: On the back side of LCM

4032T

13. PRECAUTION FOR USE

- 13.1 A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.
- 13.2 On the following occasions, the handling of the problem should be decided through discussion and agreement between responsible persons of the both parties.
 - 1) When a question is arisen in the specifications.
 - 2) When a new problem is arisen which is not specified in this specifications.
 - When an inspection specifications change or operating condition change in customer is reported to HITACHI, and some problem is arisen in this specification due to the change.
 - 4) When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

The precaution that should be observed when handling LCM have been explained above. If any points are unclear or if you have any request, please contact HITACHI.

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