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75K72234

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Description:

18M Network Search Engine (NSE) with Dual LA-1 (QDR) Interfaces

Additional Information:

LA-1 (QDR) clock rates up to 250MHz, simultaneous multi-database lookup, control plane maintenance port with a PCI interface, advanced power management and instruction completion notification. It is offered in a 900 BGA package (35mm x 35mm).

Related Documents[Product Brief](#)[White Paper](#)[Brochure](#)**Orderable Parts Parametric Table:**

Orderable Part	Package	Speed	Voltage	Temperature	Status	Sample
75K72234S167BL	FCBGA 900	167 MHz	1.2 V	C	New	Y
75K72234S200BL	FCBGA 900	200 MHz	1.2 V	C	New	Y
75K72234S250BL	FCBGA 900	250 MHz	1.2 V	C	New	Y

Related Documents:

Document Type	Document Title	Size	Revision Date
Product Brief	18M NSE with Dual LA-1 Interface	422 KB	05/28/2004
	NSE Development Board for IXDP2401/2801/2851	54 KB	10/01/2003
	NSE Development Board for IXDP2400	55 KB	05/01/2003
White Paper	LA-1: Look-Aside Processor Interface and Applications	52 KB	04/24/2004
	Optimum Search Methods for Switch/Router Databases	507 KB	02/06/2003
	Overview of Packet Processing	210 KB	01/27/2003
	Advanced Database Management	242 KB	01/27/2003
	Network Processors Heightens Requirements	113 KB	01/09/2003
Brochure	Design High-Speed Intelligent Packet-Processing	64 KB	10/03/2002
	Networking Search Engines Flyer	249 KB	11/04/2002

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