Vishay High Power Products

Schottky Rectifier, 1.0 A

FEATURES

- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

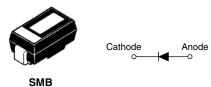
DESCRIPTION

The 10BQ060PbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	1.0	A	
V _{RRM}		60	V	
I _{FSM}	$t_p = 5 \ \mu s \ sine$	700	A	
V _F	1.0 Apk, T _J = 125 °C	0.57	V	
TJ	Range	- 55 to 150	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	10BQ060PbF	UNITS	
Maximum DC reverse voltage	V _R	60	V	
Maximum working peak reverse voltage	V _{RWM}	80	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	$I_{F(AV)}$ 50 % duty cycle at T _L = 103 °C, rectangular waveform		1.0	А
Maximum peak one cycle	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	700	A
non-repetitive surge current		10 ms sine or 6 ms rect. pulse		42	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 4 mH		2.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _B typical		1.0	А



1.0 A

60 V

PRODUCT SUMMARY

I_{F(AV)}

 V_R





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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	1 A	• T _J = 25 °C	0.6	v
Maximum forward voltage drop		2 A		0.76	
See fig. 1		1 A	T _J = 125 °C	0.57	
		2 A		0.69	
Maximum reverse leakage current See fig. 2	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_R = Rated V_R$	0.1	mA
		T _J = 125 °C		5.0	
Typical junction capacitance	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		62	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of charge	dV/dt	Rated V _R		10 000	V/µs

Note

⁽¹⁾ Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC operation	36	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}		80	°C/W
Approvimeto weight			0.10	g
Approximate weight			0.003	oz.
Marking device		Case style SMB (similar DO-214AA)	V1H	

Notes

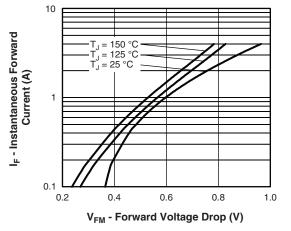
 $^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$

⁽²⁾ Mounted 1" square PCB



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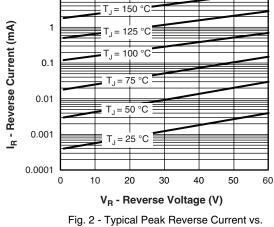
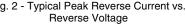


Fig. 1 - Maximum Forward Voltage Drop Characteristics Fig. 2



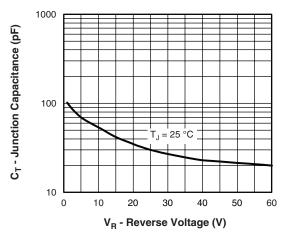


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

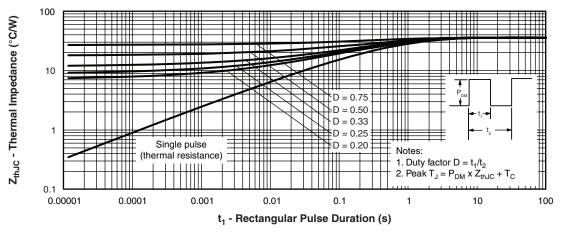


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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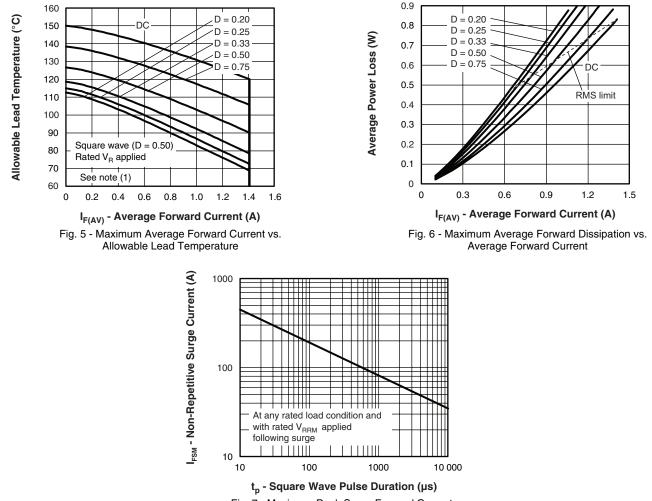


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

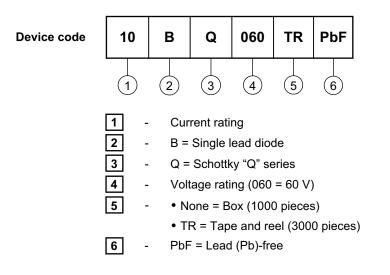
Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 D)$; $I_R at V_{R1} = 80 \%$ rated V_R



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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95017		
Part marking information	www.vishay.com/doc?95029		
Packaging information	www.vishay.com/doc?95034		



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