# DATA SHEET

# PHOTOCOUPLER PS2532-1,PS2532L-1

# HIGH COLLECTOR TO EMITTER VOLTATGE HIGH ISOLATION VOLTAGE MULTI PHOTOCOUPLER SIRIES

-NEPOC Series-

#### **\*** DESCRIPTION

NEC

The PS2532-1 and PS2532L-1 are optically coupled isolators containing a GaAs light emitting diode and an NPN silicon darlington connected phototransistor.

The PS2532-1 is in a plastic DIP (Dual In-line Package) and the PS2532L-1 is lead bending type (Gull-wing) for surface mount.

#### FEATURES

- High collector to emitter voltage (VCEO = 300 V)
- High Isolation voltage (BV = 5 000 Vr.m.s.)
- High current transfer ratio (CTR = 4 000% TYP.)
- High-speed switching (tr, tr = 100  $\mu$ s TYP.)
- Ordering number of tape product: PS2532L-1-E3, E4, F3, F4
- ★ Safety standards
  - UL approved: File No. E72422
  - BSI approved: No. 8221/8222
  - CSA approved: No. CA 101391
  - NEMKO approved: No. P98101708
  - SEMKO approved: No. 0143092/01-02
  - DEMKO approved: No. 307863
  - FIMKO approved: No. FI 17640
  - DIN EN60747-5-2 (VDE0884 Part2) approved (Option)

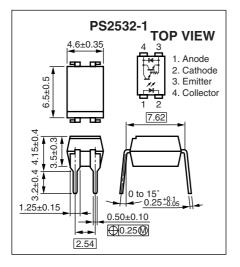
#### **APPLICATIONS**

- Telephone, Exchange equipment
- FAX/MODEM

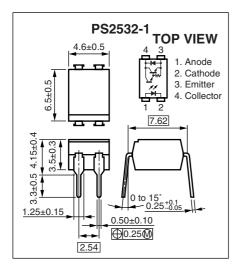
The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version. Not all devices/types available in every country. Please check with local NEC Compound Semiconductor Devices representative for availability and additional information.

\* PACKAGE DIMENSIONS (Unit : mm)

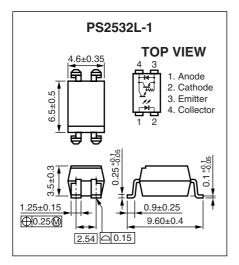
## DIP Type (New package)



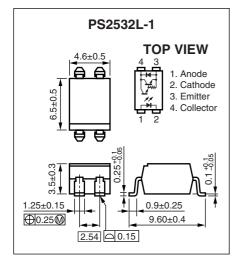
#### **DIP Type**



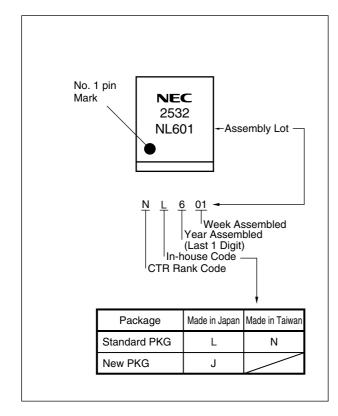
#### Lead Bending Type (New package)



#### Lead Bending Type



#### **\*** MARKING EXAMPLE



#### ★ ORDERING INFORMATION

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number <sup>*1</sup>
PS2532-1	PS2532-1-A	Pb-Free	Magazine case 100 pcs	Standard products	PS2532-1
PS2532L-1	PS2532L-1-A			(UL, CSA, BSI,	
PS2532L-1-E3	PS2532L-1-E3-A		Embossed Tape 1 000 pcs/reel	NEMKO, SEMKO,	
PS2532L-1-E4	PS2532L-1-E4-A			DEMKO, FIMKO	
PS2532L-1-F3	PS2532L-1-F3-A		Embossed Tape 2 000 pcs/reel	approved)	
PS2532L-1-F4	PS2532L-1-F4-A				
PS2532-1-V	PS2532-1-V-A		Magazine case 100 pcs	DIN EN60747-5-2	
PS2532L-1-V	PS2532L-1-V-A			(VDE0884 Part2)	
PS2532L-1-V-E3	PS2532L-1-V-E3-A		Embossed Tape 1 000 pcs/reel	Approved (Option)	
PS2532L-1-V-E4	PS2532L-1-V-E4-A				
PS2532L-1-V-F3	PS2532L-1-V-F3-A		Embossed Tape 2 000 pcs/reel		
PS2532L-1-V-F4	PS2532L-1-V-F4-A				

\*1 For the application of the Safety Standard, following part number should be used.

	Parameter	Symbol	Ratings	Unit
Diode	Forward Current (DC)	lF	80	mA
	Reverse Voltage	VR	6	V
	Power Dissipation Derating	⊿P₀/°C	1.5	mW/°C
	Power Dissipation	PD	150	mW
	Peak Forward Current <sup>™</sup>	IFP	1	А
Transistor	Collector to Emitter Voltage	Vceo	300	V
	Emitter to Collector Voltage	VECO	0.6	V
	Collector Current	lc	150	mA
	Power Dissipation Derating	⊿Pc/°C	3.0	mW/°C
	Power Dissipation	Pc	300	mW
Isolation Voltage <sup>*2</sup>		BV	5 000	Vr.m.s
Operating Ambient Temperature		TA	-55 to +100	°C
				_

#### **★** ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25 °C, unless otherwise specified)

\*1 PW = 100  $\mu$ s, Duty Cycle = 1%

Storage Temperature

\*2 AC voltage for 1 minute at  $T_A = 25^{\circ}C$ , RH = 60% between input and output Pins 1-2 shorted together, 3-4 shorted together.

Tstg

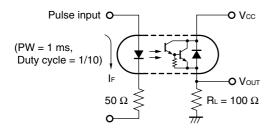
-55 to +150

°C

	Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Diode	Forward Voltage	VF	IF = 10 mA		1.15	1.40	V
	Reverse Current	IR	$V_{R} = 5 V$			5	μA
	Terminal Capacitance	Ct	V = 0 V, f = 1.0 MHz		30		pF
Transistor	Collector to Emitter Dark Current	ICEO	Vce = 300 V, IF = 0 mA			400	nA
Coupled	Current Transfer Ratio (Ic/IF)	CTR	IF = 1 mA, VCE = 2 V	1 500	4 000	6 500	%
	Collector Saturation Voltage	V <sub>CE(sat)</sub>	I⊧ = 1 mA, Ic = 2 mA			1.0	V
	Isolation Resistance	Ri-o	VI-O = 1.0 kVDC	10 <sup>11</sup>			Ω
	Isolation Capacitance	CI-0	V = 0 V, f = 1.0 MHz		0.6		pF
	Rise Time <sup>*1</sup>	tr	Vcc = 5 V, Ic = 10 mA, R∟ = 100 Ω		100		μs
	Fall Time <sup>*1</sup>	tr			100		

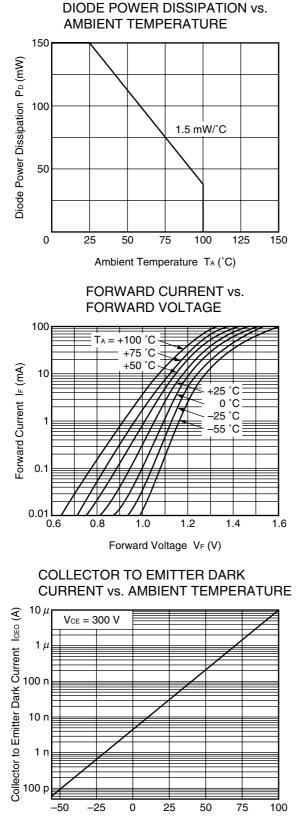
# ELECTRICAL CHARACTERISTICS (TA = 25°C)

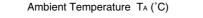
\*1 Test circuit for switching time

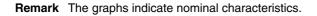




### ★ TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise specified)



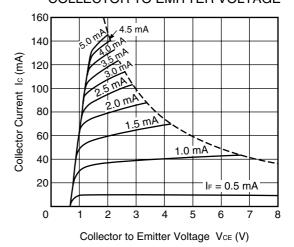




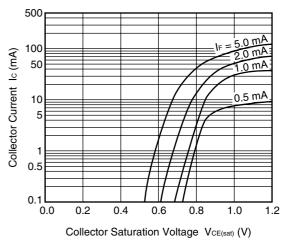
Data Sheet PN10232EJ02V0DS

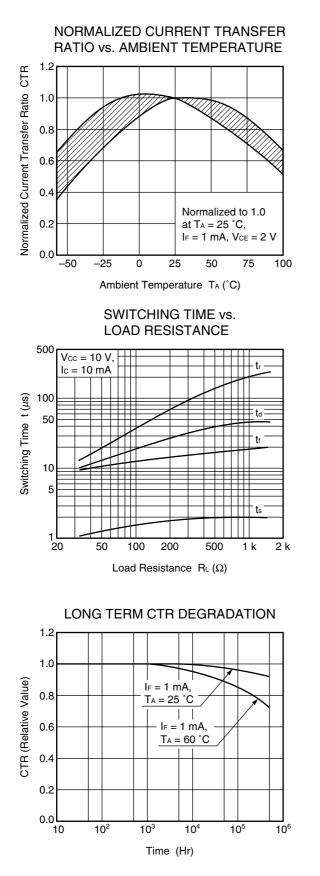
TRANSISTOR POWER DISSIPATION vs. AMBIENT TEMPERATURE 400 Transistor Power Dissipation Pc (mW) 300 200 3.0 mW/°C 100 0 25 75 100 125 150 50 Ambient Temperature T<sub>A</sub> (°C)

COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE

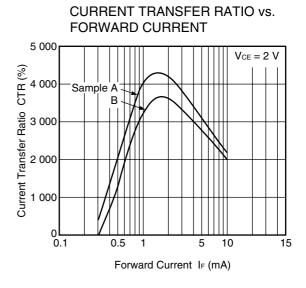


COLLECTOR CURRENT vs. COLLECTOR SATURATION VOLTAGE

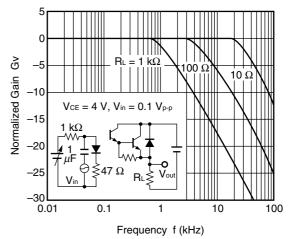




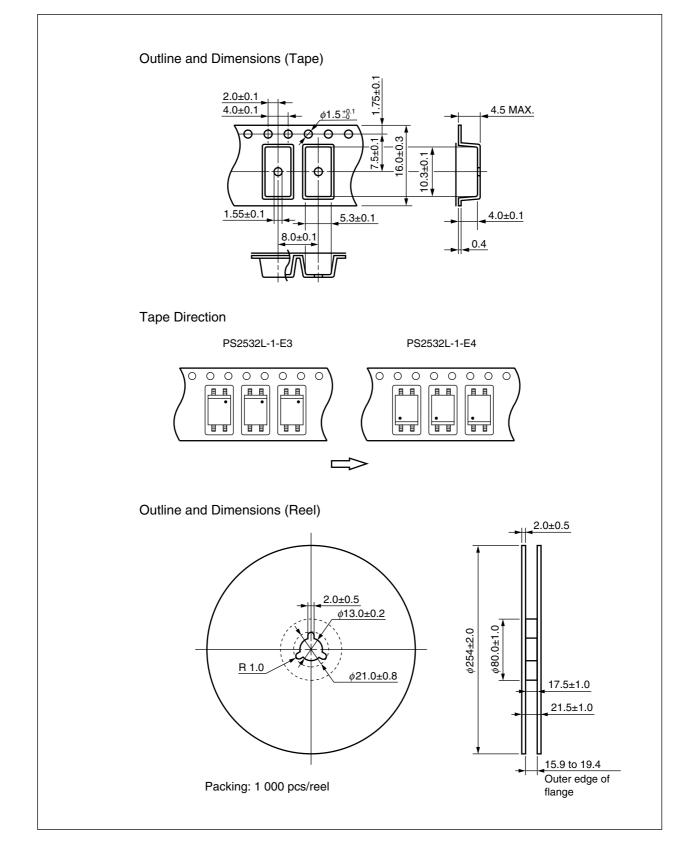
Remark The graphs indicate nominal characteristics.



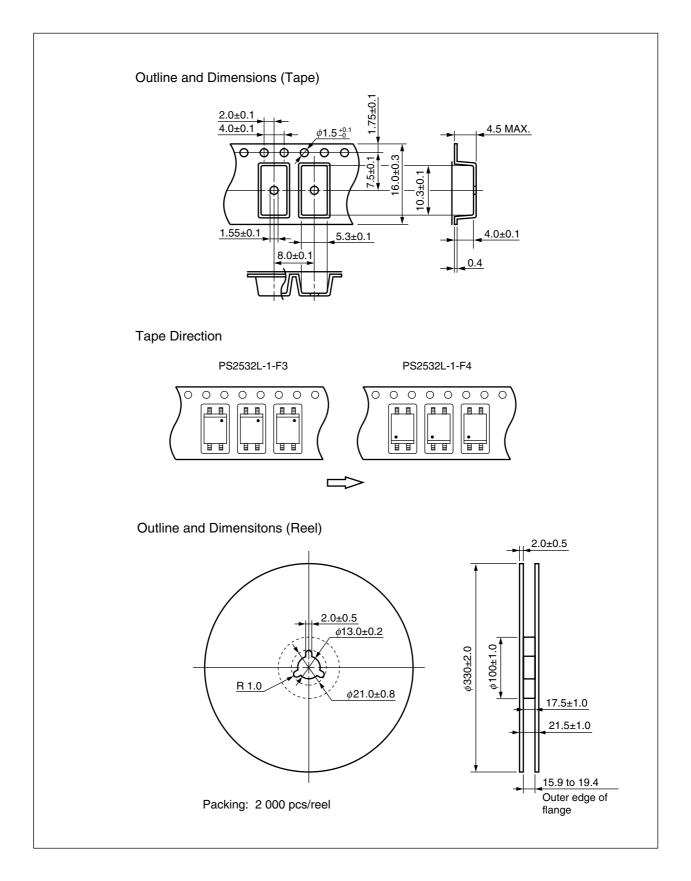
FREQUENCY RESPONSE



#### \* TAPING SPECIFICATIONS (Unit : mm)







#### NOTES ON HANDLING

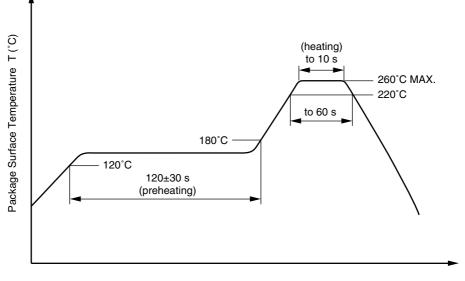
#### 1. Recommended soldering conditions

#### (1) Infrared reflow soldering

- Peak reflow temperature
- Time of peak reflow temperature
- Time of temperature higher than 220°C
- Time to preheat temperature from 120 to 180°C
- Number of reflows
- Flux

260°C or below (package surface temperature) 10 seconds or less 60 seconds or less 120±30 s Three Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

#### Recommended Temperature Profile of Infrared Reflow



Time (s)

#### (2) Wave soldering

- Temperature 260°C or below (molten solder temperature)
- Time 10 seconds or less
- Preheating conditions 120°C or below (package surface temperature)
- Number of times One (Allowed to be dipped in solder including plastic mold portion.)
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

#### ★ (3) Soldering by soldering iron

Peak temperature (lead part temperature)	350°C or below
<ul> <li>Time (each pins)</li> </ul>	3 seconds or less
• Flux	Rosin flux containing small amount of chlorine (The flux with a
	maximum chlorine content of 0.2 Wt% is recommended.)

- (a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead.
- (b) Please be sure that the temperature of the package would not be heated over 100°C.



#### (4) Cautions

#### • Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

#### 2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collector-emitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

#### \* 3. Measurement conditions of current transfer ratios (CTR), which differ according to photocoupler

Check the setting values before use, since the forward current conditions at CTR measurement differ according to product.

When using products other than at the specified forward current, the characteristics curves may differ from the standard curves due to CTR value variations or the like. Therefore, check the characteristics under the actual operating conditions and thoroughly take variations or the like into consideration before use.

#### USAGE CAUTIONS

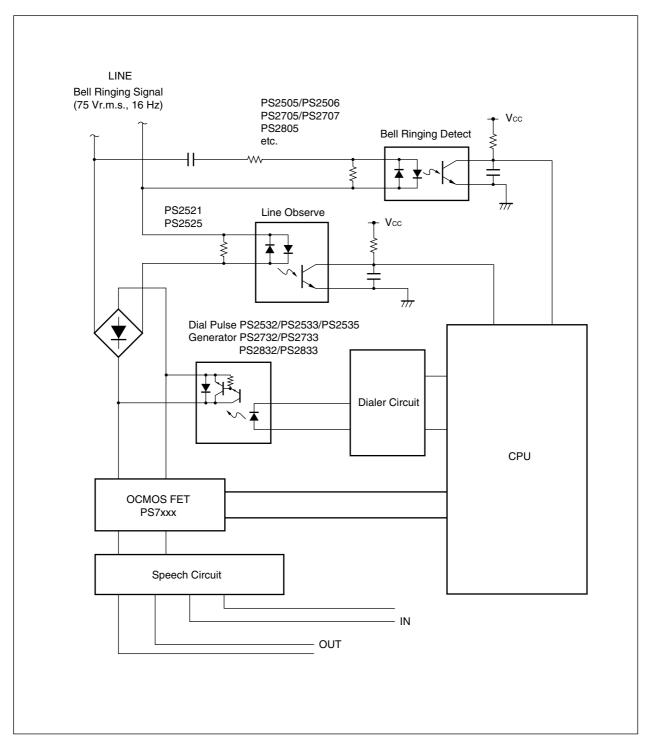
- 1. Protect against static electricity when handling.
- 2. Avoid storage at a high temperature and high humidity.

\*

# SPECIFICATION OF VDE MARKS LICENSE DOCUMENT (VDE0884)

Parameter	Symbol	Speck	Unit
Application classification (DIN VDE 0109)			
for rated line voltages $\leq$ 300 Vr.m.s.		IV	
for rated line voltages $\leq$ 600 Vr.m.s.		III	
Climatic test class (DIN IEC 68 Teil 1/09.80)		55/100/21	
Dielectric strength			
maximum operating isolation voltage	UIORM	890	Vpeak
Test voltage (partial discharge test, procedure a for type test and random test)	Upr	1 068	Vpeak
$U_{pr} = 1.2 \times U_{IORM}, P_d < 5 pC$			
Test voltage (partial discharge test, procedure b for all devices test)	Upr	1 424	V <sub>peak</sub>
$U_{pr} = 1.6 \times U_{IORM}, P_d < 5 pC$			
Highest permissible overvoltage	Utr	8 000	V <sub>peak</sub>
Degree of pollution (DIN VDE 0109)		2	
Clearance distance		> 7.0	mm
Creepage distance		> 7.0	mm
Comparative tracking index (DIN IEC 112/VDE 0303 part 1)	СТІ	175	
Material group (DIN VDE 0109)		lll a	
Storage temperature range	Tstg	–55 to +150	°C
Operating temperature range	TA	-55 to +100	°C
Isolation resistance, minimum value			
$V_{IO} = 500 \text{ V dc at } T_A = 25^{\circ}\text{C}$	Ris MIN.	10 <sup>12</sup>	Ω
V <sub>IO</sub> = 500 V dc at T <sub>A</sub> MAX. at least 100°C	Ris MIN.	10 <sup>11</sup>	Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal			
derating curve)			
Package temperature	Tsi	175	°C
Current (input current I⊧, Psi = 0)	lsi	400	mA
Power (output or total power dissipation)	Psi	700	mW
Isolation resistance			
V <sub>IO</sub> = 500 V dc at T <sub>A</sub> = 175°C (Tsi)	Ris MIN.	10 <sup>9</sup>	Ω

#### **\*** APPLICATION FOR TELEPHONE (EXAMPLE)



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M8E 00.4-0110

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	• Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
	<ol> <li>Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.</li> </ol>
	<ol><li>Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.</li></ol>
	• Do not burn, destroy, cut, crush, or chemically dissolve the product.
	• Do not lick the product or in any way allow it to enter the mouth.

► For further information, please contact

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