Not for new design, this product will be obsoleted soon TSDF02424X/TSDF02424XR



**Vishay Semiconductors** 

# Dual - MOSMIC<sup>®</sup>- two AGC Amplifiers for TV-Tuner Prestage with 5 V Supply Voltage

#### Comments

MOSMIC - MOS Monolithic Integrated Circuit

#### Features

- Two AGC amplifiers in a single package
- Easy Gate 1 switch-off with PNP switching transistors inside PLL
- Integrated gate protection diodes
- Low noise figure
- High gain, medium forward transadmittance (24 mS typ.)
- Biasing network on chip
- · Improved cross modulation at gain reduction
- High AGC-range with less steep slope
- SMD package, reverse pinning possible
- Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

#### Applications

Low noise gain controlled input stages in UHF-and VHF- tuner with 5 V supply voltage.

#### **Typical Application**







16602

#### **Mechanical Data**

Typ: TSDF02424X Case: SOT-363 Plastic case Weight: approx. 6.0 mg **Pinning:** 1 = Gate 1 (amplifier 1), 2 = Gate 2, 3 = Drain (amplifier 1), 4 = Drain (amplifier 2), 5 =Source, 6 =Gate1 (amplifier 2) Typ: TSDF02424XR Case: SOT-363 Plastic case Weight: approx. 6.0 mg **Pinning:** 1 = Gate 1 (amplifier 1), 2 = Source, 3 = Drain (amplifier 1), 4 = Drain (amplifier 2), 5 = Gate 2, 6 = Gate1 (amplifier 2)V - Vishay Y - Year, is variable for digit from 0 to 9 (e.g. 0 = 2000, 1 = 2001)CW - Calendar Week, is variable for number from 01 to 52 Number of Calendar Week is always indicating place of pin 1

#### Parts Table

Part	Marking	Package
TSDF02424X	WC5	SOT-363
TSDF02424XR	W5C	SOT-363R

Document Number 85088 Rev. 1.3, 05-Sep-08





All of following data and characteristics are valid for operating either amplifier 1 (pin 1, 3, 2, 5) or amplifier 2 (pin 6, 4, 2, 5)

#### **Absolute Maximum Ratings**

 $T_{amb} = 25 \ ^{\circ}C$ , unless otherwise specified

Parameter	Test condition	Symbol	Value	Unit
Drain - source voltage		V <sub>DS</sub>	8	V
Drain current		۱ <sub>D</sub>	25	mA
Gate 1/Gate 2 - source peak current		± I <sub>G1/G2SM</sub>	10	mA
Gate 1/Gate 2 - source voltage		$+ V_{G1/\pm G2SM}$	6	V
		- V <sub>G1SM</sub>	1.5	V
Total power dissipation	$T_{amb} \le 60 \ ^{\circ}C$	P <sub>tot</sub>	160	mW
Channel temperature		T <sub>Ch</sub>	150	°C
Storage temperature range		T <sub>stg</sub>	- 55 to + 150	۵°

#### **Maximum Thermal Resistance**

Parameter	Test condition	Symbol	Value	Unit
Channel ambient	1)	R <sub>thChA</sub>	450	K/W

 $^{1)}$  on glass fibre printed board (25 x 20 x 1.5)  $mm^3$  plated with 35  $\mu m$  Cu

#### **Electrical DC Characteristics**

 $T_{amb}$  = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Drain - source breakdown voltage	$I_D = 10 \ \mu A, \ V_{G1S} = V_{G2S} = 0$	V <sub>(BR)DSS</sub>	12			V
Gate 1 - source breakdown voltage	+ $I_{G1S}$ = 10 mA, $V_{G2S}$ = $V_{DS}$ = 0	+ V <sub>(BR)G1SS</sub>	7		10	V
Gate 2 - source breakdown voltage	$\pm I_{G2S} = 10 \text{ mA}, V_{G2S} = V_{DS} = 0$	$\pm V_{(BR)G2SS}$	7		10	V
Gate 1 - source leakage current	$+ V_{G1S} = 5 V, V_{G2S} = V_{DS} = 0$	+ I <sub>G1SS</sub>			20	nA
Gate 2 - source leakage current	$\pm V_{G2S} = 5 V, V_{G1S} = V_{DS} = 0$	± I <sub>G2SS</sub>			20	nA
Drain - source operating current	$V_{DS} = V_{RG1} = 5 V, V_{G2S} = 4 V,$ $R_{G1} = 56 k\Omega$	I <sub>DSO</sub>	8	13	18	mA
Gate 1 - source cut-off voltage	$V_{DS} = 5 \text{ V}, V_{G2S} = 4, I_D = 20 \ \mu\text{A}$	V <sub>G1S(OFF)</sub>	0.5		1.3	V
Gate 2 - source cut-off voltage	$\label{eq:VDS} \begin{split} V_{DS} &= V_{RG1} = 5 \text{ V},        $	V <sub>G2S(OFF)</sub>	0.8	1.0	1.4	V

#### Remark on improving intermodulation behavior:

By setting  $R_{G1}$  smaller than 56 k $\Omega$ , typical value of  $I_{DSO}$  will raise and improved intermodulation behavior will be performed.



## TSDF02424X/TSDF02424XR

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#### **Electrical AC Characteristics**

 $T_{amb}$  = 25 °C, unless otherwise specified

 $V_{DS} = V_{RG1} = 5 \text{ V}, V_{G2S} = 4 \text{ V}, R_{G1} = 56 \text{ k}\Omega, I_D = I_{DSO}, f = 1 \text{ MHz}$ 

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Forward transadmittance		y21s	20	24	28	mS
Gate 1 input capacitance		C <sub>issg1</sub>		1.7	2.1	pF
Feedback capacitance		C <sub>rss</sub>		15	30	fF
Output capacitance		C <sub>oss</sub>		0.9		pF
Power gain	G <sub>S</sub> = 2 mS, G <sub>L</sub> = 0.5 mS, f = 200 MHz	G <sub>ps</sub>		26		dB
	G <sub>S</sub> = 3,3 mS, G <sub>L</sub> = 1 mS, f = 800 MHz	G <sub>ps</sub>	16.5	21		dB
AGC range	V <sub>DS</sub> = 5 V, V <sub>G2S</sub> = 1 to 4 V, f = 800 MHz	$\Delta G_{ps}$		45		dB
Noise figure	G <sub>S</sub> = 2 mS, G <sub>L</sub> = 0.5 mS, f = 200 MHz	F		1		dB
	G <sub>S</sub> = 3.3 mS, G <sub>L</sub> = 1 mS, f = 800 MHz	F		1.3		dB
Cross modulation	Input level for k = 1 % @ 0 dB AGC f <sub>w</sub> = 50 MHz, $f_{unw} = 60 MHz$	X <sub>mod</sub>	90			dBμV
	Input level for k = 1 % @ 40 dB AGC f <sub>w</sub> = 50 MHz, $f_{unw} = 60 MHz$	X <sub>mod</sub>	100	105		dBμV

#### Package Dimensions in mm (Inches)



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## TSDF02424X/TSDF02424XR

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#### **Ozone Depleting Substances Policy Statement**

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

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