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Industry's Best System Power

- ⌘ Superior power management of processors and memory devices
- ⌘ 2.5W typical power consumption with all ports operating at maximum speed maximum)
- ⌘ DDR2 Memory Controller (up to 50% memory power savings compared to D
- ⌘ Integrated Clock Generator removes power requirement of external compon

Industry's Best System Cost

- ⌘ DDR2 Memory Controller
- ⌘ Integrated Clock Generator with optional spread spectrum capability
 - ⌘ Removes cost, power, and design complexity associated with external generation and buffering
- ⌘ Reduced design complexity and high reliability design for lower PCB costs
 - ⌘ Designed for 200MHz operation with only eight PCB layers

Industry's Highest Performance

- ⌘ Best system performance per watt
- ⌘ Best system performance per dollar
- ⌘ 200 MHz 60x/MPX processor bus
 - ⌘ Dual processor support
 - ⌘ Advanced pipeline architecture
 - ⌘ Low processor memory latency
- ⌘ High performance PCI/X bus, up to 133MHz operation
 - ⌘ PCI/X Host or Agent modes supported
- ⌘ Low latency nonblocking internal switch fabric

Advanced Packaging

- ⌘ RoHS compliant packaging
- ⌘ 1023-pin, 33 x 33 mm, FCBGA
- ⌘ Pin compatible with Tsi108

Block Diagram

Ordering Information

Product Number	Frequency	Temperature	Package	Pins
Tsi109200CL	200 MHz	Commercial	BGA	1023
Tsi109200CLY	200 MHz	Commercial	BGA (RoHS)	1023
Tsi109200IL	200 MHz	Industrial	BGA	1023
Tsi109200ILY	200 MHz	Industrial	BGA (RoHS)	1023

[ZTE Selects Tundra's PCI Express Product for Next Generation Platform](#)
August 27, 2008

[Tundra Introduces MultiStandard RapidIO Evaluation Platform](#)
July 28, 2008

[Tundra Tsi574 RapidIO Switch Selected by Texas Instruments](#)
June 4, 2008



[Tsi109 Feature Sheet](#)
[Technical Support Request](#)
[Customer Support Request](#)
[Sales Network](#)

[Embedded System Conference 2008](#)
October 26, 2008