

# 3 V SUPER MINIMOLD L-BAND SI MMIC DOWNCONVERTER

## **UPC2756TB**

#### **FEATURES**

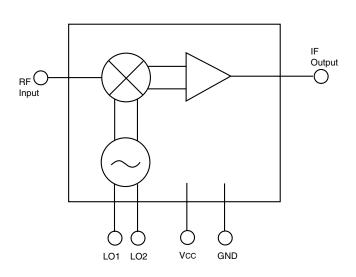
- HIGH DENSITY SURFACE MOUNTING:
   6 Pin Super Minimold or SOT-363 package
- WIDE BAND OPERATION: RF = 0.1 to 2.0 GHz IF = 10 to 300 MHz
- ON BOARD OSCILLATOR
- SUPPLY VOLTAGE: VCC = 2.7 TO 3.3 V

#### **DESCRIPTION**

NEC's UPC2756TB is a silicon MMIC integrated circuit manufactured using the NESAT III process. The device consists of a double balance mixer, an IF amplifier and a built-in LO. this device is suitable as a L-BAND downconverter for the receiver stage of wireless systems. The UPC2756TB is pin compatible and has comparable performance as the larger UPC2756T, so it is suitable for use as a replacement to help reduce system size. The IC housed in a 6 pin super minimold or SOT-363 package.

NEC's stringent quality assurance and test procedures ensure the highest reliability and performance.

#### INTERNAL BLOCK DIAGRAM



#### **ELECTRICAL CHARACTERISTICS** (TA = $25^{\circ}$ C, Vcc = 3 V, ZL = Zs = $50 \Omega$ )

	PART NUMBER PACKAGE OUTLINE	UPC2756TB S06			
SYMBOLS	PARAMETERS AND CONDITIONS	UNITS	MIN	TYP	MAX
Icc	Circuit Current (no signal)	mA	3.5	6.0	8.0
fRF	RF Frequency Response (3 dB down from the gain at fRF = 900 MHz, fIF = 150 MHz)	GHz	0.1		2.0
fiF	IF Frequency Response (3 dB down from the gain at fRF = 900 MHz, fIF = 150 MHz)	MHz	10	300	
CG	Conversion Gain <sup>1</sup> $fret{RF} = 900 \text{ MHz}, fret{fret} = 150 \text{ MHz}$ $fret{fret} = 1.6 \text{ GHz}, fret{fret} = 20 \text{ MHz}$	dB dB	11 11	14 14	17 17
NF	Noise Figure	dB dB		10 13	13 16
Psat	Saturated Output Power <sup>2</sup> free = 900 MHz, fir = 150 MHz free = 1.6 GHz, fir = 20 MHz	dBm dBm	-11 -15	-8 -12	
OIP3	SSB Output 3rd Order Intercept Point fr= 0.8~2.0 GHz, fr= 100 MHz	dBm		+4	
ISO	LO Leakage, fLo = 0.8 ~2.0 GHz at RF pin at IF pin	dBm dBm		-35 -23	
PN	Phase Noise <sup>3</sup> , fosc = 1.9 GHz	dBc/Hz		-68	
RTH (J-A)	Thermal Resistance (Junction to Ambient) Mounted on a 50 x 50 x 1.6 mm epoxy glass PWB	°C/W			325

#### Notes:

- 1. PRF = -40 dBm.
- 2. PRF = -10 dBm.
- 3. See Application Circuit.

California Eastern Laboratories

## ABSOLUTE MAXIMUM RATINGS<sup>1</sup> (TA = 25°C)

SYMBOLS	PARAMETERS	UNITS	RATINGS
Vcc	Supply Voltage	V	5.5
Рт	Total Power Dissipation <sup>2</sup>	mW	200
Тор	Operating Temperature	°C	-40 to +85
Тѕтс	Storage Temperature	°C	-55 to +150

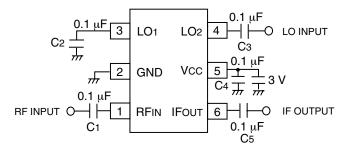
#### Notes:

- Operation in excess of any one of these parameters may result in permanent damage.
- 2. Mounted on a 50 x 50 x 1.6 mm epoxy glass PWB ( $TA = +85^{\circ}C$ ).

## RECOMMENDED OPERATING CONDITIONS

SYMBOLS	PARAMETERS	UNITS	MIN	TYP	MAX
Vcc	Supply Voltage	V	2.7	3.0	3.3
Тор	Operating Temperature	°C	-40	+25	+85

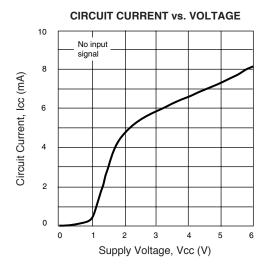
#### **TEST CIRCUIT**



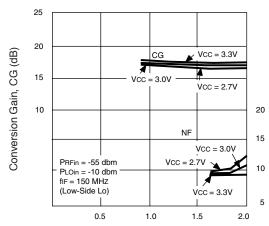
#### **PIN FUNCTIONS**

Pin No.	Symbol	Applied Voltage (V)	Pin Voltage (V)	Description	Internal Equivalent Circuit
1	RFin	-	1.2	Signal input pin to double balancec mixer. This pin must be coupled to the signal source with a blocking capacitor.	Vcc Vcc
2	GND	0	-	Ground pin. This pin should be connected to system ground with minimum inductance. Ground pattern on the board should be formed as wide as possible.	
3	LO1	-	1.2	These pins are both the base-collectors of a differential amplifier configured to oscillate when equipped with an external tank resonator circuit. Each pin must be coupled to the tank circuit with a blocking capacitor. In the case of an external LO source, bypass the unused pin with a capacitor to ground.	3 4
4	LO2	_	1.2		nhi
5	Vcc	2.7 to 3.3	_	Power supply pin. This pin should be externally equipped with a bypass capacitor to minimize ground impedance.	
6	IFоuт	_	1.7	Output of single-ended push-pull IF buffer amplifier. This is an emitter-follower output with low impedance. This pin must be coupled to the next stage with a blocking capacitor.	

#### **TYPICAL PERFORMANCE CURVES** (TA = 25°C)

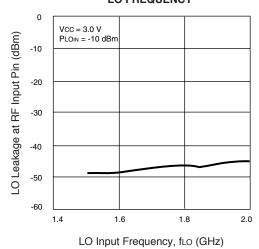


## CONVERSION GAIN AND NOISE FIGURE vs. RF INPUT FREQUENCY

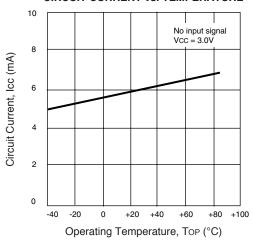


RF Input Frequency, fRF (GHz)

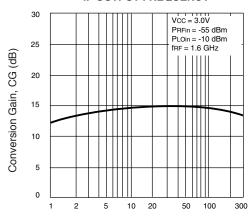
#### LO LEAKAGE AT RF PIN vs. LO FREQUENCY



#### **CIRCUIT CURRENT vs. TEMPERATURE**

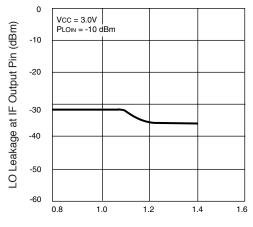


## CONVERSION GAIN vs. IF OUTPUT FREQUENCY



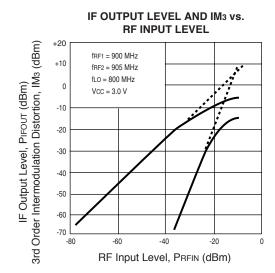
IF Output Frequency, fif (GHz)

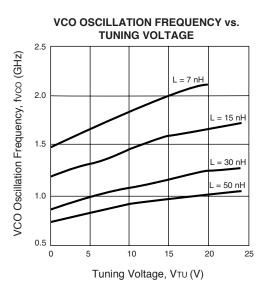
## LO LEAKAGE AT IF PIN vs. LO FREQUENCY

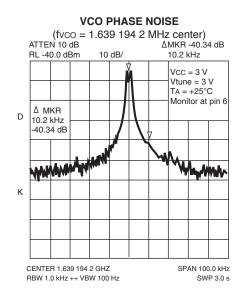


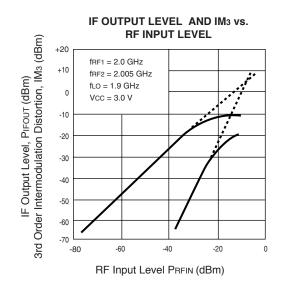
LO Input Frequency (GHz)

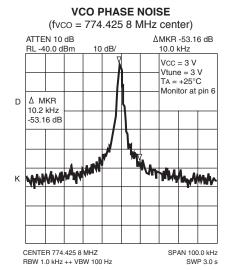
### TYPICAL PERFORMANCE CURVES (TA = 25°C)





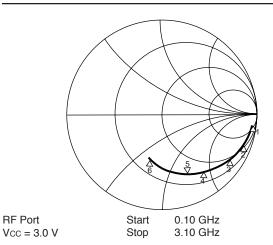




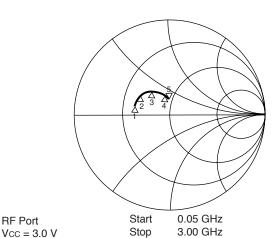


RF Port

#### TYPICAL SCATTERING PARAMETERS



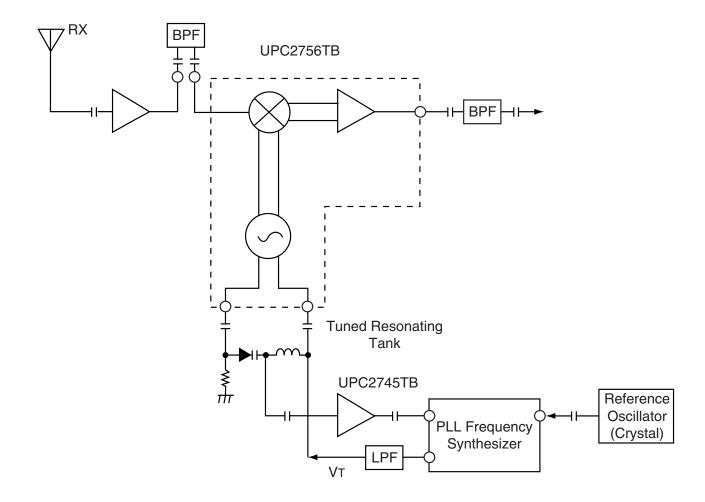
519.8  $\Omega$  –j 1.1  $\Omega$ 100 MHz 59.3 Ω –j 281.0 Ω 500 MHz 900 MHz 38.3 Ω -j 157.0 Ω 4: 1500 MHz  $31.5~\Omega~-j~90.1~\Omega$ 5: 1900 MHz 28.5  $\Omega$  –j 67.9  $\Omega$ 6: 3000 MHz  $25.7 \Omega - 31.7$ 



1: 50 MHz 22.5  $\Omega$  +j 6.1  $\Omega$ 2: 80 MHz 24.2  $\Omega$  +j 11.3  $\Omega$ 130 MHz 30.2  $\Omega$  +j 16.6  $\Omega$ 3: 4: 240 MHz 42.6 Ω +j 17.5 Ω 300 MHz  $46.6 \Omega + j 15.6 \Omega$ 

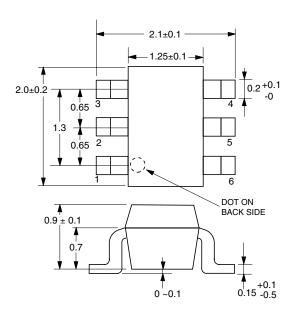
RF Port

#### SYSTEM APPLICATION EXAMPLE

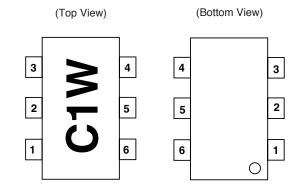


#### **OUTLINE DIMENSIONS** (Units in mm)

#### **PACKAGE OUTLINE S06**

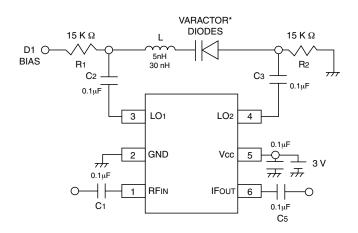


#### **LEAD CONNECTIONS**



- 1. RF INPUT
- 2. GND
- 3. LO<sub>1</sub>
- 4. LO<sub>2</sub>
- 5. Vcc
- 6. IF OUTPUT

#### **APPLICATION CIRCUIT EXAMPLE**



\* Recommended Varactor Diodes:

Alpha SMV1204-4, Toshiba 1SV186 or equivalent

#### **ORDERING INFORMATION**

PART NUMBER	QTY	
UPC2756TB-E3-A	3K/Reel	

Note:

Embossed Tape, 8 mm wide,

Pins 1, 2, 3 are in tape pull-out direction.

#### Life Support Applications

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Restricted Substance per RoHS	Concentration Limit per RoHS (values are not yet fixed)	0000	on contained devices	
Lead (Pb)	< 1000 PPM	-A -AZ Not Detected (*)		
Mercury	< 1000 PPM	Not Detected		
Cadmium	< 100 PPM	Not Detected		
Hexavalent Chromium	< 1000 PPM	Not Detected		
PBB	< 1000 PPM	Not Detected		
PBDE	< 1000 PPM	Not Detected		

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