NEC's ½W LOW VOLTAGE L, S-BAND SPDT SWITCH

## FEATURES

## - SWITCH CONTROL VOLTAGE: <br> $\mathrm{V}_{\text {cont }(H)}=1.8$ to 5.3 V (3.0 V TYP.) <br> $\mathrm{V}_{\text {cont }(\mathrm{L}} \mathrm{L}=-0.2$ to +0.2 V (0 V TYP.)

- LOW INSERTION LOSS:
0.25 dB TYP. @ 0.05 to $0.5 \mathrm{GHz}, \mathrm{V}_{\text {cont }(H)}=3.0 \mathrm{~V}, \mathrm{~V}_{\text {cont }(L)}=0 \mathrm{~V}$
0.25 dB TYP. @ 0.5 to $1.0 \mathrm{GHz}, \mathrm{V}_{\text {cont }}(H)=3.0 \mathrm{~V}, \mathrm{~V}_{\text {cont }}(\mathrm{L})=0 \mathrm{~V}$
0.30 dB TYP. @ 1.0 to $2.0 \mathrm{GHz}, \mathrm{V}_{\text {cont }(H)}=3.0 \mathrm{~V}, \mathrm{~V}_{\text {cont }}(\mathrm{L})=0 \mathrm{~V}$
0.35 dB TYP. @ 2.0 to $2.5 \mathrm{GHz}, \mathrm{V}_{\text {cont }(H)}=3.0 \mathrm{~V}$, $\mathrm{V}_{\text {cont }}(\mathrm{L})=0 \mathrm{~V}$
0.35 dB TYP. @ 2.5 to $3.0 \mathrm{GHz}, \mathrm{V}_{\text {cont }(H)}=3.0 \mathrm{~V}$, $\mathrm{V}_{\text {cont }(L)}(\mathrm{L})=0 \mathrm{~V}$
- HIGH ISOLATION:

32 dB TYP. @ 0.05 to $0.5 \mathrm{GHz}, \mathrm{V}_{\text {cont }(H)}=3.0 \mathrm{~V}, \mathrm{~V}_{\text {cont }}(\mathrm{L})=0 \mathrm{~V}$
28 dB TYP. @ 0.5 to $1.0 \mathrm{GHz}, \mathrm{V}_{\text {oont }}(\mathrm{H})=3.0 \mathrm{~V}, \mathrm{~V}_{\text {cont }}(\mathrm{L})=0 \mathrm{~V}$
27 dB TYP. @ 1.0 to $2.0 \mathrm{GHz}, \mathrm{V}_{\text {cont }(H)}=3.0 \mathrm{~V}, \mathrm{~V}_{\text {cont }(L)}=0 \mathrm{~V}$
26 dB TYP. @ 2.0 to $2.5 \mathrm{GHz}, \mathrm{V}_{\text {cont }(H)}=3.0 \mathrm{~V}, \mathrm{~V}_{\text {cont }(\mathrm{L})}=0 \mathrm{~V}$
24 dB TYP. @ 2.5 to $3.0 \mathrm{GHz}, \mathrm{V}_{\text {cont }(H)}=3.0 \mathrm{~V}, \mathrm{~V}_{\text {cont }(\mathrm{L})}=0 \mathrm{~V}$

## - POWER HANDLING:

$P_{\text {in }(1 \mathrm{~dB})}=+27.0 \mathrm{dBm}$ TYP. @ 0.5 to $3.0 \mathrm{GHz}, \mathrm{V}_{\text {cont }(H)}=3.0 \mathrm{~V}, \mathrm{~V}_{\text {cont }}(\mathrm{L})=0 \mathrm{~V}$
$\operatorname{Pin}(1 \mathrm{~dB})=+20.0 \mathrm{dBm}$ TYP. @ 0.5 to $3.0 \mathrm{GHz}, \mathrm{V}_{\text {cont }(H)}=1.8 \mathrm{~V}, \mathrm{~V}_{\text {cont }}(\mathrm{L})=0 \mathrm{~V}$

## DESCRIPTION

NEC's UPG2214TB is a GaAs MMIC L, S-band SPDT (Single Pole Double Throw) switch for mobile phones and other L, S-band applications from 0.05 to 3.0 GHz .

This device can operate from 1.8 to 5.3 V with low insertion loss and high isolation. Performance is specified at both 1.8 V and 3.0 V .

The UPG2214TB is housed in a 6-pin super minimold package suitable for high-density surface mounting.

## APPLICATIONS

- L, S-band digital cellular and cordless telephones
- Bluetooth ${ }^{\text {TM }}$, W-LAN, and WLL
- Short Range Wireless
- HIGH-DENSITY SURFACE MOUNTING:

6-pin super minimold package $(2.0 \times 1.25 \times 0.9 \mathrm{~mm})$

- Pb FREE

ORDERING INFORMATION

| Part Number | Package | Marking | Supplying Form |
| :---: | :---: | :---: | :--- |
| UPG2214TB-E4-A | 6-pin super minimold | G4J | • Embossed tape 8 mm wide <br> • Pin 4, 5, 6 face the perforation side of the tape <br>  |
|  |  | Qty 3 kpcs/reel |  |

Remark To order evaluation samples, contact your nearby sales office.
Part number for sample order: UPG2214TB-A

Caution Observe precautions when handling because these devices are sensitive to electrostatic discharge.

## PIN CONNECTIONS AND INTERNAL BLOCK DIAGRAM



TRUTH TABLE

| $\mathbf{V}_{\text {cont1 }}$ | V $_{\text {cont2 }}$ | INPUT-OUTPUT1 | INPUT-OUTPUT2 |
| :---: | :---: | :---: | :---: |
| Low | High | ON | OFF |
| High | Low | OFF | ON |

ABSOLUTE MAXIMUM RATINGS $\left(T_{A}=25^{\circ} \mathrm{C}\right.$, unless otherwise specified)

| PARAMETER | SYMBOL | RATINGS | UNIT |
| :--- | :---: | :---: | :---: |
| Switch Control Voltage | $\mathrm{V}_{\text {cont }}$ | +6.0 Note | V |
| Input Power | $\mathrm{P}_{\text {in }}$ | +30 | dBm |
| Operating Ambient Temperature | $\mathrm{T}_{\mathrm{A}}$ | -45 to +85 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature | $\mathrm{T}_{\text {stg }}$ | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |

Note $\left|V_{\text {cont1 }} V_{\text {cont2 }}\right| \leq 6.0 \mathrm{~V}$

RECOMMENDED OPERATING RANGE ( $\mathrm{TA}_{A}=25^{\circ} \mathrm{C}$, unless otherwise specified)

| Parameter | Symbol | MIN. | TYP. | MAX. | Unit |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Switch Control Voltage (H) | $\mathrm{V}_{\text {cont }(\mathrm{H})}$ | 1.8 | 3.0 | 5.3 | V |
| Switch Control Voltage (L) | $\mathrm{V}_{\text {cont }(\mathrm{L})}$ | -0.2 | 0 | 0.2 | V |

## ELECTRICAL CHARACTERISTICS

$\left(\mathrm{TA}_{\mathrm{A}}=+25^{\circ} \mathrm{C}, \mathrm{V}_{\text {cont }}(\mathrm{H})=3.0, \mathrm{~V}\right.$ cont $(\mathrm{L})=0 \mathrm{~V}, \mathrm{DC}$ blocking capacitors value $=100 \mathrm{pF}$, unless otherwise specified)

| PARAMETER | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss 1 | Lins1 | $\mathrm{f}=0.05$ to 0.5 GHz Note 1 | - | 0.25 | 0.45 | dB |
| Insertion Loss 2 | Lins2 | $\mathrm{f}=0.5$ to 1.0 GHz | - | 0.25 | 0.45 | dB |
| Insertion Loss 3 | Lins3 | $\mathrm{f}=1.0$ to 2.0 GHz | - | 0.30 | 0.50 | dB |
| Insertion Loss 4 | Lins4 | $\mathrm{f}=2.0$ to 2.5 GHz | - | 0.35 | 0.55 | dB |
| Insertion Loss 5 | Lins5 | $\mathrm{f}=2.5$ to 3.0 GHz | - | 0.35 | 0.60 | dB |
| Isolation 1 | ISL1 | $\mathrm{f}=0.05$ to 0.5 GHz Note 1 | 29 | 32 | - | dB |
| Isolation 2 | ISL2 | $\mathrm{f}=0.5$ to 1.0 GHz | 25 | 28 | - | dB |
| Isolation 3 | ISL3 | $\mathrm{f}=1.0$ to 2.0 GHz | 24 | 27 | - | dB |
| Isolation 4 | ISL4 | $\mathrm{f}=2.0$ to 2.5 GHz | 23 | 26 | - | dB |
| Isolation 5 | ISL5 | $\mathrm{f}=2.5$ to 3.0 GHz | 21 | 24 | - | dB |
| Input Return Loss 1 | RLin1 | $\mathrm{f}=0.05$ to 0.5 GHz Note 1 | 15 | 20 | - | dB |
| Input Return Loss 2 | RLin2 | $\mathrm{f}=0.5$ to 3.0 GHz | 15 | 20 | - | dB |
| Output Return Loss 1 | RLout1 | $\mathrm{f}=0.05$ to 0.5 GHz Note 1 | 15 | 20 | - | dB |
| Output Return Loss 2 | RLout2 | $\mathrm{f}=0.5$ to 3.0 GHz | 15 | 20 | - | dB |
| 0.1 dB Loss Compression Input Power Note 2 | Pin (0.1 dB) | $\mathrm{f}=2.0 / 2.5 \mathrm{GHz}$ | +21.0 | +23.0 | - | dBm |
|  |  | $\mathrm{f}=0.5$ to 3.0 GHz | - | +23.0 | - | dBm |
| 1 dB Loss Compression Input Power Note 3 | Pin (1 dB) | $\mathrm{f}=0.5$ to 3.0 GHz | - | +27.0 | - | dBm |
| 2nd Harmonics | $2 \mathrm{fo}_{0}$ | $\mathrm{f}=2.0 \mathrm{GHz}, \mathrm{P}_{\text {in }}=+15 \mathrm{dBm}$ | - | -55 | -47 | dBc |
|  |  | $\mathrm{f}=2.5 \mathrm{GHz}, \mathrm{P}_{\text {in }}=+15 \mathrm{dBm}$ | - | -55 | -47 | dBc |
| 3rd Harmonics | $3 \mathrm{f}_{0}$ | $\mathrm{f}=2.0 \mathrm{GHz}, \mathrm{Pin}_{\text {in }}=+15 \mathrm{dBm}$ | - | -55 | -47 | dBc |
|  |  | $\mathrm{f}=2.5 \mathrm{GHz}, \mathrm{P}_{\text {in }}=+15 \mathrm{dBm}$ | - | -55 | -47 | dBc |
| Intermodulation Intercept Point | $\mathrm{IIP}_{3}$ | $\begin{aligned} & \mathrm{f}=0.5 \text { to } 3.0 \mathrm{GHz}, 2 \text { tone, } \\ & \mathrm{P}_{\text {in }}=+16 \mathrm{dBm}, 5 \mathrm{MHz} \text { spicing } \end{aligned}$ | - | +58 | - | dBm |
| Switch Control Current | $I_{\text {cont }}$ |  | - | 4 | 20 | $\mu \mathrm{A}$ |
| Switch Control Speed | tsw | 50\% CTL to 90/10\% RF | - | 20 | 200 | ns |

Notes 1. DC blocking capacitors $=1,000 \mathrm{pF}$ at $\mathrm{f}=0.05$ to 0.5 GHz
2. $\operatorname{Pin}(0.1 \mathrm{~dB})$ is the measured input power level when the insertion loss increases 0.1 dB more than that of linear range.
3. $\operatorname{Pin}(1 \mathrm{~dB})$ is the measured input power level when the insertion loss increases 1 dB more than that of linear range.

## ELECTRICAL CHARACTERISTICS

$\left(\mathrm{TA}_{\mathrm{A}}=+25^{\circ} \mathrm{C}, \mathrm{V}\right.$ cont $(\mathrm{H})=1.8, \mathrm{~V}$ cont $(\mathrm{L})=0 \mathrm{~V}$, DC blocking capacitors value $=100 \mathrm{pF}$, unless otherwise specified)

| PARAMETER | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss 6 | Lins6 | $\mathrm{f}=0.05$ to 0.5 GHz Note 1 | - | 0.25 | 0.50 | dB |
| Insertion Loss 7 | Lins7 | $\mathrm{f}=0.5$ to 1.0 GHz | - | 0.25 | 0.50 | dB |
| Insertion Loss 8 | Lins8 | $\mathrm{f}=1.0$ to 2.0 GHz | - | 0.30 | 0.55 | dB |
| Insertion Loss 9 | Lins9 | $\mathrm{f}=2.0$ to 2.5 GHz | - | 0.35 | 0.60 | dB |
| Insertion Loss 10 | Lins10 | $\mathrm{f}=2.5$ to 3.0 GHz | - | 0.35 | 0.65 | dB |
| Isolation 6 | ISL6 | $\mathrm{f}=0.05$ to 0.5 GHz Note 1 | 27 | 30 | - | dB |
| Isolation 7 | ISL7 | $\mathrm{f}=0.5$ to 2.0 GHz | 23 | 27 | - | dB |
| Isolation 8 | ISL8 | $\mathrm{f}=2.0$ to 2.5 GHz | 21 | 25 | - | dB |
| Isolation 9 | ISL9 | $\mathrm{f}=2.5$ to 3.0 GHz | 20 | 24 | - | dB |
| Input Return Loss 3 | RLin3 | $\mathrm{f}=0.05$ to 3.0 GHz Note 1 | 15 | 20 | - | dB |
| Output Return Loss 3 | RLout3 | $\mathrm{f}=0.05$ to 3.0 GHz Note 1 | 15 | 20 | - | dB |
| 0.1 dB Loss Compression | Pin ( 0.1 dB ) | $\mathrm{f}=2.0 / 2.5 \mathrm{GHz}$ | +14.0 | +17.0 | - | dBm |
| Input Power Note 2 |  | $\mathrm{f}=0.5$ to 3.0 GHz | - | +17.0 | - | dBm |
| 1 dB Loss Compression Input Power Note 3 | Pin (1 dB) | $\mathrm{f}=0.5$ to 3.0 GHz | - | +20.0 | - | dBm |
| Switch Control Current | Icont |  | - | 4 | 20 | $\mu \mathrm{A}$ |
| Switch Control Speed | tsw | 50\% CTL to 90/10\% RF | - | 20 | 200 | ns |

Notes 1. DC blocking capacitors $=1000 \mathrm{pF}$ at $\mathrm{f}=0.05$ to 0.5 GHz
2. $P$ in $(0.1 \mathrm{~dB})$ is the measured input power level when the insertion loss increases 0.1 dB more than that of linear range.
3. $\operatorname{Pin}(1 \mathrm{~dB})$ is the measured input power level when the insertion loss increases 1 dB more than that of linear range.

## Caution It is necessary to use DC blocking capacitors with this device.

The value of DC blocking capacitors should be chosen to accommodate the frequency of operation, bandwidth, switching speed and the condition with the actual board of your system. The range of recommended DC blocking capacitor value is less than 100 pF for frequencies above 0.5 GHz , and $1,000 \mathrm{pF}$ for frequencies below 0.5 GHz .

## EVALUATION CIRCUIT



Note C0: 0.05 to $0.5 \mathrm{GHz} 1,000 \mathrm{pF}$ : 0.5 to 3.0 GHz 100 pF

The application circuits and their parameters are for reference only and are not intended for actual design-ins.

## ILLUSTRATION OF THE TEST CIRCUIT ASSEMBLED ON EVALUATION BOARD



## USING THE NEC EVALUATION BOARD

| SYMBOL | VALUES |
| :--- | :---: |
| C1, C2, C3 | 100 pF |
| C4, C5 | $1,000 \mathrm{pF}$ |

## TYPICAL CHARACTERISTICS

$\left(\mathrm{TA}_{\mathrm{A}}=+25^{\circ} \mathrm{C}, \mathrm{V}_{\text {cont }}(\mathrm{H})=3.0 \mathrm{~V}, \mathrm{~V}_{\text {cont }}(\mathrm{L})=0 \mathrm{~V}, \mathrm{DC}\right.$ blocking capacitors $=100 \mathrm{pF}$, unless otherwise specified)


Remark The graphs indicate nominal characteristics.

Caution These characteristics values include the losses of the NEC evaluation board.

INPUT-OUTPUT1
ISOLATION vs. FREQUENCY


INPUT-OUTPUT1
INPUT RETURN LOSS vs. FREQUENCY


INPUT-OUTPUT1
OUTPUT RETURN LOSS vs. FREQUENCY


INPUT-OUTPUT2
ISOLATION vs. FREQUENCY


INPUT-OUTPUT2
INPUT RETURN LOSS vs. FREQUENCY


INPUT-OUTPUT2
OUTPUT RETURN LOSS vs. FREQUENCY


Remark The graphs indicate nominal characteristics.

## OUTPUT POWER vs. INPUT POWER



Remark The graphs indicate nominal characteristics.

PACKAGE DIMENSIONS
6-PIN SUPER MINIMOLD (UNIT: mm)


## RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

| Soldering Method | Soldering Conditions | Condition Symbol |  |
| :--- | :--- | :--- | :---: |
| Infrared Reflow | Peak temperature (package surface temperature) | $: 260^{\circ} \mathrm{C}$ or below | IR260 |
|  | Time at peak temperature | $: 10$ seconds or less |  |
|  | Time at temperature of $220^{\circ} \mathrm{C}$ or higher | $: 60$ seconds or less |  |
|  | Preheating time at 120 to $180^{\circ} \mathrm{C}$ | $: 120 \pm 30$ seconds |  |
|  | Maximum number of reflow processes | $: 3$ times |  |
|  | Maximum chlorine content of rosin flux (\% mass) | $: 0.2 \%(\mathrm{Wt}$.) or below |  |
| VPS | Peak temperature (package surface temperature) | $: 215^{\circ} \mathrm{C}$ or below | VP215 |
|  | Time at temperature of $200^{\circ} \mathrm{C}$ or higher | $: 25$ to 40 seconds |  |
|  | Preheating time at 120 to $150^{\circ} \mathrm{C}$ | $: 30$ to 60 seconds |  |
|  | Maximum number of reflow processes | $: 3$ times |  |
|  | Maximum chlorine content of rosin flux (\% mass) | $: 0.2 \%$ (Wt.) or below |  |
|  | Peak temperature (molten solder temperature) | $: 260^{\circ} \mathrm{C}$ or below | WS260 |
|  | Time at peak temperature | $: 10$ seconds or less |  |
|  | Preheating temperature (package surface temperature) $: 120^{\circ} \mathrm{C}$ or below |  |  |
|  | Maximum number of flow processes | $: 1$ time |  |
|  | Maximum chlorine content of rosin flux (\% mass) | $: 0.2 \%($ Wt.) or below |  |
|  | Peak temperature (pin temperature) | $: 350^{\circ} \mathrm{C}$ or below | HS350 |
|  | Soldering time (per side of device) | $: 3$ seconds or less |  |
|  | Maximum chlorine content of rosin flux (\% mass) | $: 0.2 \%$ (Wt.) or below |  |

Caution Do not use different soldering methods together (except for partial heating).

Life Support Applications
These NEC products are not intended for use in life support devices, appliances, or systems where the malfunction of these products can reasonably be expected to result in personal injury. The customers of CEL using or selling these products for use in such applications do so at their own risk and agree to fully indemnify CEL for all damages resulting from such improper use or sale.

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## Subject: Compliance with EU Directives

CEL certifies, to its knowledge, that semiconductor and laser products detailed below are compliant with the requirements of European Union (EU) Directive 2002/95/EC Restriction on Use of Hazardous Substances in electrical and electronic equipment (RoHS) and the requirements of EU Directive 2003/11/EC Restriction on Penta and Octa BDE.

CEL Pb-free products have the same base part number with a suffix added. The suffix -A indicates that the device is Pb -free. The -AZ suffix is used to designate devices containing Pb which are exempted from the requirement of RoHS directive (*). In all cases the devices have Pb-free terminals. All devices with these suffixes meet the requirements of the RoHS directive.

This status is based on CEL's understanding of the EU Directives and knowledge of the materials that go into its products as of the date of disclosure of this information.

| Restricted Substance <br> per RoHS | Concentration Limit per RoHS <br> (values are not yet fixed) | Concentration contained <br> in CEL devices |  |
| :--- | :---: | :---: | :---: |
| Lead (Pb) | $<1000$ PPM | - -A |  |
| Mercury | $<1000$ PPM | Not Detected |  |
| Cadmium | $<100$ PPM | Not Detected |  |
| Hexavalent Chromium | $<1000$ PPM | Not Detected |  |
| PBB | $<1000$ PPM | Not Detected |  |
| PBDE | $<1000$ PPM | Not Detected |  |

If you should have any additional questions regarding our devices and compliance to environmental standards, please do not hesitate to contact your local representative.

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