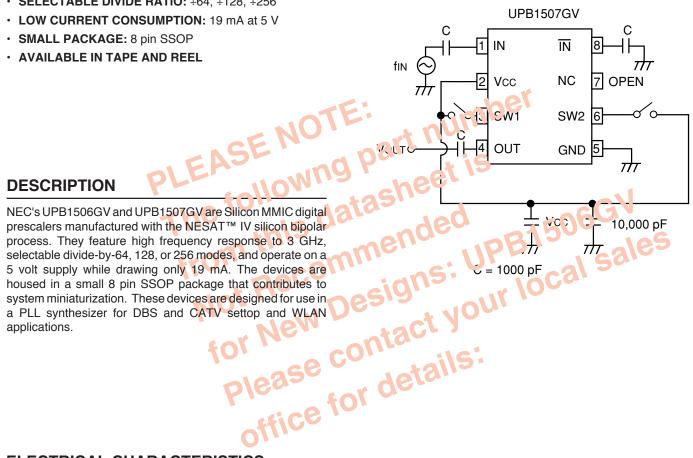
NEC's 3.0 GHz DIVIDE BY 64/128/256 PRESCALER

UPB1506GV UPB1507GV

FEATURES

- **HIGH FREQUENCY OPERATION TO 3 GHz**
- SELECTABLE DIVIDE RATIO: +64, +128, +256

TEST CIRCUIT



ELECTRICAL CHARACTERISTICS (TA = -40 to +85°C, Vcc = 4.5 to 5.5 V, Zs = 50Ω)

PART NUMBER PACKAGE OUTLINE			UPB1506GV,UPB1507GV S08		
SYMBOLS	PARAMETERS AND CONDITIONS	UNITS	MIN	ТҮР	MAX
lcc	Circuit Current	mA	12.5	19	26.5
fin(U)	Upper Limit Operating Frequency, PIN = -15 to +6 dBm	GHz		3.0	
fIN(L)1	Lower Limit Operating Frequency, PIN = -10 to +6 dBm	GHz			0.5
fIN(L)2	Lower Limit Operating Frequency, PIN = -15 to +6 dBm	GHz			1.0
PIN1	Input Power, fin = 1.0 to 3.0 GHz	dBm	-15		+6
PIN2	Input Power, fin = 0.5 to 1.0 GHz	dBm	-10		+6
Vout	Output Voltage, CL = 0.8 pF	Vp-p	1.2	1.6	
VIN(H)	Division Ratio Control Input High	V		Vcc	
VIN(L)	Division Ratio Control Input Low	V		OPEN or GND	

California Eastern Laboratories

ABSOLUTE MAXIMUM RATINGS¹ (TA = 25°C)

SYMBOLS	PARAMETERS	UNITS	RATINGS
Vcc	Supply Voltage	V	-0.5 to 6.0
VIN	Input Voltage	V	-0.5 to Vcc + 0.5
Pin	Input Power	dBm	+10
PD	Power Dissipation ²	mW	250
Тор	Operating Temperature	°C	-45 to +85
Tstg	Storage Temperature	°C	-55 to +150

Notes:

1. Operation in excess of any one of these parameters may result in permanent damage.

 Mounted on a double-sided copper clad 50x50x1.6 mm epoxy glass PWB (T_A = +85°C).

RECOMMENDED OPERATING CONDITIONS

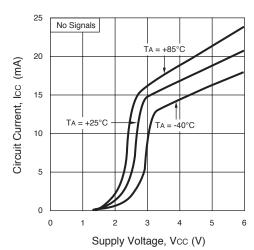
SYMBOL	PARAMETER	UNITS	MIN	ТҮР	MAX
Vcc	Supply Voltage	V	4.5	5.0	5.5
Тор	Operating Temperature	°C	-40	+25	+85

PIN DESCRIPTIONS

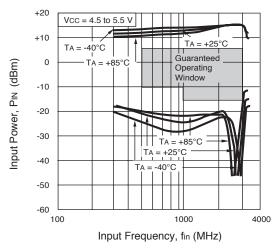
Pir	n No.	Pin	Applied	Pin		Description				
UPB1506GV	UPB1507GV	Name	Voltage (V)	Voltage (V)						
2	1	IN	_	2.9	Signal input pin. This pin should be coupled to the source with a capacitor (eg 1000 pF).					
3	8	ĪN	-	2.9	Signal input bypass pin. This pin must be equipped with a bypass capacitor (eg 1000 pF) to ground.					
4	5	GND	0	-	Ground pin. Ground pattern on the board should be formed as wide as possible to minimize ground impedance.					
1	3	SW1	H/L	_	Divided ratio input pin. The ratio can be controlled by the following input data to these pins.					
								S	W2	
								Н	L	
							Н	÷64	÷128	
6	6	SW2			SW	V1	L	÷128	÷256	
					These pins should be equipped with a bypass capacitor (e.g. 1000 pF) to ground.					
8	2	Vcc	4.5 to 5.5	_	Power supply pin. This pin must be equipped with bypass capacitor (eg 1000 pF) to ground.					
7	4	OUT	_	2.6 to 4.7	Divided frequency output pin. This pin is designed as an emitter follower output. This pin can be connected to CMOS input due to 1.2 Vp-p MIN output.					
5	7	NC	_	-	No con	nec	tion. This p	pin must be op	pened.	

TYPICAL PERFORMANCE CURVES (TA = +25°C unless otherwise noted)

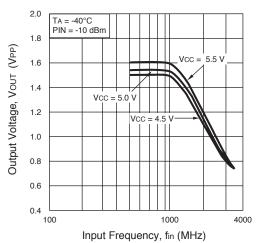
CURRENT vs. VOLTAGE and TEMPERATURE



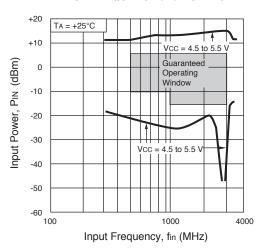
INPUT POWER vs. INPUT FREQUENCY and TEMPERATURE



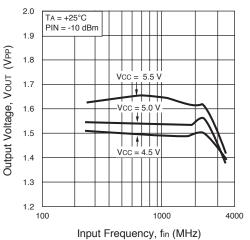
OUTPUT VOLTAGE vs. INPUT FREQUENCY and VOLTAGE



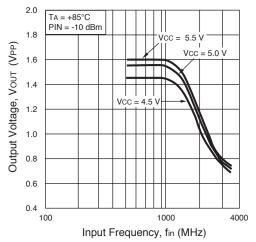
INPUT POWER vs. INPUT FREQUENCY and VOLTAGE



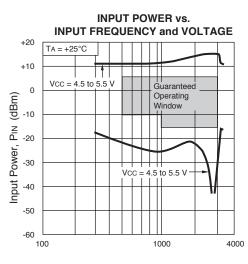
OUTPUT VOLTAGE vs. INPUT FREQUENCY and VOLTAGE



OUTPUT VOLTAGE vs. INPUT FREQUENCY and VOLTAGE

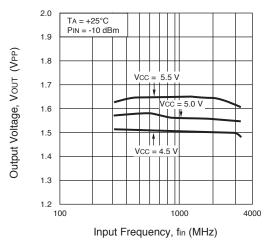


TYPICAL PERFORMANCE CURVES (TA = +25°C unless otherwise noted)

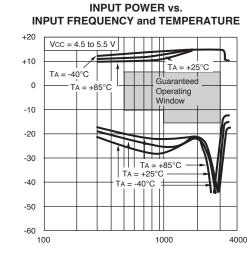


Input Frequency, fin (MHz)

OUTPUT VOLTAGE vs. INPUT FREQUENCY and VOLTAGE

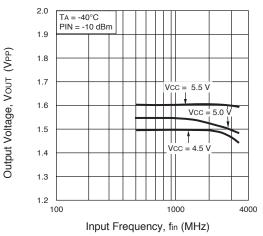


OUTPUT VOLTAGE vs. INPUT FREQUENCY and VOLTAGE 2.0 TA = +85°C $PIN = -10 \, dBm$ 1.9 Output Voltage, Vour (VPP) 1.8 1.7 Vcc = 5.5 V 1.6 VCC = 5.0 V1.5 <u>| |</u>†| |[VCC = 4.5 V 1.4 1.3 1.2 100 1000 4000 Input Frequency, fin (MHz)

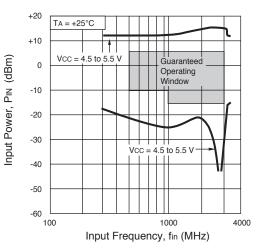


Input Frequency, fin (MHz)

OUTPUT VOLTAGE vs. INPUT FREQUENCY and VOLTAGE

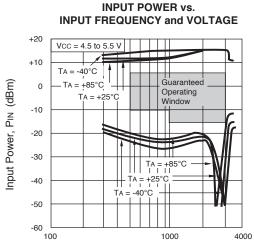


INPUT POWER vs. INPUT FREQUENCY and VOLTAGE



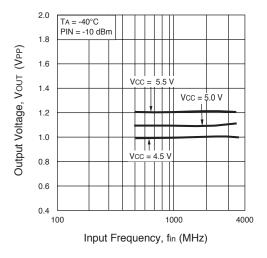
Input Power, Pin (dBm)

TYPICAL PERFORMANCE CURVES (TA = +25°C unless otherwise noted)

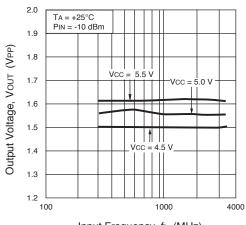


Input Frequency, fin (MHz)

OUTPUT VOLTAGE vs. INPUT FREQUENCY and VOLTAGE

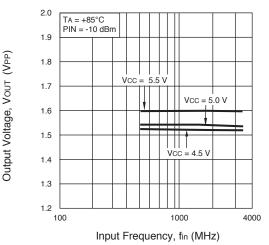


OUTPUT VOLTAGE vs. INPUT FREQUENCY and VOLTAGE



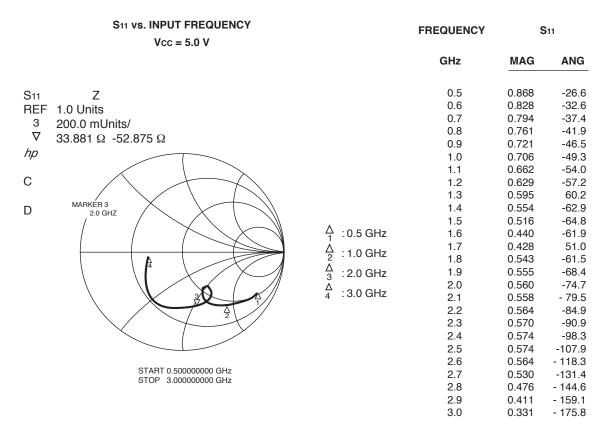
Input Frequency, fin (MHz)

OUTPUT VOLTAGE vs. INPUT FREQUENCY and VOLTAGE



Downloaded from Elcodis.com electronic components distributor

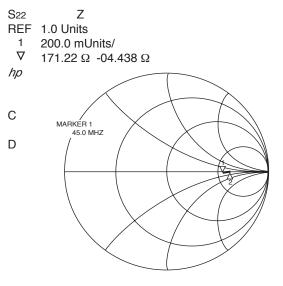
UPB1506GV



UPB1506GV

S22 vs. OUTPUT FREQUENCY

Divide by 64 mode, Vcc = 5.0 V

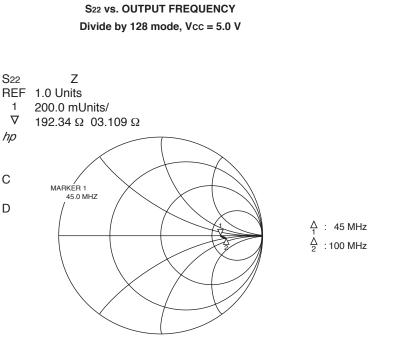


START 0.045000000 GHz STOP 0.100000000 GHz

MHz MAG ANG 45.0 0.542 -1.4 50.0 0.602 -0.3 55.0 0.616 0.0 60.0 0.605 1.1 65.0 0.609 0.7
50.0 0.602 -0.3 55.0 0.616 0.0 60.0 0.605 1.1
55.0 0.616 0.0 60.0 0.605 1.1
60.0 0.605 1.1
65.0 0.609 0.7
70.0 0.616 0.3
75.0 0.620 0.1
80.0 0.622 0.0
85.0 0.619 0.6
90.0 0.610 0.9
95.0 0.626 -0.7
100.0 0.623 -1.7

 $\begin{array}{c} \Delta \\ 1 \end{array}$: 45 MHz $\begin{array}{c} \Delta \\ 2 \end{array}$: 100 MHz

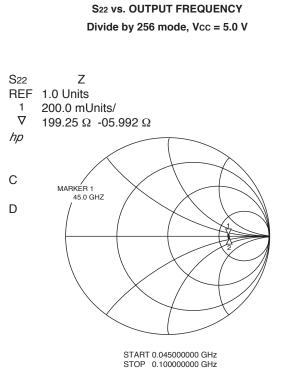
UPB1506GV



START 0.045000000 GHz STOP 0.100000000 GHz

FREQUENCY	S 22	
MHz	MAG	ANG
45.0	0.590	-0.4
50.0	0.604	-1.0
55.0	0.610	-1.1
60.0	0.607	-0.8
65.0	0.548	-5.9
70.0	0.630	-0.0
75.0	0.615	-1.0
80.0	0.618	-1.4
85.0	0.617	-1.2
90.0	0.616	-2.2
95.0	0.623	-2.4
100.0	0.624	-2.3

UPB1506GV

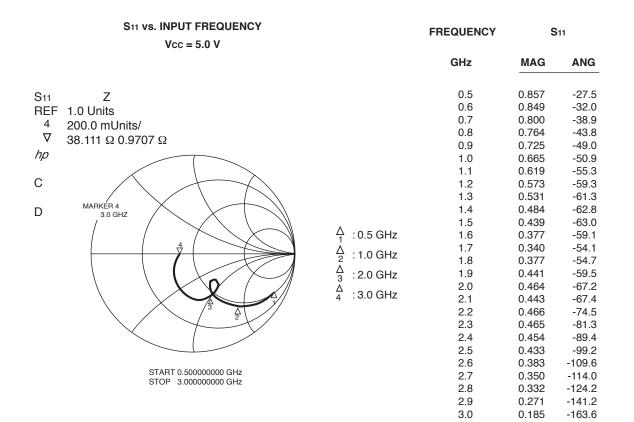


FREQUENCY	S	22
MHz	MAG	ANG
45.0	0.601	-0.9
50.0	0.609	-1.6
55.0	0.611	-1.5
60.0	0.620	-1.4
65.0	0.607	-2.1
70.0	0.615	-1.9
75.0	0.613	-3.2
80.0	0.611	-2.8
85.0	0.607	-2.5
90.0	0.605	-2.4
95.0	0.610	-3.0
100.0	0.608	-2.8

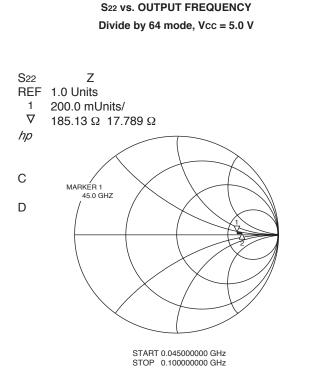
 $\frac{\Delta}{2}$: 100 MHz

 Δ_1 : 45 MHz

UPB1507GV



UPB1507GV



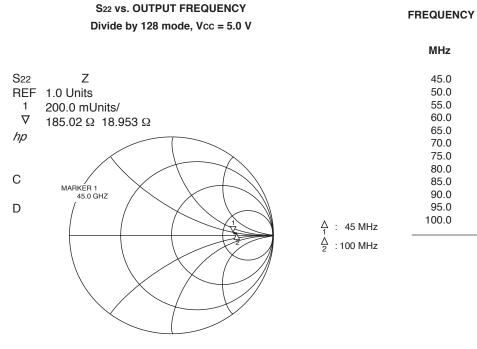
FREQUENCY	S	22
MHz	MAG	ANG
45.0	0.580	3.4
50.0	0.572	2.5
55.0	0.574	3.0
60.0	0.574	2.7
65.0	0.584	3.0
70.0	0.587	2.6
75.0	0.592	2.4
80.0	0.587	2.6
85.0	0.589	2.9
90.0	0.591	2.9
95.0	0.573	1.7
100.0	0.604	2.9

∆ 2 : 100 MHz

: 45 MHz

4

UPB1507GV

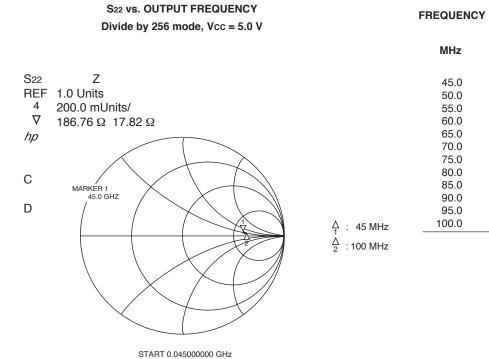


START 0.045000000 GHz STOP 0.100000000 GHz

MHz	MAG	ANG
45.0 50.0 55.0 60.0 65.0 70.0 75.0 80.0 85.0	0.578 0.571 0.572 0.576 0.584 0.587 0.589 0.589 0.589	3.2 2.8 3.3 3.0 3.1 2.8 2.4 2.8 3.0
90.0 95.0	0.593 0.598	2.8 3.0
100.0	0.602	2.9

S22

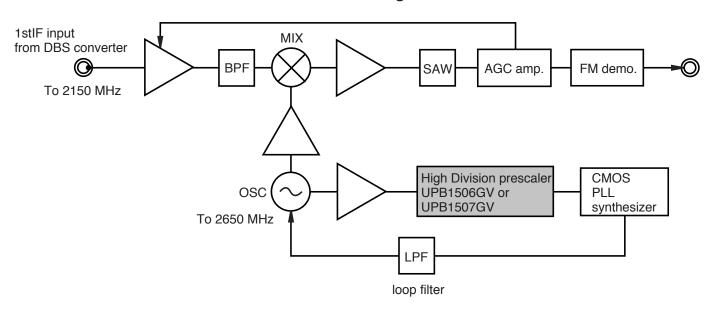
<u>UPB1507GV</u>



STOP 0.10000000 GHz

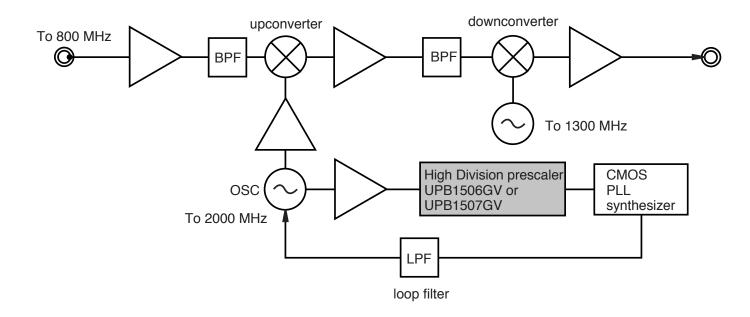
S22 MAG ANG 0.580 3.0 0.572 2.8 0.571 2.9 0.576 2.9 0.585 3.2 0.590 2.8 0.589 2.5 0.590 2.6 0.588 2.9 0.597 2.9 0.600 3.1 0.601 3.1

SYSTEM APPLICATION EXAMPLE

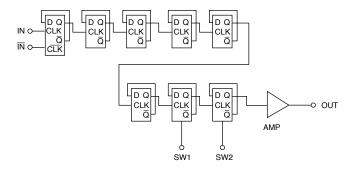


RF unit block of Analog DBS tuners

RF unit block of Analog CATV converter

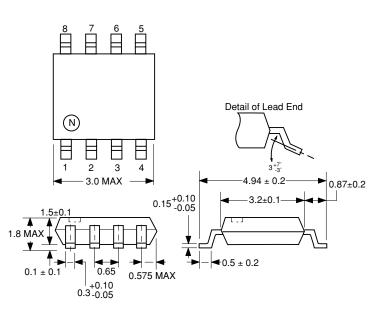


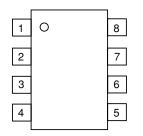
INTERNAL BLOCK DIAGRAM



OUTLINE DIMENSIONS (Units in mm)

PACKAGE OUTLINE S08





PIN CONNECTIONS

Pin No.	UPB1506GV	UPB1507GV
1	SW1	IN
2	IN	Vcc
3	IN	SW1
4	GND	OUT
5	OPEN	GND
6	SW2	SW2
7	OUT	OPEN
8	Vcc	ĪN

ORDERING INFORMATION

PART NUMBER	QUANTITY	MARKING
UPB1506GV-E1	1000/Reel	1506
UPB1507GV-E1-A	1000/Reel	1507

Note:

1. Embossed tape 8 mm wide.

Pin 1 is in the tape pull-out direction.

Life Support Applications

These NEC products are not intended for use in life support devices, appliances, or systems where the malfunction of these products can reasonably be expected to result in personal injury. The customers of CEL using or selling these products for use in such applications do so at their own risk and agree to fully indemnify CEL for all damages resulting from such improper use or sale.

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7/22/2004

NEC



Subject: Compliance with EU Directives

CEL certifies, to its knowledge, that semiconductor and laser products detailed below are compliant with the requirements of European Union (EU) Directive 2002/95/EC Restriction on Use of Hazardous Substances in electrical and electronic equipment (RoHS) and the requirements of EU Directive 2003/11/EC Restriction on Penta and Octa BDE.

CEL Pb-free products have the same base part number with a suffix added. The suffix –A indicates that the device is Pb-free. The –AZ suffix is used to designate devices containing Pb which are exempted from the requirement of RoHS directive (*). In all cases the devices have Pb-free terminals. All devices with these suffixes meet the requirements of the RoHS directive.

This status is based on CEL's understanding of the EU Directives and knowledge of the materials that go into its products as of the date of disclosure of this information.

Restricted Substance per RoHS	Concentration Limit per RoHS (values are not yet fixed)	Concentration contained in CEL devices	
Lead (Pb)	< 1000 PPM	-A-AZNot Detected(*)	
Mercury	< 1000 PPM	Not Detected	
Cadmium	< 100 PPM	Not Detected	
Hexavalent Chromium	< 1000 PPM	Not Detected	
РВВ	< 1000 PPM	Not Detected	
PBDE	< 1000 PPM	Not Detected	

If you should have any additional questions regarding our devices and compliance to environmental standards, please do not hesitate to contact your local representative.

In no event shall CEL's liability arising out of such information exceed the total purchase price of the CEL part(s) at issue sold by CEL to customer on an annual basis.

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