National Semiconductor

March 2005

LM4041
Precision Micropower Shunt Voltage Reference

General Description

Ideal for space critical applications, the LM4041 precision voltage reference is available in the sub-miniature SC70 and SOT-23 surface-mount packages. The LM4041's advanced design eliminates the need for an external stabilizing capacitor while ensuring stability with any capacitive load, thus making the LM4041 easy to use. Further reducing design effort is the availability of a fixed (1.225V) and adjustable reverse breakdown voltage. The minimum operating current is 60 µA for the LM4041-1.2 and the LM4041-ADJ. Both versions have a maximum operating current of 12 mA.

The LM4041 utilizes fuse and zener-zap reverse breakdown or reference voltage trim during wafer sort to ensure that the prime parts have an accuracy of better than ±0.1% (A grade) at 25˚C. Bandgap reference temperature drift curvature correction and low dynamic impedance ensure stable reverse breakdown voltage accuracy over a wide range of operating temperatures and currents.

Features

- Small packages: SOT-23, TO-92, and SC70
- No output capacitor required
- Tolerates capacitive loads
- Reverse breakdown voltage options of 1.225V and adjustable

Key Specifications (LM4041-1.2)

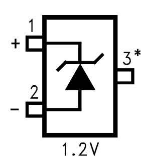
■ Output voltage tolerance (A grade, 25˚C)	±0.1%(max)
■ Low output noise (10 Hz to 10kHz)	$20\mu V_{rms}$
■ Wide operating current range	60µA to 12mA
■ Industrial temperature range	−40˚C to +85˚C
■ Extended temperature range	−40˚C to +125˚C
■ Low temperature coefficient	100 ppm/˚C (max)

Applications

- Portable, Battery-Powered Equipment
- Data Acquisition Systems
- Instrumentation
- Process Control
- Energy Management
- Automotive
- Precision Audio Components

Connection Diagrams

SOT-23



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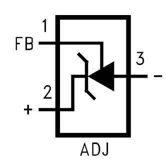
*This pin must be left floating or connected to pin 2.



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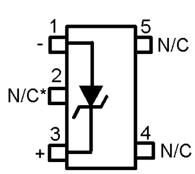
Top View
See NS Package Number MF03A
(JEDEC Registration TO-236AB)

SC-70



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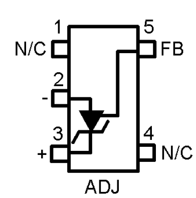

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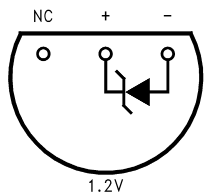
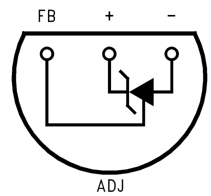
Top View
See NS Package Number MAA05A

Connection Diagrams (Continued)

TO-92





Bottom View
See NS Package Number Z03A

Ordering Information

Reverse Breakdown Voltage Tolerance at 25˚C and Average Reverse Breakdown Voltage Temperature Coefficient	Package					NS Package Number
	M3 (SOT-23)		M7 (SC70)		Z (TO-92)	
	Supplied as 1000 Units Tape and Reel	Supplied as 3000 Units Tape and Reel	Supplied as 1000 Units Tape and Reel	Supplied as 3000 Units Tape and Reel		
±0.1%, 100 ppm/˚C max (A grade)	LM4041AIM3-1.2	LM4041AIM3X-1.2			LM4041AIZ-1.2	MF03A, Z03A
±0.2%, 100 ppm/˚C max (B grade)	LM4041BIM3-1.2	LM4041BIM3X-1.2	LM4041BIM7-1.2	LM4041BIM7X-1.2	LM4041BIZ-1.2	MF03A, Z03A, MAA05A
±0.5%, 100 ppm/˚C max (C grade)	LM4041CEM3-1.2 LM4041CIM3-1.2 LM4041CEM3-ADJ LM4041CIM3-ADJ	LM4041CEM3X-1.2 LM4041CIM3X-1.2 LM4041CEM3X-ADJ LM4041CIM3X-ADJ	LM4041CIM7-1.2 LM4041CIM7-ADJ	LM4041CIM7X-1.2 LM4041CIM7X-ADJ	LM4041CIZ-1.2 LM4041CIZ-ADJ	MF03A, Z03A, MAA05A
±1.0%, 150 ppm/˚C max (D grade)	LM4041DEM3-1.2 LM4041DIM3-1.2 LM4041DEM3-ADJ LM4041DIM3-ADJ	LM4041DEM3X-1.2 LM4041DIM3X-1.2 LM4041DEM3X-ADJ LM4041DIM3X-ADJ	LM4041DIM7-1.2 LM4041DIM7-ADJ	LM4041DIM7X-1.2 LM4041DIM7X-ADJ	LM4041DIZ-1.2 LM4041DIZ-ADJ	MF03A, Z03A, MAA05A
±2.0%, 150 ppm/˚C max (E grade)	LM4041EEM3-1.2 LM4041EIM3-1.2	LM4041EEM3X-1.2 LM4041EIM3X-1.2	LM4041EIM7-1.2	LM4041EIM7X-1.2	LM4041EIZ-1.2	MF03A, Z03A, MAA05A

SOT-23 and SC70 Package Marking Information

Only three fields of marking are possible on the SOT-23's and SC70's small surface. This table gives the meaning of the three fields.

Part Marking	Field Definition
R1A (SOT-23 Only)	First Field:
R1B	R = Reference
R1C	Second Field:
R1D	1 = 1.225V Voltage Option
R1E	A = Adjustable
	Third Field:
RAC	A–E = Initial Reverse Breakdown
RAD	Voltage or Reference Voltage Tolerance
	A = ±0.1%, B = ±0.2%, C = ±0.5%, D = ±1.0%, E = ±2.0%

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Reverse Current	20 mA
Forward Current	10 mA
Maximum Output Voltage	
(LM4041-ADJ)	15V
Power Dissipation (T_A = 25˚C) (Note 2)	
M3 Package	306 mW
Z Package	550 mW
M7 Package	241mW
Storage Temperature	−65˚C to +150˚C
Lead Temperature	
M3 Packages	
Vapor phase (60 seconds)	+215˚C
Infrared (15 seconds)	+220˚C
Z Package	
Soldering (10 seconds)	+260˚C
ESD Susceptibility	
Human Body Model (Note 3)	2 kV
Machine Model (Note 3)	200V

See AN-450 "Surface Mounting Methods and Their Effect on Product Reliability" for other methods of soldering surface mount devices.

Operating Ratings (Notes 1, 2)

Temperature Range	(T_{min} ≤ T_A ≤ T_{max})
Industrial Temperature Range	−40˚C ≤ T_A ≤ +85˚C
Extended Temperature Range	−40˚C ≤ T_A ≤ +125˚C
Reverse Current	
LM4041-1.2	60 µA to 12 mA
LM4041-ADJ	60 µA to 12 mA
Output Voltage Range	
LM4041-ADJ	1.24V to 10V

LM4041-1.2 Electrical Characteristics (Industrial Temperature Range)

Boldface limits apply for T_A = T_J = T_{MIN} to T_{MAX}; all other limits T_A = T_J = 25˚C. The grades A and B designate initial Reverse Breakdown Voltage tolerances of ±0.1% and ±0.2%, respectively.

Symbol	Parameter	Conditions	Typical (Note 4)	LM4041AIM3 LM4041AIZ Limits (Note 5)	LM4041BIM3 LM4041BIZ LM4041BIM7 Limits (Note 5)	Units (Limit)
V_R	Reverse Breakdown Voltage	I_R = 100 µA	1.225			V
	Reverse Breakdown Voltage Tolerance (Note 6)	I_R = 100 µA		±1.2 **±9.2**	±2.4 **±10.4**	mV (max) mV (max)
I_{RMIN}	Minimum Operating Current		45	60 **65**	60 **65**	µA µA (max) µA (max)
$\Delta V_R/\Delta T$	Average Reverse Breakdown Voltage Temperature Coefficient (Note 6)	I_R= 10 mA I_R = 1 mA I_R = 100 µA	±20 ±15 ±15	**±100**	**±100**	ppm/˚C ppm/˚C (max) ppm/˚C
$\Delta V_R/\Delta I_R$	Reverse Breakdown Voltage Change with Operating Current Change (Note 9)	I_{RMIN} ≤ I_R ≤ 1 mA	0.7	1.5 **2.0**	1.5 **2.0**	mV mV (max) mV (max)
		1 mA ≤ I_R ≤ 12 mA	4.0	6.0 **8.0**	6.0 **8.0**	mV mV (max) mV (max)
Z_R	Reverse Dynamic Impedance	I_R = 1 mA, f = 120 Hz, I_{AC}= 0.1 I_R	0.5	1.5	1.5	Ω Ω (max)
e_N	Wideband Noise	I_R = 100 µA 10 Hz ≤ f ≤ 10 kHz	20			μV_{rms}
ΔV_R	Reverse Breakdown Voltage Long Term Stability	t = 1000 hrs T = 25˚C ±0.1˚C I_R = 100 µA	120			ppm
V_{HYST}	Thermal Hysteresis (Note 10)	ΔT = −40˚C to +125˚C	0.08			%

LM4041-1.2
Electrical Characteristics (Industrial Temperature Range) (Continued)

LM4041-1.2
Electrical Characteristics (Industrial Temperature Range)

Boldface limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX}; all other limits $T_A = T_J = 25^{\circ}C$. The grades C, D and E designate initial Reverse Breakdown Voltage tolerances of ±0.5%, ±1.0% and ±2.0%, respectively.

Symbol	Parameter	Conditions	Typical (Note 4)	LM4041CIM3 LM4041CIZ LM4041CIM7 Limits (Note 5)	LM4041DIM3 LM4041DIZ LM4041DIM7 Limits (Note 5)	LM4041EIM3 LM4041EIZ LM4041EIM7 Limits (Note 5)	Units (Limit)
V_R	Reverse Breakdown Voltage	I_R = 100 µA	1.225				V
	Reverse Breakdown Voltage Tolerance (Note 6)	I_R = 100 µA		±6	±12	±25	mV (max)
				±14	**±24**	**±36**	mV (max)
I_{RMIN}	Minimum Operating Current		45				µA
				60	65	65	µA (max)
				65	**70**	**70**	µA (max)
$\Delta V_R/\Delta T$	V_R Temperature Coefficient (Note 6)	I_R = 10 mA	±20				ppm/˚C
		I_R = 1 mA	±15	**±100**	**±150**	**±150**	ppm/˚C (max)
		I_R= 100 µA	±15				ppm/˚C
$\Delta V_R/\Delta I_R$	Reverse Breakdown Voltage Change with Operating Current Change (Note 9)	$I_{RMIN} \leq I_R \leq$ 1 mA	0.7				mV
				1.5	2.0	2.0	mV (max)
				2.0	**2.5**	**2.5**	mV (max)
		1 mA $\leq I_R \leq$ 12 mA	2.5				mV
				6.0	8.0	8.0	mV (max)
				8.0	**10.0**	**10.0**	mV (max)
Z_R	Reverse Dynamic Impedance	I_R = 1 mA, f = 120 Hz	0.5				Ω
		$I_{AC} = 0.1\ I_R$		1.5	2.0	2.0	Ω(max)
e_N	Wideband Noise	I_R = 100 µA 10 Hz ≤ f ≤ 10 kHz	20				μV_{rms}
ΔV_R	Reverse Breakdown Voltage Long Term Stability	t = 1000 hrs T = 25˚C ±0.1˚C I_R = 100 µA	120				ppm
V_{HYST}	Thermal Hysteresis (Note 10)	ΔT = −40˚C to +125˚C	0.08				%

LM4041-1.2
Electrical Characteristics (Extended Temperature Range)

Boldface limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX}; all other limits $T_A = T_J = 25^\circ C$. The grades C, D and E designate initial Reverse Breakdown Voltage tolerance of ±0.5%, ±1.0% and ±2.0% respectively.

Symbol	Parameter	Conditions	Typical (Note 4)	LM4041CEM3 Limits (Note 5)	LM4041DEM3 Limits (Note 5)	LM4041EEM3 Limits (Note 5)	Units (Limit)
V_R	Reverse Breakdown Voltage	I_R = 100 µA	1.225				V
	Reverse Breakdown Voltage Error (Note 6)	I_R = 100 µA		±6 **±18.4**	±12 **±31**	±25 **±43**	mV (max) mV (max)
I_{RMIN}	Minimum Operating Current		45	60 **68**	65 **73**	65 **73**	µA µA (max) µA (max)
$\Delta V_R/\Delta T$	VR Temperature Coefficient(Note 6)	I_R= 10 mA I_R = 1 mA I_R = 100 µA	±20 ±15 ±15	**±100**	**±150**	**±150**	ppm/˚C ppm/˚C (max) ppm/˚C
$\Delta V_R/\Delta I_R$	Reverse Breakdown Change with Current (Note 9)	$I_{RMIN} \le I_R \le$ 1.0 mA	0.7	1.5 **2.0**	2.0 **2.5**	2.0 **2.5**	mV mV (max) mV (max)
		1 mA $\le I_R \le$ 12 mA	2.5	6.0 **8.0**	8.0 **10.0**	8.0 **10.0**	mV mV (max) mV (max)
Z_R	Reverse Dynamic Impedance	I_R = 1 mA, f = 120 Hz, I_{AC}= 0.1 I_R	0.5	**1.5**	**2.0**	**2.0**	Ω Ω (max)
e_N	Noise Voltage	I_R = 100 µA 10 Hz ≤ f ≤ 10 kHz	20				μV_{rms}
ΔV_R	Long Term Stability (Non-Cumulative)	t = 1000 hrs T = 25˚C ±0.1˚C I_R = 100 µA	120				ppm
V_{HYST}	Thermal Hysteresis (Note 10)	ΔT = −40˚C to +125˚C	0.08				%

LM4041-ADJ (Adjustable) Electrical Characteristics (Industrial Temperature Range)

Boldface limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX}; all other limits T_J = 25˚C unless otherwise specified (SOT-23, see (Note 7)), $I_{RMIN} \leq I_R \leq$ 12 mA, $V_{REF} \leq V_{OUT} \leq$ 10V. The grades C and D designate initial Reference Voltage Tolerances of ±0.5% and ±1%, respectively for V_{OUT} = 5V.

Symbol	Parameter	Conditions	Typical (Note 4)	LM4041CIM3 LM4041CIZ LM4041CIM7 (Note 5)	LM4041DIM3 LM4041DIZ LM4041DIM7 (Note 5)	Units (Limit)
V_{REF}	Reference Voltage	I_R = 100 µA, V_{OUT} = 5V	1.233			V
	Reference Voltage Tolerance (Note 8)	I_R = 100 µA, V_{OUT} = 5V		±6.2 **±14**	±12 **±24**	mV (max) mV (max)
I_{RMIN}	Minimum Operating Current		45	60 **65**	65 **70**	µA µA (max) µA (max)
$\Delta V_{REF}/\Delta I_R$	Reference Voltage Change with Operating Current Change (Note 9)	$I_{RMIN} \leq I_R \leq$ 1 mA SOT-23: $V_{OUT} \geq$ 1.6V (Note 7)	0.7	1.5 **2.0**	2.0 **2.5**	mV mV (max) mV (max)
		1 mA $\leq I_R \leq$ 12 mA SOT-23: $V_{OUT} \geq$ 1.6V (Note 7)	2	4 **6**	6 **8**	mV mV (max) mV (max)
$\Delta V_{REF}/\Delta V_O$	Reference Voltage Change with Output Voltage Change	I_R = 1 mA	−1.55	−2.0 **−2.5**	−2.5 **−3.0**	mV/V mV/V (max) mV/V (max)
I_{FB}	Feedback Current		60	100 **120**	150 **200**	nA nA (max) nA (max)
$\Delta V_{REF}/\Delta T$	Average Reference Voltage Temperature Coefficient (Note 8)	V_{OUT} = 5V, I_R = 10 mA I_R = 1 mA I_R = 100 µA	20 15 15	**±100**	**±150**	ppm/˚C ppm/˚C (max) ppm/˚C
Z_{OUT}	Dynamic Output Impedance	I_R = 1 mA, f = 120 Hz, I_{AC} = 0.1 I_R $V_{OUT} = V_{REF}$ V_{OUT} = 10V	0.3 2			Ω Ω
e_N	Wideband Noise	I_R = 100 µA $V_{OUT} = V_{REF}$ 10 Hz $\leq$ f $\leq$ 10 kHz	20			μV_{rms}
ΔV_{REF}	Reference Voltage Long Term Stability	t = 1000 hrs, I_R = 100 µA T = 25˚C ±0.1˚C	120			ppm
V_{HYST}	Thermal Hysteresis (Note 10)	ΔT = −40˚C to +125˚C	0.08			%

LM4041-ADJ (Adjustable) Electrical Characteristics (Extended Temperature Range)

Boldface limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX}; all other limits T_J = 25˚C unless otherwise specified (SOT-23, see (Note 7)), $I_{RMIN} \leq I_R \leq$ 12 mA, $V_{REF} \leq V_{OUT} \leq$ 10V. The grades C and D designate initial Reference Voltage Tolerances of ±0.5% and ±1%, respectively for V_{OUT} = 5V.

Symbol	Parameter	Conditions	Typical (Note 4)	LM4041CEM3 (Note 5)	LM4041DEM3 (Note 5)	Units (Limit)
V_{REF}	Reference Voltage	I_R = 100 µA, V_{OUT} = 5V	1.233			V
	Reference Voltage Tolerance (Note 8)	I_R = 100 µA, V_{OUT} = 5V		±6.2 **±18**	±12 **±30**	mV (max) mV (max)
I_{RMIN}	Minimum Operating Current		45	60 **68**	65 **73**	µA µA (max) µA (max)
$\Delta V_{REF}/\Delta I_R$	Reference Voltage Change with Operating Current Change (Note 9)	$I_{RMIN} \leq I_R \leq$ 1 mA SOT-23: $V_{OUT} \geq$ 1.6V (Note 7)	0.7	1.5 **2.0**	2.0 **2.5**	mV mV (max) mV (max)
		1 mA $\leq I_R \leq$ 12 mA SOT-23: $V_{OUT} \geq$ 1.6V(Note 7)	2	8 **6**	10 **8**	mV mV (max) mV (max)
$\Delta V_{REF}/\Delta V_O$	Reference Voltage Change with Output Voltage Change	I_R = 1 mA	−1.55	−2.0 **−3.0**	−2.5 **−4.0**	mV/V mV/V (max) mV/V (max)
I_{FB}	Feedback Current		60	100 **120**	150 **200**	nA nA (max) nA (max)
$\Delta V_{REF}/\Delta T$	Average Reference Voltage Temperature Coefficient (Note 8)	V_{OUT} = 5V, I_R = 10 mA I_R = 1 mA I_R = 100 µA	20 15 15	**±100**	**±150**	ppm/˚C ppm/˚C (max) ppm/˚C
Z_{OUT}	Dynamic Output Impedance	I_R = 1 mA, f = 120 Hz, I_{AC} = 0.1 I_R $V_{OUT} = V_{REF}$ V_{OUT} = 10V	 0.3 2			 Ω Ω
e_N	Wideband Noise	I_R = 100 µA $V_{OUT} = V_{REF}$ 10 Hz $\leq f \leq$ 10 kHz	20			$µV_{rms}$
ΔV_{REF}	Reference Voltage Long Term Stability	t = 1000 hrs, I_R = 100 µA T = 25˚C ±0.1˚C	120			ppm
V_{HYST}	Thermal Hysteresis (Note 10)	ΔT = −40˚C to +125˚C	0.08			%

LM4041-ADJ (Adjustable) Electrical Characteristics (Extended Temperature Range) (Continued)

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics. The guaranteed specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions.

Note 2: The maximum power dissipation must be derated at elevated temperatures and is dictated by T_{Jmax} (maximum junction temperature), θ_{JA} (junction to ambient thermal resistance), and T_A (ambient temperature). The maximum allowable power dissipation at any temperature is $PD_{max} = (T_{Jmax} - T_A)/\theta_{JA}$ or the number given in the Absolute Maximum Ratings, whichever is lower. For the LM4041, T_{Jmax} = 125˚C, and the typical thermal resistance (θ_{JA}), when board mounted, is 326˚C/W for the SOT-23 package, 415˚C/W for the SC70 package and 180˚C/W with 0.4" lead length and 170˚C/W with 0.125" lead length for the TO-92 package.

Note 3: The human body model is a 100 pF capacitor discharged through a 1.5 kΩ resistor into each pin. The machine model is a 200 pF capacitor discharged directly into each pin. All pins are rated at 2kV for Human Body Model, but the feedback pin which is rated at 1kV.

Note 4: Typicals are at T_J = 25˚C and represent most likely parametric norm.

Note 5: Limits are 100% production tested at 25˚C. Limits over temperature are guaranteed through correlation using Statistical Quality Control (SQC) methods. The limits are used to calculate National's AOQL.

Note 6: The boldface (over-temperature) limit for Reverse Breakdown Voltage Tolerance is defined as the room temperature Reverse Breakdown Voltage Tolerance $\pm[(\Delta V_R/\Delta T)(\max \Delta T)(V_R)]$. Where, $\Delta V_R/\Delta T$ is the V_R temperature coefficient, maxΔT is the maximum difference in temperature from the reference point of 25 ˚C to T_{MAX} or T_{MIN}, and V_R is the reverse breakdown voltage. The total over-temperature tolerance for the different grades in the industrial temperature range where maxΔT=65˚C is shown below:

A-grade: ±0.75% = ±0.1% ±100 ppm/˚C x 65˚C
B-grade: ±0.85% = ±0.2% ±100 ppm/˚C x 65˚C
C-grade: ±1.15% = ±0.5% ±100 ppm/˚C x 65˚C
D-grade: ±1.98% = ±1.0% ±150 ppm/˚C x 65˚C
E-grade: ±2.98% = ±2.0% ±150 ppm/˚C x 65˚C

The total over-temperature tolerance for the different grades in the extended temperature range where max ΔT = 100 ˚C is shown below:

B-grade: ±1.2% = ±0.2% ±100 ppm/˚C x 100˚C
C-grade: ±1.5% = ±0.5% ±100 ppm/˚C x 100˚C
D-grade: ±2.5% = ±1.0% ±150 ppm/˚C x 100˚C
E-grade: ±4.5% = ±2.0% ±150 ppm/˚C x 100˚C

Therefore, as an example, the A-grade LM4041-1.2 has an over-temperature Reverse Breakdown Voltage tolerance of ±1.2V x 0.75% = ±9.2 mV.

Note 7: When $V_{OUT} \leq$ 1.6V, the LM4041-ADJ in the SOT-23 package must operate at reduced I_R. This is caused by the series resistance of the die attach between the die (-) output and the package (-) output pin. See the Output Saturation (SOT-23 only) curve in the Typical Performance Characteristics section.

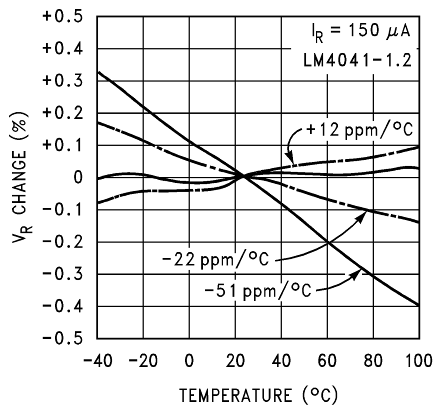
Note 8: Reference voltage and temperature coefficient will change with output voltage. See Typical Performance Characteristics curves.

Note 9: Load regulation is measured on pulse basis from no load to the specified load current. Ouput changes due to die temperature change must be taken into account separately.-

Note 10: Thermal hysteresis is defined as the difference in voltage measured at +25˚C after cycling to temperature -40˚C and the 25˚C measurement after cycling to temperature +125˚C.

Typical Performance Characteristics

Temperature Drift for Different Average Temperature Coefficient



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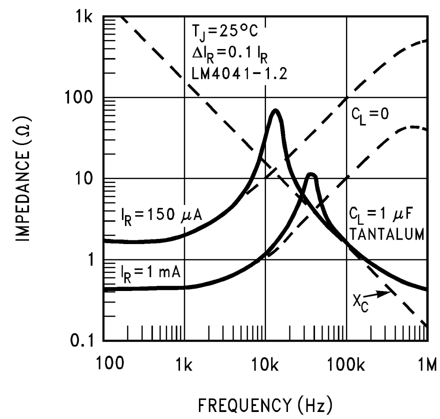
Output Impedance vs Frequency



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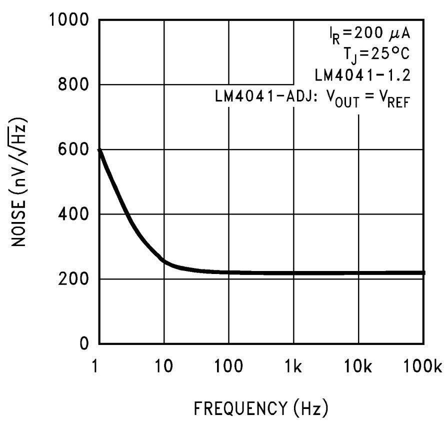
Noise Voltage



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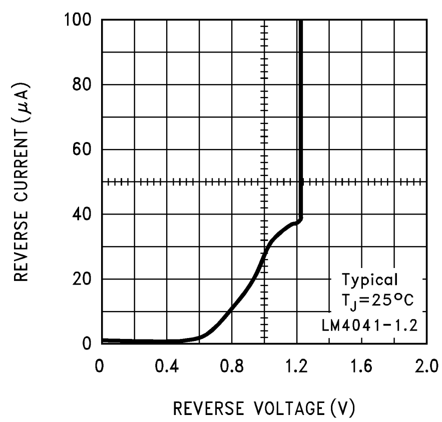
Reverse Characteristics and Minimum Operating Current



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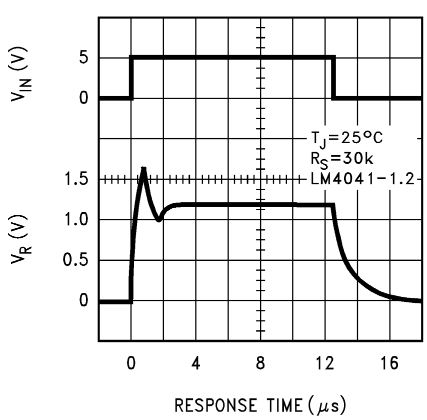
Start-Up Characteristics



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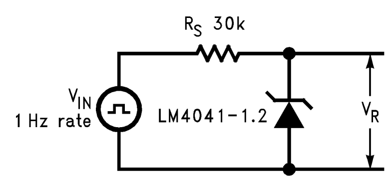

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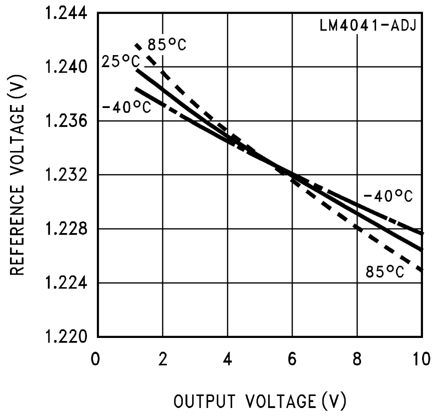
Typical Performance Characteristics (Continued)

Reference Voltage vs Output Voltage and Temperature



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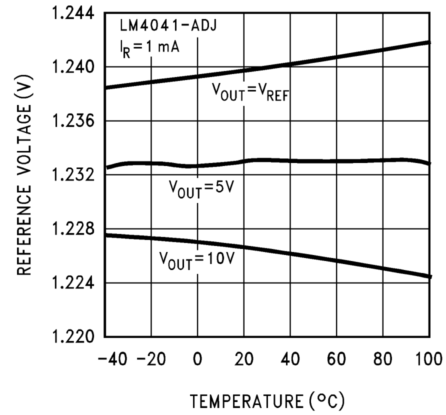
Reference Voltage vs Temperature and Output Voltage



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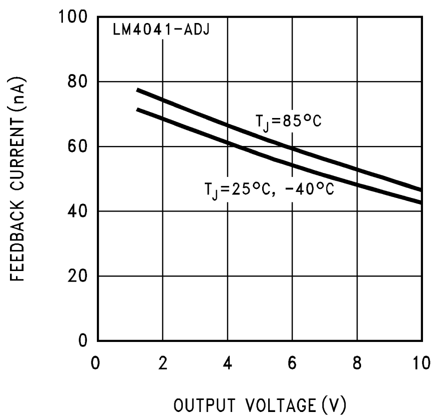
Feedback Current vs Output Voltage and Temperature



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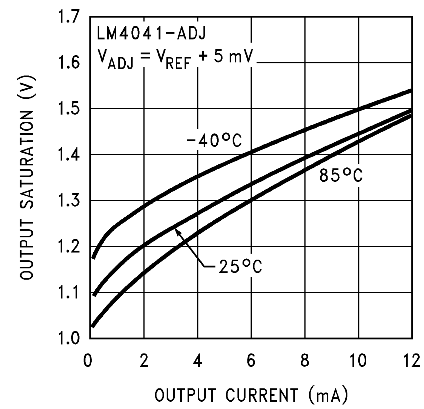
Output Saturation (SOT-23 Only)



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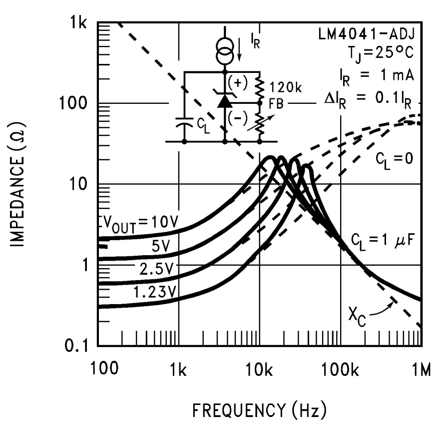
Output Impedance vs Frequency



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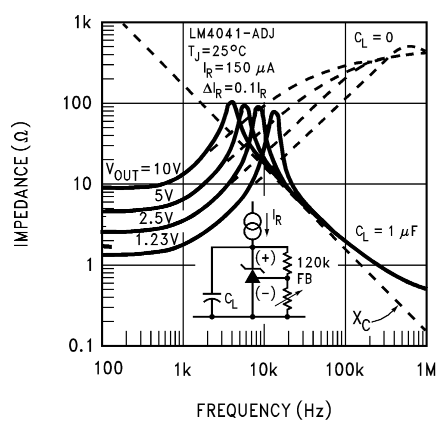
Output Impedance vs Frequency



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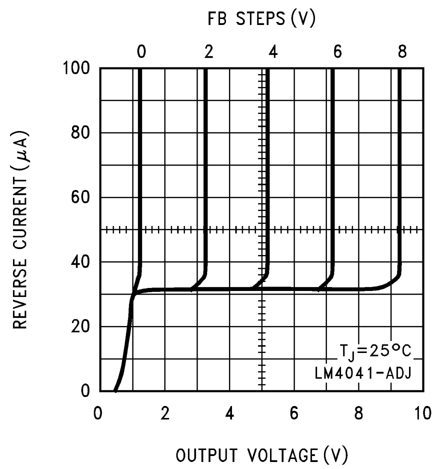
Typical Performance Characteristics (Continued)

Reverse Characteristics



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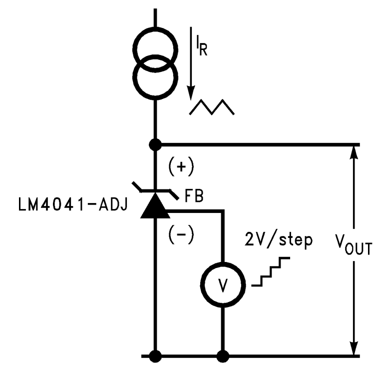

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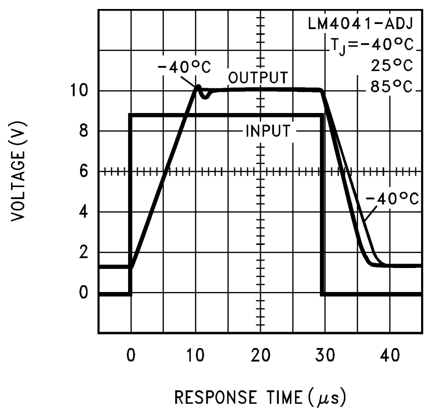
Large Signal Response



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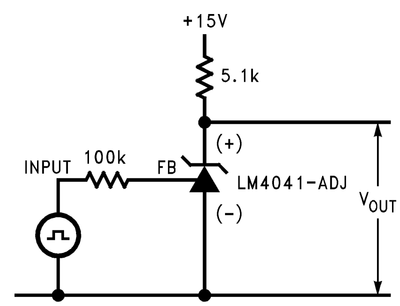
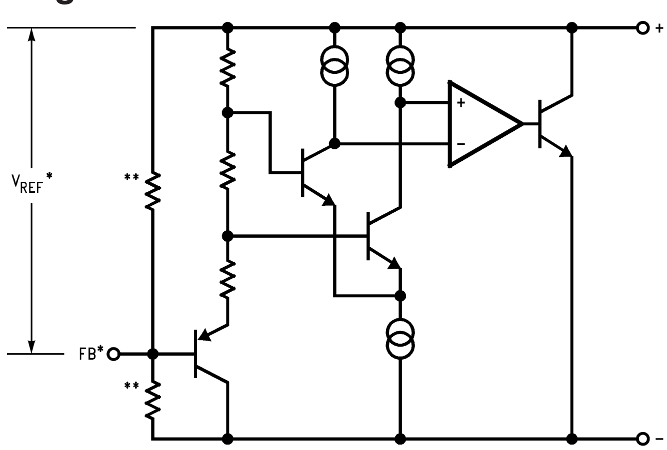


01139218

Functional Block Diagram



01139221

*LM4041-ADJ only

**LM4041-1.2 only

Applications Information

The LM4041 is a precision micro-power curvature-corrected bandgap shunt voltage reference. For space critical applications, the LM4041 is available in the sub-miniature SOT-23 and SC70 surface-mount package. The LM4041 has been designed for stable operation without the need of an external capacitor connected between the "+" pin and the "−" pin. If, however, a bypass capacitor is used, the LM4041 remains stable. Design effort is further reduced with the choice of either a fixed 1.2V or an adjustable reverse breakdown voltage. The minimum operating current is 60 µA for the LM4041-1.2 and the LM4041-ADJ. Both versions have a maximum operating current of 12 mA.

LM4041s using the SOT-23 package have pin 3 connected as the (-) output through the package's die attach interface. Therefore, the LM4041-1.2's pin 3 must be left floating or connected to pin 2 and the LM4041-ADJ's pin 3 is the (-) output.

LM4041s using the SC70 package have pin 2 connected as the (−) output through the packages' die attach interface. Therefore, the LM4041-1.2's pin 2 must be left floating or connected to pin 1, and the LM4041-ADJ's pin 2 is the (−) output.

The typical thermal hysteresis specification is defined as the change in +25˚C voltage measured after thermal cycling. The device is thermal cycled to temperature -40˚C and then measured at 25˚C. Next the device is thermal cycled to temperature +125˚C and again measured at 25˚C. The resulting V_{OUT} delta shift between the 25˚C measurements is thermal hysteresis. Thermal hysteresis is common in precision references and is induced by thermal-mechanical package stress. Changes in environmental storage temperature, operating temperature and board mounting temperature are all factors that can contribute to thermal hysteresis.

In a conventional shunt regulator application *(Figure 1*), an external series resistor (R_S) is connected between the supply voltage and the LM4041. R_S determines the current that flows through the load (I_L) and the LM4041 (I_Q). Since load current and supply voltage may vary, R_S should be small enough to supply at least the minimum acceptable I_Q to the LM4041 even when the supply voltage is at its minimum and the load current is at its maximum value. When the supply voltage is at its maximum and I_L is at its minimum, R_S should be large enough so that the current flowing through the LM4041 is less than 12 mA.

R_S should be selected based on the supply voltage, (V_S), the desired load and operating current, (I_L and I_Q), and the LM4041's reverse breakdown voltage, V_R.

$$R_S = \frac{V_S - V_R}{I_L + I_Q}$$

The LM4041-ADJ's output voltage can be adjusted to any value in the range of 1.24V through 10V. It is a function of the internal reference voltage (V_{REF}) and the ratio of the external feedback resistors as shown in *Figure 2* . The output voltage is found using the equation

$$V_O = V_{REF}[(R2/R1) + 1] \quad (1)$$

where V_O is the output voltage. The actual value of the internal V_{REF} is a function of V_O. The "corrected" V_{REF} is determined by

$$V_{REF} = \Delta V_O\ (\Delta V_{REF}/\Delta V_O) + V_Y \quad (2)$$

where

$$V_Y = 1.240\ V$$

and

$$\Delta V_O = (V_O - V_Y)$$

$\Delta V_{REF}/\Delta V_O$ is found in the Electrical Characteristics and is typically −1.55 mV/V. You can get a more accurate indication of the output voltage by replacing the value of V_{REF} in equation (1) with the value found using equation (2).

Note that the actual output voltage can deviate from that predicted using the typical value of $\Delta V_{REF}/\Delta V_O$ in equation (2): for C-grade parts, the worst-case $\Delta V_{REF}/\Delta V_O$ is −2.5 mV/V. For D-grade parts, the worst-case $\Delta V_{REF}/\Delta V_O$ is −3.0 mV/V.

Typical Applications

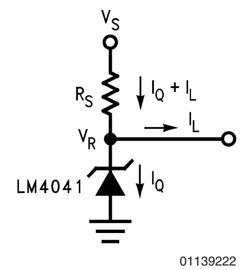
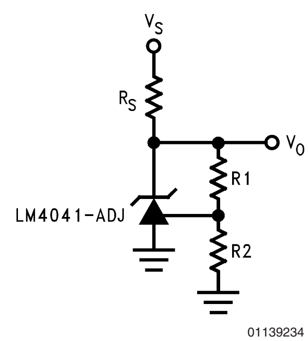


FIGURE 1. Shunt Regulator



$$V_O = V_{REF}[(R2/R1) + 1]$$

FIGURE 2. Adjustable Shunt Regulator

Typical Applications (Continued)

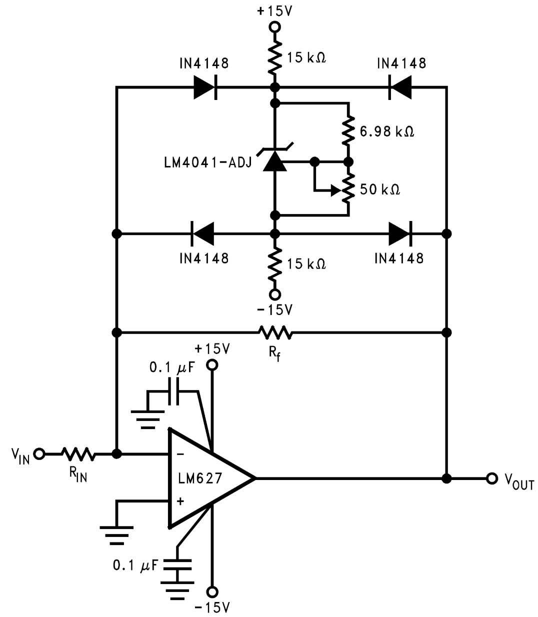


FIGURE 3. Bounded amplifier reduces saturation-induced delays and can prevent succeeding stage damage. Nominal clamping voltage is ±V_O (LM4041's reverse breakdown voltage) +2 diode V_F.

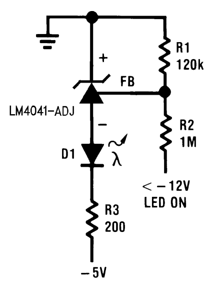


FIGURE 4. Voltage Level Detector

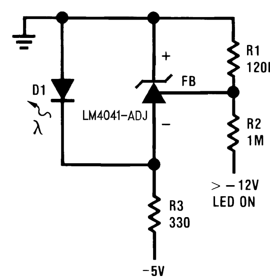


FIGURE 5. Voltage Level Detector

Typical Applications (Continued)

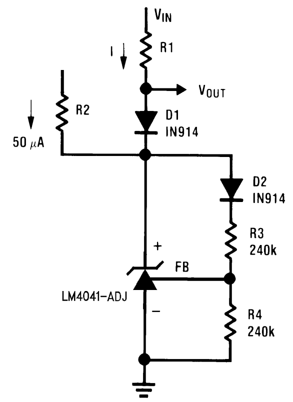


FIGURE 6. Fast Positive Clamp 2.4V + V_{D1}

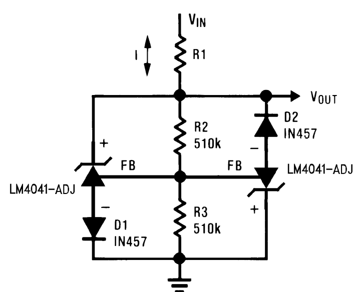


FIGURE 7. Bidirectional Clamp ±2.4V

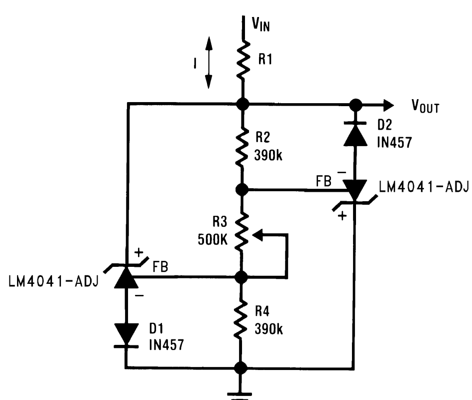


FIGURE 8. Bidirectional Adjustable Clamp ±18V to ±2.4V

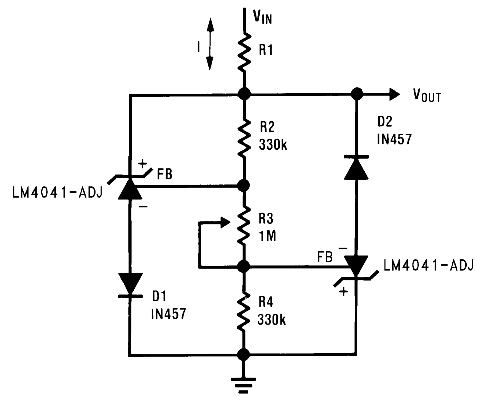


FIGURE 9. Bidirectional Adjustable Clamp ±2.4V to ±6V

Typical Applications (Continued)

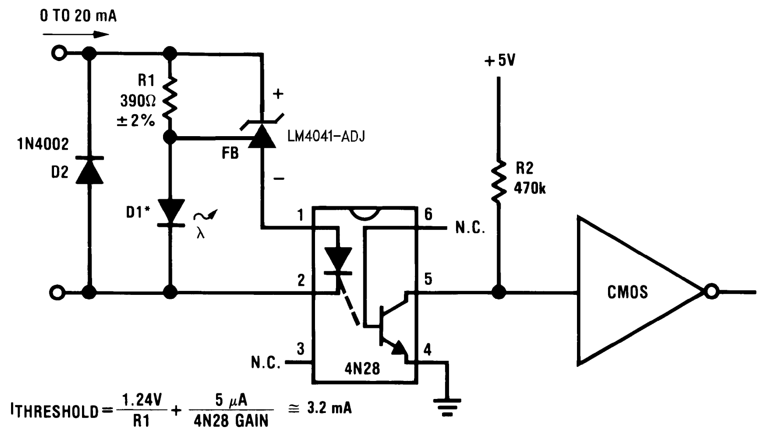


FIGURE 10. Simple Floating Current Detector

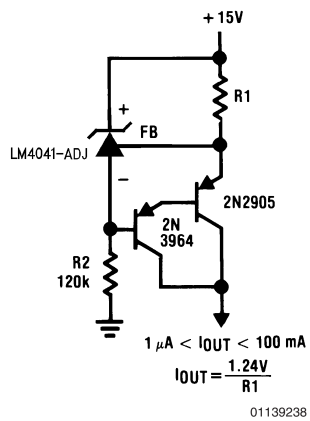


FIGURE 11. Current Source

Note 11: *D1 can be any LED, V_F = 1.5V to 2.2V at 3 mA. D1 may act as an indicator. D1 will be on if $I_{THRESHOLD}$ falls below the threshold current, except with I = 0.

Typical Applications (Continued)

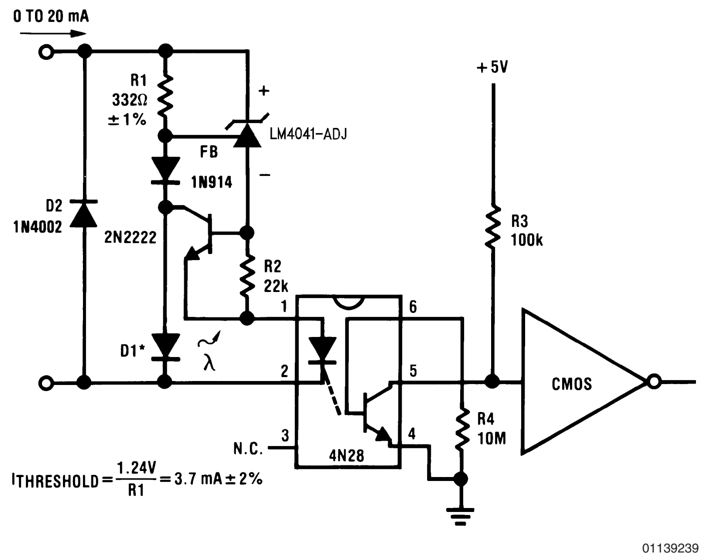
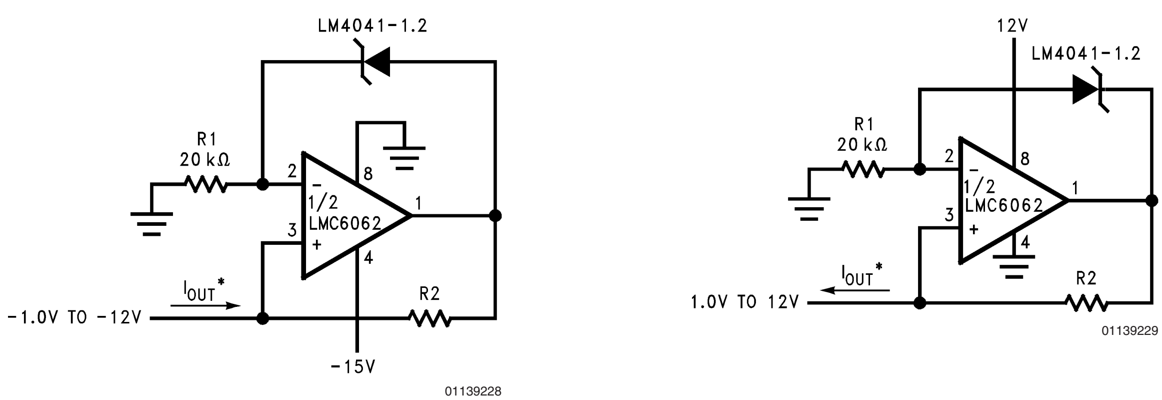


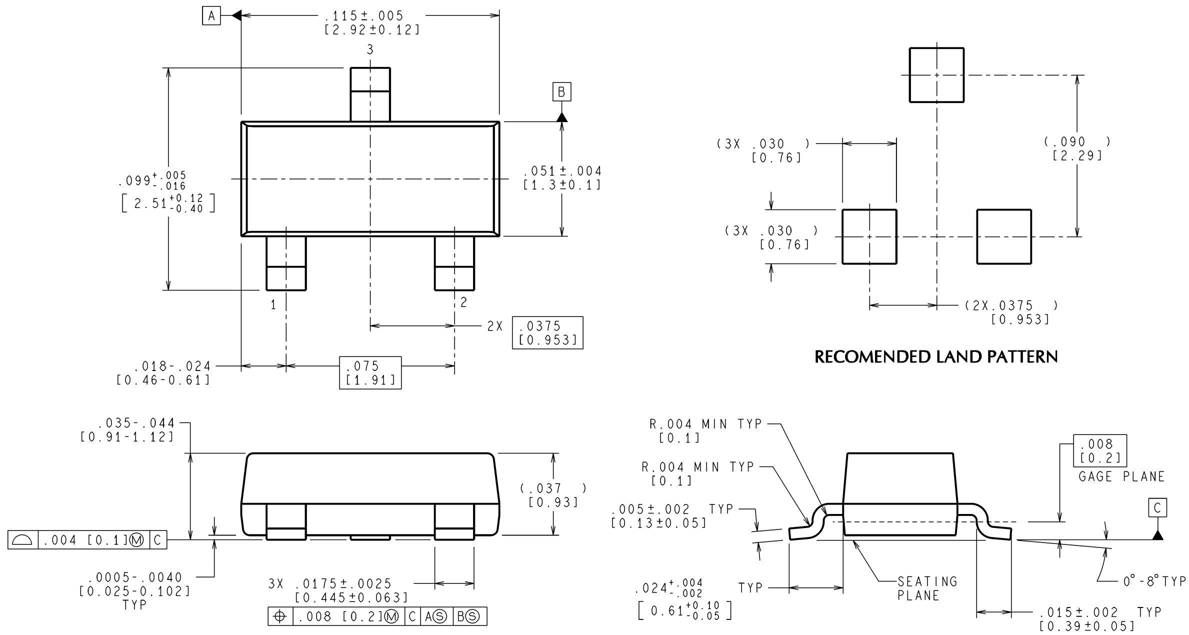
FIGURE 12. Precision Floating Current Detector



$$^{*}I_{OUT} = \frac{1.2V}{R2}$$

FIGURE 13. Precision 1 µA to 1 mA Current Sources

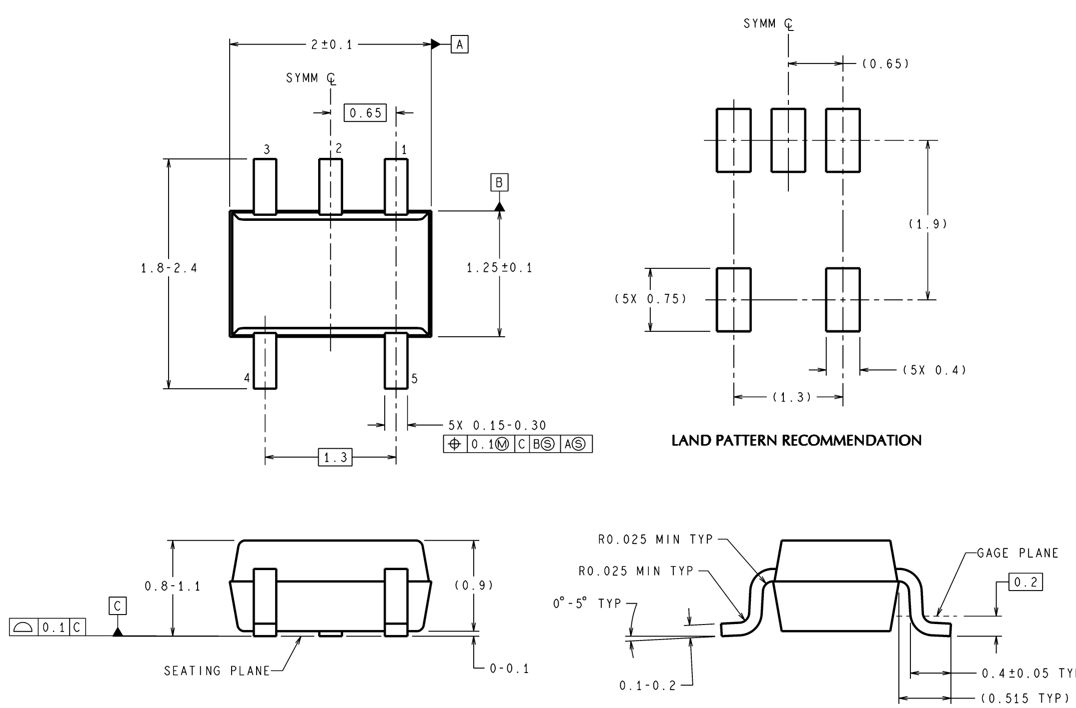
Physical Dimensions inches (millimeters) unless otherwise noted



CONTROLLING DIMENSION IS INCH
VALUES IN [ ] ARE MILLIMETERS

MF03A (Rev B)

Plastic Surface Mount Package (M3)
NS Package Number MF03A
(JEDEC Registration TO-236AB)

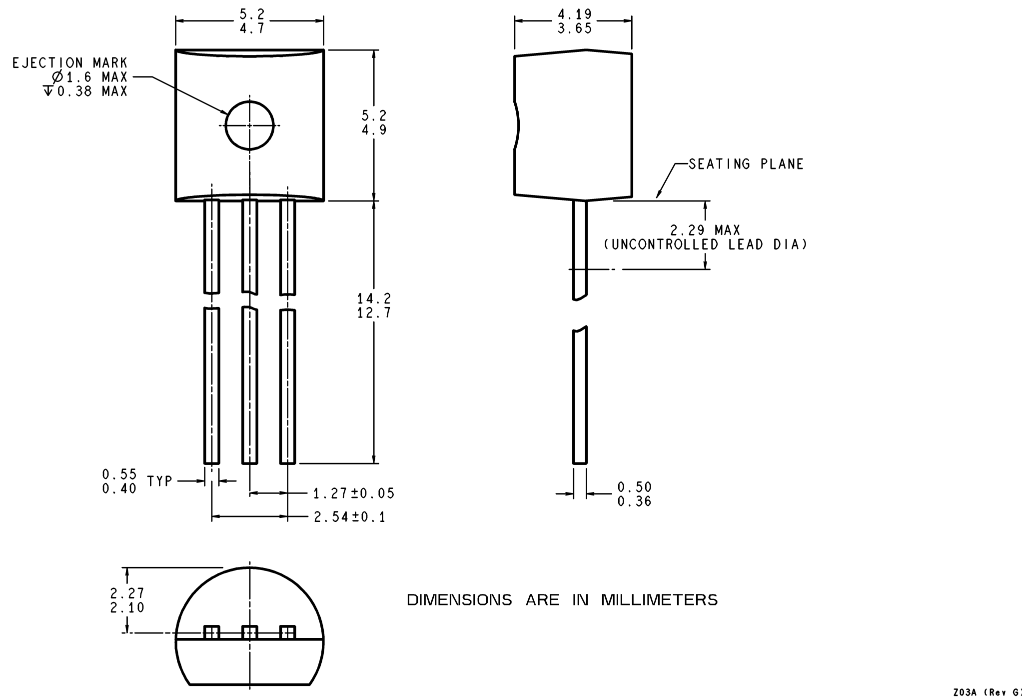


DIMENSIONS ARE IN MILLIMETERS

MAA05A (Rev C)

Molded Package (SC70)
NS Package Number MAA05A

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



Plastic Package (Z)
NS Package Number Z03A

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