

## Connection Diagrams



* No internal connection, but should be soldered to PC board for best heat transfer.

Top View
Order Number LM2574-3.3HVN, LM2574HVN-5.0, LM2574HVN-12, LM2574HVN-15, LM2574HVN-ADJ, LM2574N-3.3, LM2574N-5.0, LM2574N-12,

LM2574N-15 or LM2574N-ADJ
See NS Package Number N08A


Order Number LM2574HVM-3.3, LM2574HVM-5.0, LM2574HVM-12, LM2574HVM-15, LM2574HVM-ADJ,

LM2574M-3.3 LM2574M-5.0, LM2574M-12,
LM2574M-15 or LM2574M-ADJ
See NS Package Number M14B

| Absolute Maximum Ratings (Note 1) | Lead Temperature |  |
| :---: | :---: | :---: |
| If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications. | (Soldering, 10 seconds) | $260^{\circ} \mathrm{C}$ |
|  | Maximum Junction Temperature | $150^{\circ} \mathrm{C}$ |
|  | Power Dissipation | Internally Limited |
| Maximum Supply Voltage | Operating Ratings |  |
| LM2574 45V | Operating Ratings |  |
| LM2574HV 63V | Temperature Range |  |
| $\overline{\mathrm{ON}} /$ OFF Pin Input Voltage $\quad-0.3 \mathrm{~V} \leq \mathrm{V} \leq+\mathrm{V}_{\text {IN }}$ | LM2574/LM2574HV | $-40^{\circ} \mathrm{C} \leq \mathrm{T}_{J} \leq+125^{\circ} \mathrm{C}$ |
| Output Voltage to Ground | Supply Voltage |  |
| (Steady State) -1V | LM2574 | 40 V |
| Minimum ESD Rating | LM2574HV | 60 V |
| $(\mathrm{C}=100 \mathrm{pF}, \mathrm{R}=1.5 \mathrm{k} \Omega)$ |  |  |
| Storage Temperature Range $\quad-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |  |  |

## LM2574-3.3, LM2574HV-3.3

## Electrical Characteristics

Specifications with standard type face are for $\mathrm{T}_{J}=25^{\circ} \mathrm{C}$, and those with boldface type apply over full Operating Temperature Range.

| Symbol | Parameter | Conditions | LM2574-3.3 <br> LM2574HV-3.3 |  | Units <br> (Limits) |
| :--- | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Typ | Limit |  |

SYSTEM PARAMETERS (Note 3) Test Circuit Figure 2

| $\mathrm{V}_{\text {OUT }}$ | Output Voltage | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=100 \mathrm{~mA}$ | 3.3 |  | V |
| :--- | :--- | :--- | :---: | :---: | :---: |
|  |  |  |  | 3.234 |  <br> $\mathrm{V}(\mathrm{Min})$ <br> $\mathrm{V}(\mathrm{Max})$ |
| $\mathrm{V}_{\text {OUT }}$ | Output Voltage | $4.75 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq 40 \mathrm{~V}, 0.1 \mathrm{~A} \leq \mathrm{I}_{\text {LOAD }} \leq 0.5 \mathrm{~A}$ | 3.3 |  | V |
|  | LM2574 |  |  | $3.168 / 3.135$ | $\mathrm{~V}(\mathrm{Min})$ |
|  |  |  |  | $3.432 / 3.465$ | $\mathrm{~V}(\mathrm{Max})$ |
| $\mathrm{V}_{\text {OUT }}$ | Output Voltage | $4.75 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq 60 \mathrm{~V}, 0.1 \mathrm{~A} \leq \mathrm{I}_{\text {LOAD }} \leq 0.5 \mathrm{~A}$ | 3.3 |  |  |
|  | LM2574HV |  |  | $3.168 / 3.135$ | $\mathrm{~V}(\mathrm{Min})$ |
|  |  |  | 72 |  | $\mathrm{~V}(\mathrm{Max})$ |
| $\eta$ | Efficiency | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=0.5 \mathrm{~A}$ | $\%$ |  |  |

## LM2574-5.0, LM2574HV-5.0

## Electrical Characteristics

Specifications with standard type face are for $\mathrm{T}_{J}=25^{\circ} \mathrm{C}$, and those with boldface type apply over full Operating Temperature Range.

| Symbol | Parameter | Conditions | $\begin{gathered} \hline \text { LM2574-5.0 } \\ \text { LM2574HV-5.0 } \end{gathered}$ |  | Units (Limits) |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Typ | Limit (Note 2) |  |
| SYSTEM PARAMETERS (Note 3) Test Circuit Figure 2 |  |  |  |  |  |
| $\mathrm{V}_{\text {OUT }}$ | Output Voltage | $\mathrm{V}_{\mathrm{IN}}=12 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=100 \mathrm{~mA}$ | 5 | $\begin{aligned} & 4.900 \\ & 5.100 \end{aligned}$ | V <br> V(Min) <br> V(Max) |
| $\mathrm{V}_{\text {OUT }}$ | Output Voltage <br> LM2574 | $7 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq 40 \mathrm{~V}, 0.1 \mathrm{~A} \leq \mathrm{I}_{\text {LOAD }} \leq 0.5 \mathrm{~A}$ | 5 | $\begin{aligned} & 4.800 / 4.750 \\ & 5.200 / 5.250 \\ & \hline \end{aligned}$ | V <br> V(Min) <br> V(Max) |
| $\mathrm{V}_{\text {OUT }}$ | Output Voltage LM2574HV | $7 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq 60 \mathrm{~V}, 0.1 \mathrm{~A} \leq \mathrm{I}_{\text {LOAD }} \leq 0.5 \mathrm{~A}$ | 5 | $\begin{aligned} & 4.800 / 4.750 \\ & 5.225 / 5.275 \end{aligned}$ | V(Min) <br> V(Max) |
| $\eta$ | Efficiency | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=0.5 \mathrm{~A}$ | 77 |  | \% |


| LM2574-12, LM2574HV-12 Electrical Characteristics |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Symbol | Parameter | Conditions | $\begin{gathered} \text { LM2574-12 } \\ \text { LM2574HV-12 } \end{gathered}$ |  | Units (Limits) |
|  |  |  | Typ | Limit (Note 2) |  |
| SYSTEM PARAMETERS (Note 3) Test Circuit Figure 2 |  |  |  |  |  |
| $\mathrm{V}_{\text {OUt }}$ | Output Voltage | $\mathrm{V}_{\mathrm{IN}}=25 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=100 \mathrm{~mA}$ | 12 | $\begin{aligned} & 11.76 \\ & 12.24 \end{aligned}$ | V <br> V (Min) <br> V(Max) |
| $\mathrm{V}_{\text {OUT }}$ | Output Voltage <br> LM2574 | $15 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq 40 \mathrm{~V}, 0.1 \mathrm{~A} \leq \mathrm{I}_{\text {LOAD }} \leq 0.5 \mathrm{~A}$ | 12 | $\begin{aligned} & 11.52 / 11.40 \\ & 12.48 / 12.60 \\ & \hline \end{aligned}$ | V V(Min) V(Max) |
| $\mathrm{V}_{\text {OUT }}$ | Output Voltage <br> LM2574HV | $15 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq 60 \mathrm{~V}, 0.1 \mathrm{~A} \leq \mathrm{I}_{\text {LOAD }} \leq 0.5 \mathrm{~A}$ | 12 | $\begin{aligned} & 11.52 / 11.40 \\ & 12.54 / 12.66 \end{aligned}$ | V(Min) <br> V(Max) |
| $\eta$ | Efficiency | $\mathrm{V}_{\text {IN }}=15 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=0.5 \mathrm{~A}$ | 88 |  | \% |
| LM2574-15, LM2574HV-15 Electrical Characteristics <br> Specifications with standard type face are for $\mathrm{T}_{J}=25^{\circ} \mathrm{C}$, and those with boldface type apply over full Operating Temperature Range. |  |  |  |  |  |
| Symbol | Parameter | Conditions | $\begin{gathered} \text { LM2574-15 } \\ \text { LM2574HV-15 } \end{gathered}$ |  | Units (Limits) |
|  |  |  | Typ | $\begin{aligned} & \hline \text { Limit } \\ & (\text { Note 2) } \\ & \hline \end{aligned}$ |  |
| SYSTEM PARAMETERS (Note 3) Test Circuit Figure 2 |  |  |  |  |  |
| $\mathrm{V}_{\text {OUT }}$ | Output Voltage | $\mathrm{V}_{\mathrm{IN}}=30 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=100 \mathrm{~mA}$ | 15 | $\begin{aligned} & 14.70 \\ & 15.30 \end{aligned}$ | $\begin{gathered} \hline \text { V } \\ \text { V(Min) } \\ \text { V(Max) } \\ \hline \end{gathered}$ |
| $\mathrm{V}_{\text {OUT }}$ | Output Voltage <br> LM2574 | $18 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq 40 \mathrm{~V}, 0.1 \mathrm{~A} \leq \mathrm{I}_{\text {LOAD }} \leq 0.5 \mathrm{~A}$ | 15 | $\begin{aligned} & 14.40 / 14.25 \\ & 15.60 / 15.75 \\ & \hline \end{aligned}$ | V <br> V(Min) <br> V(Max) |
| $\mathrm{V}_{\text {OUT }}$ | Output Voltage LM2574HV | $18 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq 60 \mathrm{~V}, 0.1 \mathrm{~A} \leq \mathrm{I}_{\text {LOAD }} \leq 0.5 \mathrm{~A}$ | 15 | $\begin{aligned} & 14.40 / 14.25 \\ & 15.68 / 15.83 \\ & \hline \end{aligned}$ | V(Min) <br> V(Max) |
| $\eta$ | Efficiency | $\mathrm{V}_{\mathrm{IN}}=18 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=0.5 \mathrm{~A}$ | 88 |  | \% |
| LM2574-ADJ, LM2574HV-ADJElectrical Characteristics |  |  |  |  |  |
| Symbol | Parameter | Conditions | $\begin{aligned} & \text { LM2574-ADJ } \\ & \text { LM2574HV-ADJ } \end{aligned}$ |  | Units (Limits) |
|  |  |  | Typ | Limit (Note 2) |  |
| SYSTEM PARAMETERS (Note 3) Test Circuit Figure 2 |  |  |  |  |  |
| $\mathrm{V}_{\text {FB }}$ | Feedback Voltage | $\mathrm{V}_{\mathrm{IN}}=12 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=100 \mathrm{~mA}$ | 1.230 | $\begin{aligned} & 1.217 \\ & 1.243 \end{aligned}$ | V V(Min) V(Max) |

## LM2574-ADJ, LM2574HV-ADJ <br> Electrical Characteristics (Continued)

| Symbol | Parameter | Conditions | $\begin{gathered} \text { LM2574-ADJ } \\ \text { LM2574HV-ADJ } \end{gathered}$ |  | Units(Limits) |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Typ | Limit (Note 2) |  |
| SYSTEM PARAMETERS (Note 3) Test Circuit Figure 2 |  |  |  |  |  |
| $\mathrm{V}_{\mathrm{FB}}$ | Feedback Voltage LM2574 | $7 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq 40 \mathrm{~V}, 0.1 \mathrm{~A} \leq \mathrm{I}_{\text {LOAD }} \leq 0.5 \mathrm{~A}$ <br> $\mathrm{V}_{\text {Out }}$ Programmed for 5V. Circuit of Figure 2 | 1.230 | $\begin{aligned} & 1.193 / \mathbf{1 . 1 8 0} \\ & 1.267 / 1.280 \\ & \hline \end{aligned}$ | $\begin{gathered} \text { V } \\ \text { V(Min) } \\ \text { V(Max) } \\ \hline \end{gathered}$ |
| $\mathrm{V}_{\mathrm{FB}}$ | Feedback Voltage LM2574HV | $7 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq 60 \mathrm{~V}, 0.1 \mathrm{~A} \leq \mathrm{I}_{\text {LOAD }} \leq 0.5 \mathrm{~A}$ <br> $\mathrm{V}_{\text {Out }}$ Programmed for 5 V . Circuit of Figure 2 | 1.230 | $\begin{aligned} & 1.193 / 1.180 \\ & 1.273 / 1.286 \end{aligned}$ | V(Min) <br> V(Max) |
| $\eta$ | Efficiency | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=5 \mathrm{~V}, \mathrm{I}_{\text {LOAD }}=0.5 \mathrm{~A}$ | 77 |  | \% |

## All Output Voltage Versions <br> \section*{Electrical Characteristics}

| Symbol | Parameter | Conditions | $\begin{gathered} \text { LM2574-XX } \\ \text { LM2574HV-XX } \end{gathered}$ |  | Units (Limits) |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Typ | Limit <br> (Note 2) |  |
| DEVICE PARAMETERS |  |  |  |  |  |
| $\mathrm{I}_{\mathrm{b}}$ | Feedback Bias Current | Adjustable Version Only, $\mathrm{V}_{\text {Out }}=5 \mathrm{~V}$ | 50 | 100/500 | nA |
| $\mathrm{f}_{0}$ | Oscillator Frequency | (see Note 10) | 52 | $\begin{aligned} & 47 / 42 \\ & 58 / 63 \end{aligned}$ | $\begin{gathered} \hline \mathrm{kHz} \\ \mathrm{kHz}(\mathrm{Min}) \\ \mathrm{kHz}(\operatorname{Max}) \\ \hline \end{gathered}$ |
| $\mathrm{V}_{\text {SAT }}$ | Saturation Voltage | $\mathrm{I}_{\text {Out }}=0.5 \mathrm{~A}($ Note 4) | 0.9 | 1.2/1.4 | $\begin{gathered} \mathrm{V} \\ \mathrm{~V}(\max ) \end{gathered}$ |
| DC | Max Duty Cycle (ON) | (Note 5) | 98 | 93 | $\begin{gathered} \% \\ \%(\mathrm{Min}) \end{gathered}$ |
| $\mathrm{I}_{\mathrm{CL}}$ | Current Limit | Peak Current, (Notes 4, 10) | 1.0 | $\begin{gathered} 0.7 / 0.65 \\ 1.6 / 1.8 \end{gathered}$ | A $A(\operatorname{Min})$ $A(\operatorname{Max})$ |
| $\mathrm{I}_{\mathrm{L}}$ | Output Leakage <br> Current | (Notes 6, 7) Output $=0 \mathrm{~V}$ <br>  Output $=-1 \mathrm{~V}$ <br>  Output $=-1 \mathrm{~V}$ | 7.5 | $\begin{gathered} 2 \\ 30 \end{gathered}$ | $\begin{gathered} \mathrm{mA}(\mathrm{Max}) \\ \mathrm{mA} \\ \mathrm{~mA}(\mathrm{Max}) \\ \hline \end{gathered}$ |
| $\mathrm{I}_{\mathrm{Q}}$ | Quiescent Current | (Note 6) | 5 | 10 | $\begin{gathered} \mathrm{mA} \\ \mathrm{~mA}(\mathrm{Max}) \end{gathered}$ |
| $\mathrm{I}_{\text {StBY }}$ | Standby Quiescent Current | $\overline{\text { ON }} /$ OFF Pin $=5 \mathrm{~V}$ (OFF) | 50 | 200 | $\mu \mathrm{A}$ $\mu \mathrm{A}(\mathrm{Max})$ |
| $\begin{aligned} & \theta_{\mathrm{JA}} \\ & \theta_{\mathrm{JA}} \\ & \theta_{\mathrm{JA}} \\ & \theta_{\mathrm{JA}} \end{aligned}$ | Thermal Resistance | N Package, Junction to Ambient (Note 8) <br> N Package, Junction to Ambient (Note 9) <br> M Package, Junction to Ambient (Note 8) <br> M Package, Junction to Ambient (Note 9) | $\begin{gathered} 92 \\ 72 \\ 102 \\ 78 \end{gathered}$ |  | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |

## All Output Voltage Versions Electrical Characteristics (Continued)

Specifications with standard type face are for $T_{J}=25^{\circ} \mathrm{C}$, and those with boldface type apply over full Operating Temperature Range. Unless otherwise specified, $\mathrm{V}_{I N}=12 \mathrm{~V}$ for the $3.3 \mathrm{~V}, 5 \mathrm{~V}$, and Adjustable version, $\mathrm{V}_{\mathrm{IN}}=25 \mathrm{~V}$ for the 12 V version, and $\mathrm{V}_{\text {IN }}=30 \mathrm{~V}$ for the 15 V version. $I_{\text {LOAD }}=100 \mathrm{~mA}$.

| Symbol | Parameter | Conditions | $\begin{gathered} \text { LM2574-XX } \\ \text { LM2574HV-XX } \end{gathered}$ |  | Units(Limits) |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Typ | Limit (Note 2) |  |
| $\overline{\text { ON }}$ /OFF CONTROL Test Circuit Figure 2 |  |  |  |  |  |
| $\mathrm{V}_{\mathrm{IH}}$ | $\overline{\mathrm{ON}} / \mathrm{OFF}$ Pin Logic Input Level | $\mathrm{V}_{\text {OUT }}=0 \mathrm{~V}$ | 1.4 | 2.2/2.4 | V(Min) |
| $\mathrm{V}_{\text {IL }}$ |  | $\mathrm{V}_{\text {Out }}=$ Nominal Output Voltage | 1.2 | 1.0/0.8 | V(Max) |
| $\mathrm{I}_{\mathrm{H}}$ | $\overline{\mathrm{ON}} /$ OFF Pin Input Current | $\overline{\mathrm{ON}} / \mathrm{OFF}$ Pin $=5 \mathrm{~V}$ (OFF) | 12 | 30 | $\mu \mathrm{A}$ $\mu \mathrm{A}(\mathrm{Max})$ |
| IIL |  | $\overline{\mathrm{ON}} / \mathrm{OFF}$ Pin $=0 \mathrm{~V}(\mathrm{ON})$ | 0 | 10 |  |

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics
Note 2: All limits guaranteed at room temperature (Standard type face) and at temperature extremes (bold type face). All room temperature limits are $100 \%$ production tested. All limits at temperature extremes are guaranteed via correlation using standard Statistical Quality Control (SQC) methods. All limits are used to calculate Average Outgoing Quality Level

Note 3: External components such as the catch diode, inductor, input and output capacitors can affect switching regulator system performance. When the LM2574 is used as shown in the Figure 2 test circuit, system performance will be as shown in system parameters section of Electrical Characteristics
Note 4: Output pin sourcing current. No diode, inductor or capacitor connected to output pin
Note 5: Feedback pin removed from output and connected to 0 V .
Note 6: Feedback pin removed from output and connected to +12 V for the Adjustable, 3.3 V , and 5 V versions, and +25 V for the 12 V and 15 V versions, to force the output transistor OFF.

Note 7: $\mathrm{V}_{\mathrm{IN}}=40 \mathrm{~V}$ ( 60 V for high voltage version)
Note 8: Junction to ambient thermal resistance with approximately 1 square inch of printed circuit board copper surrounding the leads. Additional copper area will lower thermal resistance further. See application hints in this data sheet and the thermal model in Switchers Made Simple software
Note 9: Junction to ambient thermal resistance with approximately 4 square inches of 1 oz . ( 0.0014 in . thick) printed circuit board copper surrounding the leads. Additional copper area will lower thermal resistance further. (See Note 8 .)
Note 10: The oscillator frequency reduces to approximately 18 kHz in the event of an output short or an overload which causes the regulated output voltage to drop approximately $40 \%$ from the nominal output voltage. This self protection feature lowers the average power dissipation of the IC by lowering the minimum duty cycle from $5 \%$ down to approximately $2 \%$.

## Typical Performance Characteristics (Circuit of Figure 2)

Normalized Output Voltage


Line Regulation


Dropout Voltage


## Typical Performance Characteristics (Circuit of Figure 2) (Continued)



## Oscillator Frequency



## Supply Current



## Switch Saturation

Voltage



Standby
Quiescent Current


Efficiency


Feedback Voltage vs Duty Cycle


Typical Performance Characteristics (Circuit of Figure 2) (Continued)

Feedback
Pin Current


Continuous Mode Switching Waveforms
$\mathrm{V}_{\text {out }}=5 \mathrm{~V}, 500 \mathrm{~mA}$ Load Current, $\mathrm{L}=330 \mu \mathrm{H}$


Notes:
A: Output Pin Voltage, 10V/div
B: Inductor Current, 0.2 A/div
C: Output Ripple Voltage, $20 \mathrm{mV} /$ div,
AC-Coupled
Horizontal Time Base: $5 \mu \mathrm{~s} / \mathrm{div}$

500 mA Load Transient Response for Continuous Mode Operation. L $=330 \mu \mathrm{H}, \mathrm{C}_{\text {OUt }}=300 \mu \mathrm{~F}$


Notes:
A: Output Voltage, $50 \mathrm{mV} /$ div
AC Coupled
B: 100 mA to 500 mA Load Pulse
Horizontal Time Base: $200 \mu \mathrm{~s} / \mathrm{div}$

Junction to Ambien
Thermal Resistance


Discontinuous Mode Switching Waveforms $\mathrm{V}_{\text {out }}=5 \mathrm{~V}, 100 \mathrm{~mA}$ Load Current, $\mathrm{L}=100 \mu \mathrm{H}$


Notes
A: Output Pin Voltage, 10V/div
B: Inductor Current, 0.2 A/div
C: Output Ripple Voltage, $20 \mathrm{mV} / \mathrm{div}$,
AC-Coupled
Horizontal Time Base: $5 \mu \mathrm{~s} / \mathrm{div}$

250 mA Load Transient Response for Discontinuous Mode Operation. $\mathrm{L}=68 \mu \mathrm{H}, \mathrm{C}_{\text {Out }}=470 \mu \mathrm{~F}$


Notes:
A: Output Voltage, $50 \mathrm{mV} / \mathrm{div}$.
AC Coupled
B: 50 mA to 250 mA Load Pulse
Horizontal Time Base: $200 \mu \mathrm{~s} / \mathrm{div}$

$R 1=1 k$
$3.3 \mathrm{~V}, \mathrm{R} 2=1.7 \mathrm{k}$
$5 \mathrm{~V}, \mathrm{R} 2=3.1 \mathrm{k}$
$12 \mathrm{~V}, \mathrm{R} 2=8.84 \mathrm{k}$
$15 \mathrm{~V}, \mathrm{R} 2=11.3 \mathrm{k}$
For Adj. Version
R1 $=$ Open, R2 $=0 \Omega$
Note: Pin numbers are for the 8 -pin DIP package.
FIGURE 1.

## Test Circuit and Layout Guidelines


$\mathrm{C}_{\mathrm{IN}}-22 \mu \mathrm{~F}, 75 \mathrm{~V}$
Aluminum Electrolytic
Cout - $220 \mu \mathrm{~F}, 25 \mathrm{~V}$
Aluminum Electrolytic
D1 - Schottky, 11DQ06
L1 - $330 \mu \mathrm{H}, 52627$
(for 5 V in, 3.3 V out, use
$100 \mu \mathrm{H}, \mathrm{RL}$-1284-100)
R1 - 2k, 0.1\%
R2 - 6.12k, 0.1\%

Adjustable Output Voltage Version

$V_{\text {OUT }}=V_{\text {REF }}\left(1+\frac{R_{2}}{R_{1}}\right)$
$R_{2}=R_{1}\left(\frac{V_{\text {OUT }}}{V_{\text {REF }}}-1\right)$
where $\mathrm{V}_{\text {REF }}=1.23 \mathrm{~V}$,
R1 between $1 \mathrm{k} \& 5 \mathrm{k}$.

## FIGURE 2.

As in any switching regulator, layout is very important. Rapidly switching currents associated with wiring inductance generate voltage transients which can cause problems. For minimal inductance and ground loops, the length of the leads indicated by heavy lines should be kept as short as possible. Single-point grounding (as indicated) or ground plane construction should be used for best results. When using the Adjustable version, physically locate the programming resistors near the regulator, to keep the sensitive feedback wiring short.

## Test Circuit and Layout Guidelines

(Continued)

| Inductor <br> Value | Pulse Eng. <br> (Note 1) | Renco <br> (Note 2) | NPI <br> (Note 3) |
| :---: | :---: | :---: | :---: |
| $68 \mu \mathrm{H}$ | $*$ | RL-1284-68-43 | NP5915 |
| $100 \mu \mathrm{H}$ | $*$ | RL-1284-100-43 | NP5916 |
| $150 \mu \mathrm{H}$ | 52625 | RL-1284-150-43 | NP5917 |
| $220 \mu \mathrm{H}$ | 52626 | RL-1284-220-43 | NP5918/5919 |
| $330 \mu \mathrm{H}$ | 52627 | RL-1284-330-43 | NP5920/5921 |
| $470 \mu \mathrm{H}$ | 52628 | RL-1284-470-43 | NP5922 |
| $680 \mu \mathrm{H}$ | 52629 | RL-1283-680-43 | NP5923 |
| $1000 \mu \mathrm{H}$ | 52631 | RL-1283-1000-43 | $*$ |
| $1500 \mu \mathrm{H}$ | $*$ | RL-1283-1500-43 | $*$ |
| $2200 \mu \mathrm{H}$ | $*$ | RL-1283-2200-43 | $*$ |

FIGURE 3. Inductor Selection by Manufacturer's Part Number

## U.S. Source

Note 1: Pulse Engineering, (619) 674-8100
P.O. Box 12236, San Diego, CA 92112

Note 2: Renco Electronics Inc., (516) 586-5566
60 Jeffryn Blvd. East, Deer Park, NY 11729
*Contact Manufacturer
European Source
Note 3: NPI/APC +44 (0) 634290588
47 Riverside, Medway City Estate
Strood, Rochester, Kent ME2 4DP. UK
*Contact Manufacturer

## LM2574 Series Buck Regulator Design Procedure



Given:
$\mathrm{V}_{\text {OUT }}=5 \mathrm{~V}$
$\mathrm{V}_{\text {IN }}(\operatorname{Max})=15 \mathrm{~V}$
$\mathrm{I}_{\text {LOAD }}(\mathrm{Max})=0.4 \mathrm{~A}$

1. Inductor Selection (L1)
A. Use the selection guide shown in Figure 5.
B. From the selection guide, the inductance area intersected by the 15 V line and 0.4 A line is 330 .
C. Inductor value required is $330 \mu \mathrm{H}$. From the table in Figure 3, choose Pulse Engineering PE-52627, Renco RL-1284-330, or NPI NP5920/5921.
2. Output Capacitor Selection ( $\mathrm{C}_{\text {OUT }}$ )
A. $\mathrm{C}_{\text {Out }}=100 \mu \mathrm{~F}$ to $470 \mu \mathrm{~F}$ standard aluminum electrolytic.
B. Capacitor voltage rating $=20 \mathrm{~V}$.
3. Catch Diode Selection (D1)
A. For this example, a 1A current rating is adequate.
B. Use a 20V 1 N5817 or SR102 Schottky diode, or any of the suggested fast-recovery diodes shown in Figure 9.

## 4. Input Capacitor ( $\mathrm{C}_{\mathrm{IN}}$ )

A $22 \mu \mathrm{~F}$ aluminum electrolytic capacitor located near the input and ground pins provides sufficient bypassing.

## LM2574 Series Buck Regulator Design Procedure (Continued)

INDUCTOR VALUE SELECTION GUIDES (For Continuous Mode Operation)


FIGURE 4. LM2574HV-3.3 Inductor Selection Guide


FIGURE 5. LM2574HV-5.0 Inductor Selection Guide

maximum load current (a) DS011394-14
FIGURE 6. LM2574HV-12 Inductor Selection Guide


FIGURE 7. LM2574HV-15 Inductor Selection Guide

## LM2574 Series Buck Regulator Design Procedure

(Continued)


FIGURE 8. LM2574HV-ADJ Inductor Selection Guide

| PROCEDURE (Adjustable Output Voltage Versions) |
| :--- |
| Given: |
| $\mathrm{V}_{\text {OUT }}=$ Regulated Output Voltage |
| $\mathrm{V}_{\text {IN }}(\mathrm{Max})=$ Maximum Input Voltage |
| $\mathrm{I}_{\text {LOAD }}(\mathrm{Max})=$ Maximum Load Current |
| $\mathrm{F}=$ Switching Frequency (Fixed at 52 kHz ) |
| 1. Programming Output Voltage (Selecting R1 and R2, |
| shown in Figure 2) |
| Use the following formula to select the appropriate resist |
| values. |
| $\quad \mathrm{V}_{\text {OUT }}=\mathrm{V}_{\text {REF }}\left(1+\frac{\mathrm{R}_{2}}{\mathrm{R}_{1}}\right) \quad$ where $\mathrm{V}_{\text {REF }}=1.23 \mathrm{~V}$ |
| $\mathrm{R}_{1}$ can be between 1 k and 5 k . (For best temperature coeffiter | cient and stability with time, use $1 \%$ metal film resistors)

$$
R_{2}=R_{1}\left(\frac{V_{\mathrm{OUT}}}{V_{\mathrm{REF}}}-1\right)
$$

2. Inductor Selection (L1)
A. Calculate the inductor Volt - microsecond constant,
$\mathrm{E} \cdot \mathrm{T}(\mathrm{V} \cdot \mu \mathrm{s})$, from the following formula:

$$
\mathrm{E} \bullet \mathrm{~T}=\left(\mathrm{V}_{\mathrm{IN}}-\mathrm{V}_{\mathrm{OUT}}\right) \frac{\mathrm{V}_{\mathrm{OUT}}}{\mathrm{~V}_{\mathrm{IN}}} \cdot \frac{1000}{\mathrm{~F}(\text { in } k H z)}(\mathrm{V} \bullet \mu \mathrm{~s})
$$

B. Use the $\mathrm{E} \cdot \mathrm{T}$ value from the previous formula and match it with the $\mathrm{E} \cdot \mathrm{T}$ number on the vertical axis of the Inductor Value Selection Guide shown in Figure 8.
C. On the horizontal axis, select the maximum load current.
D. Identify the inductance region intersected by the E • T value and the maximum load current value, and note the inductor value for that region.
E. Select an appropriate inductor from the table shown in Figure 3. Part numbers are listed for three inductor manufacturers. The inductor chosen must be rated for operation at the LM2574 switching frequency ( 52 kHz ) and for a current rating of $1.5 \times \mathrm{l}_{\text {LOAD }}$. For additional inductor information, see the inductor section in the application hints section of this data sheet.

## LM2574 Series Buck Regulator Design Procedure (Continued)

| PROCEDURE (Adjustable Output Voltage V |
| :--- |
| 3. Output Capacitor Selection $\left(\mathrm{C}_{\text {out }}\right)$ |
| A. The value of the output capacitor together with |
| defines the dominate pole-pair of the switching reg |
| For stable operation, the capacitor must satisfy the |
| requirement: |
| $\qquad \mathrm{C}_{\text {OUT }} \geq 13,300 \frac{\mathrm{~V}_{\text {IN }}(\mathrm{Max})}{\mathrm{V}_{\text {OUT }} \bullet \mathrm{L}(\mu \mathrm{H})}$ ( $\mu \mathrm{F}$ ) |

The above formula yields capacitor values between $5 \mu \mathrm{~F}$ and $1000 \mu \mathrm{~F}$ that will satisfy the loop requirements for stable operation. But to achieve an acceptable output ripple voltage, (approximately $1 \%$ of the output voltage) and transient response, the output capacitor may need to be several times larger than the above formula yields.
B. The capacitor's voltage rating should be at last 1.5 times greater than the output voltage. For a 24 V regulator, a rating of at least 35 V is recommended.
Higher voltage electrolytic capacitors generally have lower ESR numbers, and for this reasion it may be necessary to select a capacitor rate for a higher voltage than would normally be needed.

## 4. Catch Diode Selection (D1)

A. The catch-diode current rating must be at least 1.5 times greater than the maximum load current. Also, if the power supply design must withstand a continuous output short, the diode should have a current rating equal to the maximum current limit of the LM2574. The most stressful condition for this diode is an overload or shorted output condition. Suitable diodes are shown in the selection guide of Figure 9.
B. The reverse voltage rating of the diode should be at least 1.25 times the maximum input voltage.

## 5. Input Capacitor ( $\mathrm{C}_{\mathrm{IN}}$ )

An aluminum or tantalum electrolytic bypass capacitor located close to the regulator is needed for stable operation.

EXAMPLE (Adjustable Output Voltage Versions)
3. Output Capacitor Selection (Cout)
A. $\mathrm{C}_{\text {OUT }}>13,300 \frac{40}{24 \cdot 1000}=22.2 \mu \mathrm{~F}$

However, for acceptable output ripple voltage select
$\mathrm{C}_{\text {OUT }} \geq 100 \mu \mathrm{~F}$
$\mathrm{C}_{\text {OUt }}=100 \mu \mathrm{~F}$ electrolytic capacitor

## 4. Catch Diode Selection (D1)

A. For this example, a 1A current rating is adequate.
B. Use a 50V MBR150 or 11DQ05 Schottky diode, or any of the suggested fast-recovery diodes in Figure 9.

## 5. Input Capacitor ( $\mathrm{C}_{\mathrm{IN}}$ )

A $22 \mu \mathrm{~F}$ aluminum electrolytic capacitor located near the input and ground pins provides sufficient bypassing. See (Figure 9). To further simplify the buck regulator design procedure, National Semiconductor is making available computer design software to be used with the Simple Switcher line of switching regulators. Switchers Made Simple (version 3.3) is available on a ( $3^{1} / 2^{\prime \prime}$ ) diskette for IBM compatible computers from a National Semiconductor sales office in your area.

## LM2574 Series Buck Regulator Design Procedure (Continued)

| $\mathrm{V}_{\mathrm{R}}$ | 1 Amp Diodes |  |
| :---: | :---: | :---: |
|  | Schottky | Fast Recovery |
| 20 V | 1N5817 <br> SR102 <br> MBR120P |  |
| 30 V | 1N5818 SR103 11DQ03 MBR130P 10JQ030 | The following diodes |
| 40 V | 1N5819 <br> SR104 <br> 11DQ04 <br> 11JQ04 <br> MBR140P | are all rated to 100V |
| 50 V | $\begin{gathered} \hline \text { MBR150 } \\ \text { SR105 } \\ \text { 11DQ05 } \\ \text { 11JQ05 } \end{gathered}$ | $\begin{aligned} & \text { 11DF1 } \\ & \text { 10JF1 } \end{aligned}$ <br> MUR110 <br> HER102 |
| 60 V | $\begin{gathered} \hline \text { MBR160 } \\ \text { SR106 } \\ \text { 11DQ06 } \\ \text { 11JQ06 } \end{gathered}$ |  |
| 90 V | 11DQ09 |  |

## Application Hints

## INPUT CAPACITOR ( $\mathrm{C}_{\mathrm{IN}}$ )

To maintain stability, the regulator input pin must be bypassed with at least a $22 \mu \mathrm{~F}$ electrolytic capacitor. The capacitor's leads must be kept short, and located near the regulator.
If the operating temperature range includes temperatures below $-25^{\circ} \mathrm{C}$, the input capacitor value may need to be larger. With most electrolytic capacitors, the capacitance value decreases and the ESR increases with lower temperatures and age. Paralleling a ceramic or solid tantalum capacitor will increase the regulator stability at cold temperatures. For maximum capacitor operating lifetime, the capacitor's RMS ripple current rating should be greater than

$$
\begin{aligned}
& 1.2 \times\left(\frac{t_{O N}}{T}\right) \times I_{\text {LOAD }} \\
& \text { where } \frac{t_{O N}}{T}=\frac{V_{O U T}}{V_{I N}} \text { for a buck regulator } \\
& \text { and } \frac{t_{O N}}{T}=\frac{\left|V_{O U T}\right|}{\left|V_{O U T}\right|+V_{I N}} \text { for a buck-boost regulator. }
\end{aligned}
$$

## INDUCTOR SELECTION

All switching regulators have two basic modes of operation: continuous and discontinuous. The difference between the two types relates to the inductor current, whether it is flowing continuously, or if it drops to zero for a period of time in the normal switching cycle. Each mode has distinctively different operating characteristics, which can affect the regulator performance and requirements.
The LM2574 (or any of the Simple Switcher family) can be used for both continuous and discontinuous modes of operation.

In many cases the preferred mode of operation is in the continuous mode. It offers better load regulation, lower peak switch, inductor and diode currents, and can have lower output ripple voltage. But it does require relatively large inductor values to keep the inductor current flowing continuously, especially at low output load currents.
To simplify the inductor selection process, an inductor selection guide (nomograph) was designed (see Figure 4 through Figure 8). This guide assumes continuous mode operation, and selects an inductor that will allow a peak-to-peak inductor ripple current ( $\Delta \mathrm{I}_{\text {IND }}$ ) to be a certain percentage of the maximum design load current. In the LM2574 SIMPLE SWITCHER, the peak-to-peak inductor ripple current percentage (of load current) is allowed to change as different design load currents are selected. By allowing the percentage of inductor ripple current to increase for lower current applications, the inductor size and value can be kept relatively low.

## Application Hints (Continued)

## INDUCTOR RIPPLE CURRENT

When the switcher is operating in the continuous mode, the inductor current waveform ranges from a triangular to a sawtooth type of waveform (depending on the input voltage). For a given input voltage and output voltage, the peak-to-peak amplitude of this inductor current waveform remains constant. As the load current rises or falls, the entire sawtooth current waveform also rises or falls. The average DC value of this waveform is equal to the DC load current (in the buck regulator configuration).
If the load current drops to a low enough level, the bottom of the sawtooth current waveform will reach zero, and the switcher will change to a discontinuous mode of operation. This is a perfectly acceptable mode of operation. Any buck switching regulator (no matter how large the inductor value is) will be forced to run discontinuous if the load current is light enough.
The curve shown in Figure 10 illustrates how the peak-topeak inductor ripple current ( $\Delta \mathrm{I}_{\mathrm{IND}}$ ) is allowed to change as different maximum load currents are selected, and also how it changes as the operating point varies from the upper border to the lower border within an inductance region (see Inductor Selection guides).


FIGURE 10. Inductor Ripple Current ( $\Delta \mathrm{I}_{\mathrm{IND}}$ ) Range Based on Selection Guides from Figure 4 through Figure 8.

Consider the following example:

$$
\mathrm{V}_{\text {OUT }}=5 \mathrm{~V} @ 0.4 \mathrm{~A}
$$

$\mathrm{V}_{\text {IN }}=10 \mathrm{~V}$ minimum up to 20 V maximum
The selection guide in Figure 5 shows that for a 0.4 A load current, and an input voltage range between 10 V and 20 V , the inductance region selected by the guide is $330 \mu \mathrm{H}$. This value of inductance will allow a peak-to-peak inductor ripple current ( $\Delta \mathrm{I}_{\text {IND }}$ ) to flow that will be a percentage of the maximum load current. For this inductor value, the $\Delta \mathrm{I}_{\text {IND }}$ will also vary depending on the input voltage. As the input voltage increases to 20 V , it approaches the upper border of the inductance region, and the inductor ripple current increases. Referring to the curve in Figure 10, it can be seen that at the 0.4A load current level, and operating near the upper border of the $330 \mu \mathrm{H}$ inductance region, the $\Delta \mathrm{I}_{\text {IND }}$ will be $53 \%$ of 0.4 A , or $212 \mathrm{~mA} \mathrm{p}-\mathrm{p}$.

This $\Delta l_{\text {IND }}$ is important because from this number the peak inductor current rating can be determined, the minimum load current required before the circuit goes to discontinuous operation, and also, knowing the ESR of the output capacitor,
the output ripple voltage can be calculated, or conversely, measuring the output ripple voltage and knowing the $\Delta l_{\text {IND }}$, the ESR can be calculated.
From the previous example, the Peak-to-peak Inductor Ripple Current $\left(\Delta I_{\text {IND }}\right)=212 \mathrm{~mA} \mathrm{p}-\mathrm{p}$. Once the $\Delta_{\text {IND }}$ value is known, the following three formulas can be used to calculate additional information about the switching regulator circuit:

1. Peak Inductor or peak switch current

$$
=\left(I_{\mathrm{LOAD}}+\frac{\Delta \mathrm{I}_{\mathrm{IND}}}{2}\right)=\left(0.4 \mathrm{~A}+\frac{212}{2}\right)=506 \mathrm{~mA}
$$

2. Minimum load current before the circuit becomes discontinuous

$$
=\frac{\Delta l_{\mathrm{IND}}}{2}=\frac{212}{2}=106 \mathrm{~mA}
$$

3. Output Ripple Voltage $=\left(\left.\Delta\right|_{\text {IND }}\right) \times\left(\right.$ ESR of $\left.C_{\text {OUT }}\right)$

The selection guide chooses inductor values suitable for continuous mode operation, but if the inductor value chosen is prohibitively high, the designer should investigate the possibility of discontinuous operation. The computer design software Switchers Made Simple will provide all component values for discontinuous (as well as continuous) mode of operation.
Inductors are available in different styles such as pot core, toroid, E-frame, bobbin core, etc., as well as different core materials, such as ferrites and powdered iron. The least expensive, the bobbin core type, consists of wire wrapped on a ferrite rod core. This type of construction makes for an inexpensive inductor, but since the magnetic flux is not completely contained within the core, it generates more electromagnetic interference (EMI). This EMI can cause problems in sensitive circuits, or can give incorrect scope readings because of induced voltages in the scope probe.
The inductors listed in the selection chart include powdered iron toroid for Pulse Engineering, and ferrite bobbin core for Renco.
An inductor should not be operated beyond its maximum rated current because it may saturate. When an inductor begins to saturate, the inductance decreases rapidly and the inductor begins to look mainly resistive (the DC resistance of the winding). This can cause the inductor current to rise very rapidly and will affect the energy storage capabilities of the inductor and could cause inductor overheating. Different inductor types have different saturation characteristics, and this should be kept in mind when selecting an inductor. The inductor manufacturers' data sheets include current and energy limits to avoid inductor saturation.

## OUTPUT CAPACITOR

An output capacitor is required to filter the output voltage and is needed for loop stability. The capacitor should be located near the LM2574 using short pc board traces. Standard aluminum electrolytics are usually adequate, but low ESR types are recommended for low output ripple voltage and good stability. The ESR of a capacitor depends on many factors, some which are: the value, the voltage rating, physical size and the type of construction. In general, low value or low voltage (less than 12V) electrolytic capacitors usually have higher ESR numbers.
The amount of output ripple voltage is primarily a function of the ESR (Equivalent Series Resistance) of the output ca-

## Application Hints (Continued)

pacitor and the amplitude of the inductor ripple current $\left(\Delta l_{\text {IND }}\right)$. See the section on inductor ripple current in Application Hints.
The lower capacitor values ( $100 \mu \mathrm{~F}-330 \mu \mathrm{~F}$ ) will allow typically 50 mV to 150 mV of output ripple voltage, while largervalue capacitors will reduce the ripple to approximately 20 mV to 50 mV .
Output Ripple Voltage $=\left(\Delta I_{\text {IND }}\right)\left(E S R\right.$ of $\left.C_{\text {OUT }}\right)$
To further reduce the output ripple voltage, several standard electrolytic capacitors may be paralleled, or a higher-grade capacitor may be used. Such capacitors are often called "high-frequency," "low-inductance," or "low-ESR." These will reduce the output ripple to 10 mV or 20 mV . However, when operating in the continuous mode, reducing the ESR below $0.03 \Omega$ can cause instability in the regulator.
Tantalum capacitors can have a very low ESR, and should be carefully evaluated if it is the only output capacitor. Because of their good low temperature characteristics, a tantalum can be used in parallel with aluminum electrolytics, with the tantalum making up $10 \%$ or $20 \%$ of the total capacitance.
The capacitor's ripple current rating at 52 kHz should be at least $50 \%$ higher than the peak-to-peak inductor ripple current.

## CATCH DIODE

Buck regulators require a diode to provide a return path for the inductor current when the switch is off. This diode should be located close to the LM2574 using short leads and short printed circuit traces.
Because of their fast switching speed and low forward voltage drop, Schottky diodes provide the best efficiency, especially in low output voltage switching regulators (less than 5V). Fast-Recovery, High-Efficiency, or Ultra-Fast Recovery diodes are also suitable, but some types with an abrupt turnoff characteristic may cause instability and EMI problems. A fast-recovery diode with soft recovery characteristics is a better choice. Standard 60 Hz diodes (e.g., 1N4001 or 1N5400, etc.) are also not suitable. See Figure 9 for Schottky and "soft" fast-recovery diode selection guide.

## OUTPUT VOLTAGE RIPPLE AND TRANSIENTS

The output voltage of a switching power supply will contain a sawtooth ripple voltage at the switcher frequency, typically about $1 \%$ of the output voltage, and may also contain short voltage spikes at the peaks of the sawtooth waveform.
The output ripple voltage is due mainly to the inductor sawtooth ripple current multiplied by the ESR of the output capacitor. (See the inductor selection in the application hints.) The voltage spikes are present because of the the fast switching action of the output switch, and the parasitic inductance of the output filter capacitor. To minimize these voltage spikes, special low inductance capacitors can be used, and their lead lengths must be kept short. Wiring inductance, stray capacitance, as well as the scope probe used to evaluate these transients, all contribute to the amplitude of these spikes.
An additional small LC filter ( $20 \mu \mathrm{H}$ \& $100 \mu \mathrm{~F}$ ) can be added to the output (as shown in Figure 16 ) to further reduce the amount of output ripple and transients. A $10 \times$ reduction in output ripple voltage and transients is possible with this filter.

## Application Hints (Continued)

The data sheet thermal resistance curves and the thermal model in Switchers Made Simple software (version 3.3) can estimate the maximum junction temperature based on operating conditions. In addition, the junction temperature can be estimated in actual circuit operation by using the following equation.
$\mathrm{T}_{\mathrm{j}}=\mathrm{T}_{\mathrm{cu}}+\left(\theta_{\mathrm{j} \text {-cu }} \times \mathrm{P}_{\mathrm{D}}\right)$
With the switcher operating under worst case conditions and all other components on the board in the intended enclosure, measure the copper temperature ( $\mathrm{T}_{\mathrm{cu}}$ ) near the IC. This can be done by temporarily soldering a small thermocouple to the pc board copper near the IC, or by holding a small thermocouple on the pc board copper using thermal grease for good thermal conduction.
The thermal resistance $\left(\theta_{j-c u}\right)$ for the two packages is:
$\theta_{j \text {-cu }}=42^{\circ} \mathrm{C} / \mathrm{W}$ for the N-8 package
$\theta_{j \text {-cu }}=52^{\circ} \mathrm{C} / \mathrm{W}$ for the $\mathrm{M}-14$ package

The power dissipation $\left(P_{D}\right)$ for the IC could be measured, or it can be estimated by using the formula:

$$
P_{D}=\left(V_{I N}\right)\left(I_{S}\right)+\left(\frac{V_{\mathrm{O}}}{V_{I N}}\right)\left(I_{\text {LOAD }}\right)\left(V_{S A T}\right)
$$

Where $I_{S}$ is obtained from the typical supply current curve (adjustable version use the supply current vs. duty cycle curve).

## Additional Applications

## INVERTING REGULATOR

Figure 11 shows a LM2574-12 in a buck-boost configuration to generate a negative 12 V output from a positive input voltage. This circuit bootstraps the regulator's ground pin to the negative output voltage, then by grounding the feedback pin, the regulator senses the inverted output voltage and regulates it to -12 V .


Note: Pin numbers are for the 8 -pin DIP package.
FIGURE 11. Inverting Buck-Boost Develops -12V

For an input voltage of 8 V or more, the maximum available output current in this configuration is approximately 100 mA . At lighter loads, the minimum input voltage required drops to approximately 4.7 V .
The switch currents in this buck-boost configuration are higher than in the standard buck-mode design, thus lowering the available output current. Also, the start-up input current of the buck-boost converter is higher than the standard buckmode regulator, and this may overload an input power source with a current limit less than 0.6A. Using a delayed turn-on or an undervoltage lockout circuit (described in the next section) would allow the input voltage to rise to a high enough level before the switcher would be allowed to turn on.
Because of the structural differences between the buck and the buck-boost regulator topologies, the buck regulator design procedure section can not be used to to select the inductor or the output capacitor. The recommended range of inductor values for the buck-boost design is between $68 \mu \mathrm{H}$ and $220 \mu \mathrm{H}$, and the output capacitor values must be larger than what is normally required for buck designs. Low input voltages or high output currents require a large value output capacitor (in the thousands of micro Farads).
The peak inductor current, which is the same as the peak switch current, can be calculated from the following formula:

$$
\mathrm{I}_{\mathrm{p}} \approx \frac{\mathrm{I}_{\mathrm{LOAD}}\left(\mathrm{~V}_{\mathrm{IN}}+\left|\mathrm{V}_{\mathrm{O}}\right|\right)}{\mathrm{V}_{\mathrm{IN}}}+\frac{\mathrm{V}_{I N}\left|\mathrm{~V}_{\mathrm{O}}\right|}{\mathrm{V}_{I N}+\left|\mathrm{V}_{\mathrm{O}}\right|} \times \frac{1}{2 \mathrm{~L}_{1} \mathrm{f}_{\mathrm{OSC}}}
$$

Where $\mathrm{f}_{\text {osc }}=52 \mathrm{kHz}$. Under normal continuous inductor current operating conditions, the minimum $\mathrm{V}_{\mathrm{IN}}$ represents the worst case. Select an inductor that is rated for the peak current anticipated.
Also, the maximum voltage appearing across the regulator is the absolute sum of the input and output voltage. For a -12 V output, the maximum input voltage for the LM2574 is +28 V , or +48 V for the LM2574HV.
The Switchers Made Simple version 3.3) design software can be used to determine the feasibility of regulator designs using different topologies, different input-output parameters, different components, etc.

## NEGATIVE BOOST REGULATOR

Another variation on the buck-boost topology is the negative boost configuration. The circuit in Figure 12 accepts an input voltage ranging from -5 V to -12 V and provides a regulated -12 V output. Input voltages greater than -12 V will cause the output to rise above -12 V , but will not damage the regulator.

## Additional Applications (Continued)



Note: Pin numbers are for 8-pin DIP package.

## FIGURE 12. Negative Boost

Because of the boosting function of this type of regulator, the switch current is relatively high, especially at low input voltages. Output load current limitations are a result of the maximum current rating of the switch. Also, boost regulators can not provide current limiting load protection in the event of a shorted load, so some other means (such as a fuse) may be necessary.

## UNDERVOLTAGE LOCKOUT

In some applications it is desirable to keep the regulator off until the input voltage reaches a certain threshold. An undervoltage lockout circuit which accomplishes this task is shown in Figure 13 while Figure 14 shows the same circuit applied to a buck-boost configuration. These circuits keep the regulator off until the input voltage reaches a predetermined level.
$\mathrm{V}_{\mathrm{TH}} \approx \mathrm{V}_{\mathrm{Z} 1}+2 \mathrm{~V}_{\mathrm{BE}}(\mathrm{Q} 1)$


Note: Complete circuit not shown.
Note: Pin numbers are for 8-pin DIP package.
FIGURE 13. Undervoltage Lockout for Buck Circuit


Note: Complete circuit not shown (see Figure 11).
Note: Pin numbers are for 8 -pin DIP package.

## FIGURE 14. Undervoltage Lockout for Buck-Boost Circuit

## DELAYED STARTUP

The $\overline{O N}$ /OFF pin can be used to provide a delayed startup feature as shown in Figure 15. With an input voltage of 20 V and for the part values shown, the circuit provides approximately 10 ms of delay time before the circuit begins switching. Increasing the RC time constant can provide longer delay times. But excessively large RC time constants can cause problems with input voltages that are high in 60 Hz or 120 Hz ripple, by coupling the ripple into the ON /OFF pin. ADJUSTABLE OUTPUT, LOW-RIPPLE POWER SUPPLY
A 500 mA power supply that features an adjustable output voltage is shown in Figure 16. An additional L-C filter that reduces the output ripple by a factor of 10 or more is included in this circuit.


Note: Complete circuit not shown
Note: Pin numbers are for 8-pin DIP package.
FIGURE 15. Delayed Startup

## Additional Applications (Continued)



Note: Pin numbers are for 8-pin DIP package.

FIGURE 16. 1.2V to 55V Adjustable 500 mA Power Supply with Low Output Ripple

## Definition of Terms

## BUCK REGULATOR

A switching regulator topology in which a higher voltage is converted to a lower voltage. Also known as a step-down switching regulator.

## BUCK-BOOST REGULATOR

A switching regulator topology in which a positive voltage is converted to a negative voltage without a transformer.

## DUTY CYCLE (D)

Ratio of the output switch's on-time to the oscillator period.

$$
\begin{aligned}
\text { for buck regulator } & \mathrm{D} & =\frac{t_{\mathrm{ON}}}{T} & =\frac{\mathrm{V}_{\mathrm{OUT}}}{\mathrm{~V}_{\mathrm{IN}}} \\
\text { for buck-boost regulator } & \mathrm{D} & =\frac{t_{\mathrm{ON}}}{T} & =\frac{\left|\mathrm{V}_{\mathrm{O}}\right|}{\left|\mathrm{V}_{\mathrm{O}}\right|+\mathrm{V}_{\mathrm{IN}}}
\end{aligned}
$$

## CATCH DIODE OR CURRENT STEERING DIODE

The diode which provides a return path for the load current when the LM2574 switch is OFF.
EFFICIENCY ( $\eta$ )
The proportion of input power actually delivered to the load.

$$
\eta=\frac{\mathrm{P}_{\mathrm{OUT}}}{\mathrm{P}_{\mathrm{IN}}}=\frac{\mathrm{P}_{\text {OUT }}}{\mathrm{P}_{\text {OUT }}+\mathrm{P}_{\text {LOSS }}}
$$

## CAPACITOR EQUIVALENT SERIES RESISTANCE (ESR)

The purely resistive component of a real capacitor's impedance (see Figure 17). It causes power loss resulting in capacitor heating, which directly affects the capacitor's operating lifetime. When used as a switching regulator output filter, higher ESR values result in higher output ripple voltages.


FIGURE 17. Simple Model of a Real Capacitor
Most standard aluminum electrolytic capacitors in the $100 \mu \mathrm{~F}-1000 \mu \mathrm{~F}$ range have $0.5 \Omega$ to $0.1 \Omega$ ESR. Higher-
grade capacitors ("low-ESR", "high-frequency", or "lowinductance") in the $100 \mu \mathrm{~F}-1000 \mu \mathrm{~F}$ range generally have ESR of less than $0.15 \Omega$.

## EQUIVALENT SERIES INDUCTANCE (ESL)

The pure inductance component of a capacitor (see Figure 17). The amount of inductance is determined to a large extent on the capacitor's construction. In a buck regulator, this unwanted inductance causes voltage spikes to appear on the output.

## OUTPUT RIPPLE VOLTAGE

The AC component of the switching regulator's output voltage. It is usually dominated by the output capacitor's ESR multiplied by the inductor's ripple current ( $\Delta \mathrm{I}_{\mathrm{IND}}$ ). The peak-to-peak value of this sawtooth ripple current can be determined by reading the Inductor Ripple Current section of the Application hints.

## CAPACITOR RIPPLE CURRENT

RMS value of the maximum allowable alternating current at which a capacitor can be operated continuously at a specified temperature.

## STANDBY QUIESCENT CURRENT (I Stby )

Supply current required by the LM2574 when in the standby mode ( $\overline{\mathrm{ON}} / \mathrm{OFF}$ pin is driven to TTL-high voltage, thus turning the output switch OFF).

## INDUCTOR RIPPLE CURRENT ( $\Delta \mathrm{I}_{\mathrm{IND}}$ )

The peak-to-peak value of the inductor current waveform, typically a sawtooth waveform when the regulator is operating in the continuous mode (vs. discontinuous mode).

## CONTINUOUS/DISCONTINUOUS MODE OPERATION

Relates to the inductor current. In the continuous mode, the inductor current is always flowing and never drops to zero, vs. the discontinuous mode, where the inductor current drops to zero for a period of time in the normal switching cycle.

## Definition of Terms (Continued)

## INDUCTOR SATURATION

The condition which exists when an inductor cannot hold any more magnetic flux. When an inductor saturates, the inductor appears less inductive and the resistive component dominates. Inductor current is then limited only by the DC resistance of the wire and the available source current.

OPERATING VOLT MICROSECOND CONSTANT (E•T ${ }_{\text {op }}$ ) The product (in Volt• $\mu \mathrm{s}$ ) of the voltage applied to the inductor and the time the voltage is applied. This $\mathrm{E} \cdot \mathrm{T}_{\mathrm{op}}$ constant is a measure of the energy handling capability of an inductor and is dependent upon the type of core, the core area, the number of turns, and the duty cycle.

Physical Dimensions inches (millimeters) unless otherwise noted


14-Lead Wide Surface Mount (WM)
Order Number LM2574M-3.3, LM2574HVM-3.3, LM2574M-5.0,
LM2574HVM-5.0, LM2574M-12, LM2574HVM-12, LM2574M-15, LM2574HVM-15, LM2574M-ADJ or LM2574HVM-ADJ

## NS Package Number M14B

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)


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