

FEATURES

1. This specification shall be applied to photocoupler. Model No. LTV-817 as an option.

2. Applicable Models (Business dealing name)

* Dual-in-line package :

LTV702V-V

* Wide lead spacing package :

LTV702VM-V

* Surface mounting package :

LTV702VS-V

* Tape and reel packaging :

LTV702VSTA1-V

3. The relevant models are the models Approved by VDE according to DIN VDE 0884:1992-06

Approved Model No.: LTV-702V / LTV-702VM / LTV-702VS

VDE approved No.: 094722

(According to the specification DIN VDE 0884:1992-06)

* Operating isolation voltage V_{IORM} : 700V (Peak)

* Transient voltage V_{TR} : 6000V (Peak)

* Pollution : 2 (According to VDE 0110-1 : 1997-04)

* Clearances distance (Between input and output) : 7.0mm (MIN.)

* Creepage distance (Between input and output) : 7.0mm (MIN.)


* Isolation thickness between input and output : 0.4mm (MIN.)

* Safety limit values Current (I_{si}) : 400mA (Diode side)

Power (P_{si}) : 700mW (Phototransistor side)

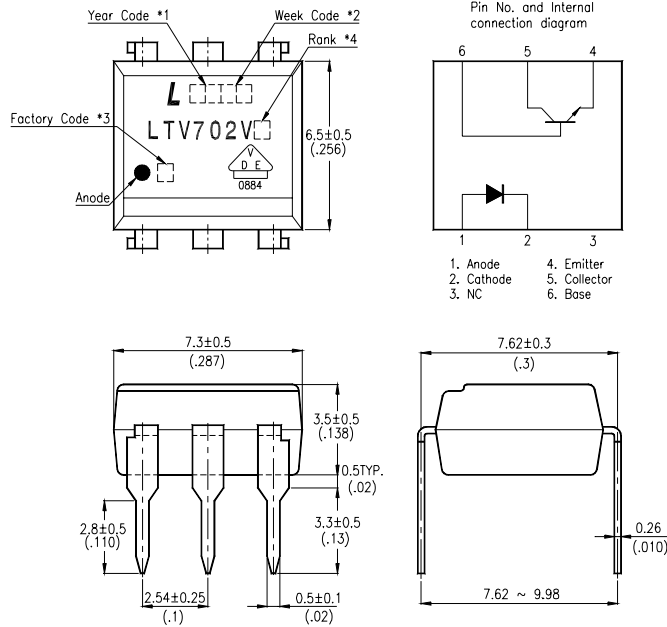
Temperature(T_{si}) : 175°C

In order to keep safety electric isolation of photocoupler, please set the protective circuit to keep within safety limit values when the actual application equipment troubled.

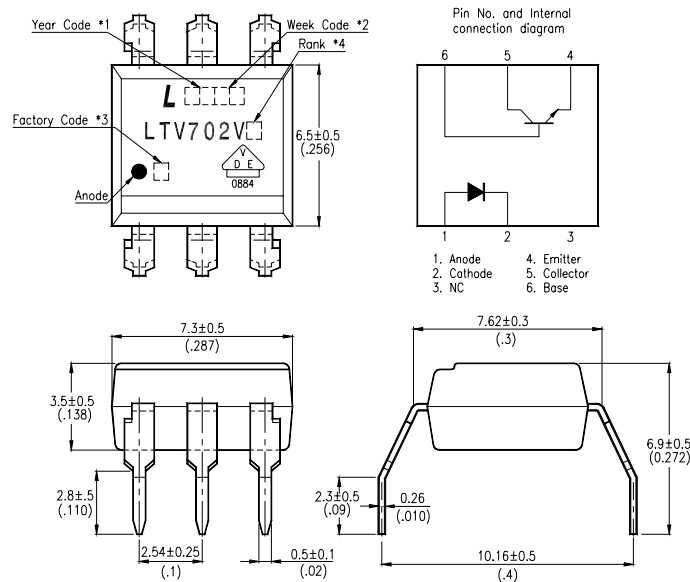
* Indication of VDE 0884 approval prints "  " on sleeve package.

OUTLINE DIMENSIONS

LTV702V-V :



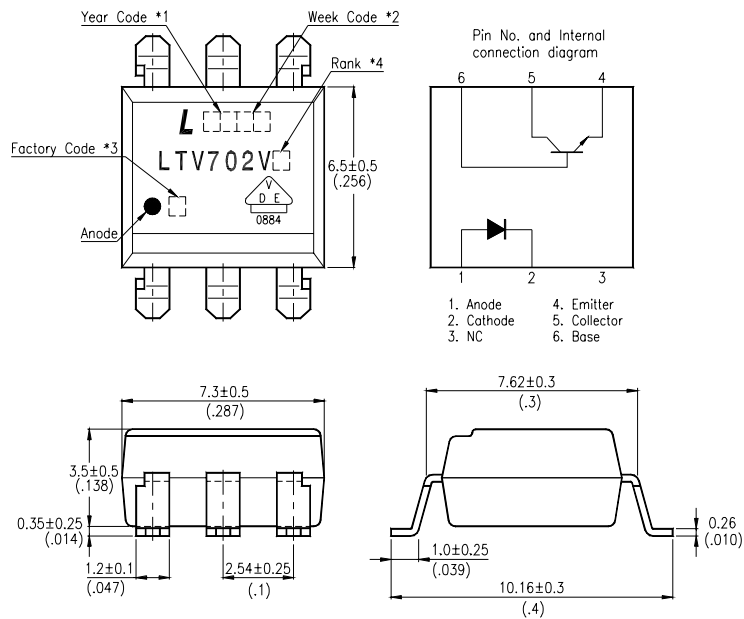
LTV702VM-V :



- *1. Year date code.
- *2. 2-digit work week.
- *3. Factory identification mark shall be marked (Z : Taiwan, Y : Thailand, X : China).
- *4. Rank shall be or shall not be marked.

OUTLINE DIMENSIONS

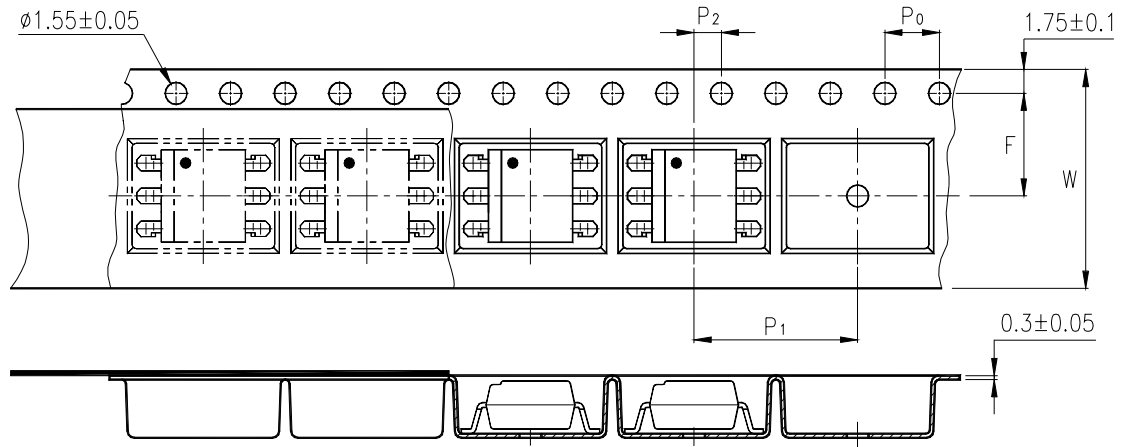
LTV702VS-V :



- *1. Year date code.
- *2. 2-digit work week.
- *3. Factory identification mark shall be marked (Z : Taiwan, Y : Thailand, X : China).
- *4. Rank shall be or shall not be marked.

TAPING DIMENSIONS

LTV702VSTA1-V :



| Description | Symbol | Dimensions in mm (inches) |
|--|--------|-----------------------------|
| Tape wide | W | 16 ± 0.3 (.63) |
| Pitch of sprocket holes | P_0 | 4 ± 0.1 (.15) |
| Distance of compartment | F | 7.5 ± 0.1 (.295) |
| Distance of compartment to compartment | P_1 | 2 ± 0.1 (.079) |
| Distance of compartment to compartment | P_2 | 12 ± 0.1 (.472) |

ABSOLUTE MAXIMUM RATING

(Ta = 25°C)

| PARAMETER | | SYMBOL | RATING | UNIT |
|-------------------------|-----------------------------|------------------|------------|------------------|
| INPUT | Forward Current | I _F | 60 | mA |
| | Reverse Voltage | V _R | 6 | V |
| | Power Dissipation | P | 105 | mW |
| OUTPUT | Collector - Emitter Voltage | V _{CEO} | 70 | V |
| | Emitter - Collector Voltage | V _{ECO} | 6 | V |
| | Collector - Base Voltage | V _{CBO} | 70 | V |
| | Emitter - Base Voltage | V _{EBO} | 6 | V |
| | Collector Current | I _C | 50 | mA |
| | Collector Power Dissipation | P _C | 160 | mW |
| Total Power Dissipation | | P _{tot} | 200 | mW |
| *1 | Isolation Voltage | V _{iso} | 5,000 | V _{rms} |
| Operating Temperature | | T _{opr} | -55 ~ +100 | °C |
| Storage Temperature | | T _{stg} | -55 ~ +150 | °C |
| *2 | Soldering Temperature | T _{sol} | 260 | °C |

*1. AC For 1 Minute, R.H. = 40 ~ 60%

Isolation voltage shall be measured using the following method.

- (1) Short between anode and cathode on the primary side and between collector, emitter and base on the secondary side.
- (2) The isolation voltage tester with zero-cross circuit shall be used.
- (3) The waveform of applied voltage shall be a sine wave.

*2. For 10 Seconds

ELECTRICAL - OPTICAL CHARACTERISTICS

(Ta = 25°C)

| PARAMETER | | SYMBOL | MIN. | TYP. | MAX. | UNIT | CONDITIONS |
|--------------------------|--------------------------------------|----------------------|--------------------|--------------------|------|------|--|
| INPUT | Forward Voltage | V _F | — | 1.4 | 1.7 | V | I _F =60mA |
| | Reverse Current | I _R | — | — | 10 | μA | V _R =6V |
| | Terminal Capacitance | C _t | — | 30 | 250 | pF | V=0, f=1KHz |
| OUTPUT | Collector Dark Current | I _{CEO} | — | — | 50 | nA | V _{CE} =10V, I _F =0 |
| | Collector-Emitter Breakdown Voltage | BV _{CEO} | 70 | — | — | V | I _C =0.1mA I _F =0 |
| | Emitter-Collector Breakdown Voltage | BV _{ECO} | 6 | — | — | V | I _E =10μA I _F =0 |
| | Collector-Base Breakdown Voltage | BV _{CBO} | 70 | — | — | V | I _C =0.1mA I _F =0 |
| TRANSFER CHARACTERISTICS | Collector Current | I _C | 4 | — | 32 | mA | I _F =10mA |
| | * Current Transfer Ratio | CTR | 40 | — | 320 | % | V _{CE} =5V |
| | Collector-Emitter Saturation Voltage | V _{CE(sat)} | — | 0.25 | 0.4 | V | I _F =10mA I _C =2.5mA |
| | Isolation Resistance | R _{iso} | 5×10 ¹⁰ | 1×10 ¹¹ | — | Ω | DC500V 40 ~ 60% R.H. |
| | Floating Capacitance | C _f | — | 0.6 | 1 | pF | V=0, f=1MHz |
| | Cut-Off Frequency | f _c | — | 150 | — | kHz | V _{CC} =5V, I _F =10mA R _L =75Ω, -3dB |
| | Response Time (Rise) | t _r | — | 2 | 7 | μs | V _{CC} =5V, I _F =10mA R _L =75Ω |
| | Response Time (Fall) | t _f | — | 2 | 8 | μs | |

$$* \text{CTR} = \frac{I_C}{I_F} \times 100\%$$

RANK TABLE OF CURRENT TRANSFER RATIO CTR

| MODEL NO. | RANK MARK | CTR (%) |
|-----------|-----------------------------|-----------|
| LTV702V-V | A | 40 ~ 80 |
| | B | 63 ~ 125 |
| | C | 100 ~ 200 |
| | D | 160 ~ 320 |
| | A or B or C or D or No mark | 40 ~ 320 |

| | |
|-------------------|--|
| CONDITIONS | $I_F = 10 \text{ mA}$ $V_{CE} = 5 \text{ V}$ $T_a = 25 \text{ }^\circ\text{C}$ |
|-------------------|--|

ISOLATION SPECIFICATION ACCORDING TO VDE 0884

| Parameter | Symbol | Conditions | Rating | Unit | Remark |
|---|---------------|---------------------|--------------------|-------------|---------------------------|
| Class of environmental test | - | DIN IEC68 | 55/100/21 | - | |
| Pollution | - | DIN VDE0110 | 2 | - | |
| Maximum Operating Isolation Voltage | V_{IORM} | - | 700 | V_{PEAK} | Refer to the Diagram 1, 2 |
| Partial Discharge Test Voltage (Between Input and Output) | Diagram 1 | V_{pr} | $t_p=60s, q_c<5pC$ | V_{PEAK} | |
| | Diagram 2 | | $t_p=1s, q_c<5pC$ | V_{PEAK} | |
| Maximum Over-voltage | $V_{INITIAL}$ | $t_{INI} = 10s$ | 6000 | V_{PEAK} | |
| Safety Maximum Ratings | | | | | Refer to the Figure 1, 3 |
| 1) Case Temperature | T_{si} | $I_F = 0, P_c = 0$ | 175 | $^{\circ}C$ | |
| 2) Input Current | I_{si} | $P_c=0$ | 400 | mA | |
| 3) Electric Power (Output or Total Power Issipation) | P_{si} | - | 700 | mW | |
| Isolation Resistance (Test Voltage Between Input and Output : DC500V) | R_{ISO} | $T_a=T_{si}$ | MIN. 10^9 | Ω | |
| | | $T_a=T_{opr}(MAX.)$ | MIN. 10^{11} | | |
| | | $T_a=25^{\circ}C$ | MIN. 10^{12} | | |

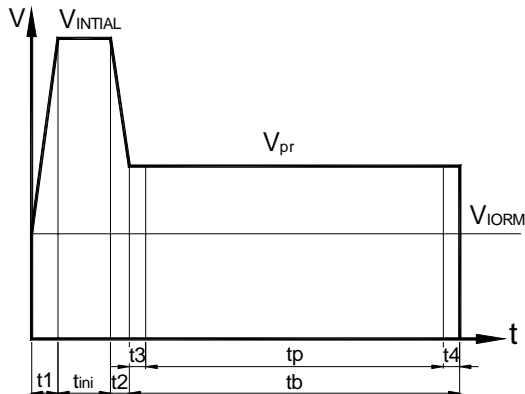
Precautions in performing isolation test

* Partial discharge test methods shall be the ones according to the specifications of VDE 0884:1992-06

* Please don't carry out isolation test (V_{ISO}) over $V_{INITIAL}$, This product deteriorates isolation characteristics by partial discharge due to applying high voltage (ex. $V_{INITIAL}$). And there is possibility that this product occurs partial discharge in operating isolation voltage (V_{IORM})

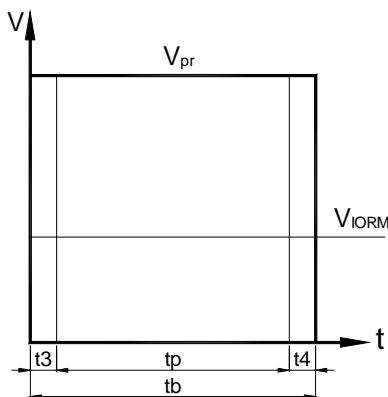
PARTIAL DISCHARGE TEST METHOD

Method (A) for type testing and random testing.



t_1, t_2 = 1 to 10s
 t_3, t_4 = 1s
 t_p (Partial Discharge Measuring Time) = 60s
 t_b = 62s
 t_{ini} = 10s

Method (B) for routine testing.



t_3, t_4 = 0.1s
 t_p (Partial Discharge Measuring Time) = 1s
 t_b = 1.2s

The partial discharge level shall not exceed 5 pC during the partial discharge measuring time interval t_p under the test conditions shown above.

CHARACTERISTICS CURVES

Fig.1 Forward Current vs. Ambient Temperature

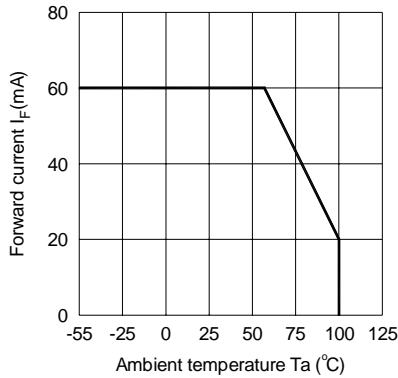


Fig.2 Collector Power Dissipation vs. Ambient Temperature

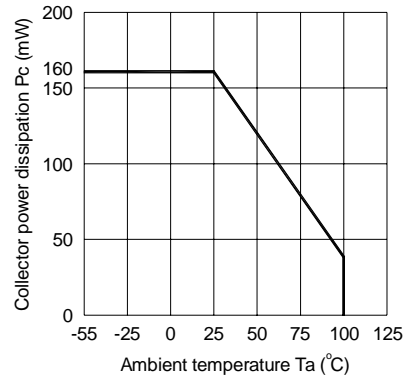


Fig.3 Collector-emitter Saturation Voltage vs. Forward Current

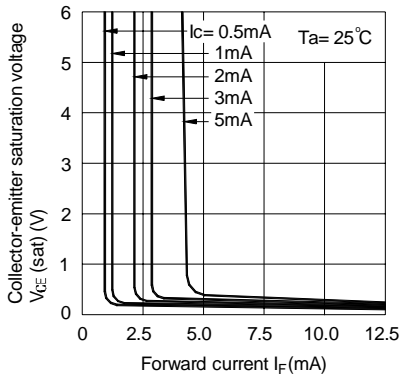


Fig.4 Forward Current vs. Forward Voltage

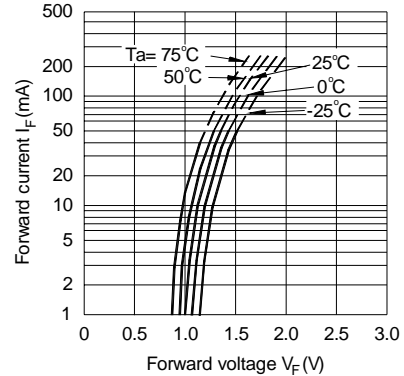


Fig.5 Current Transfer Ratio vs. Forward Current

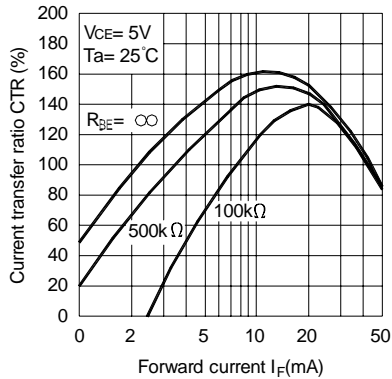
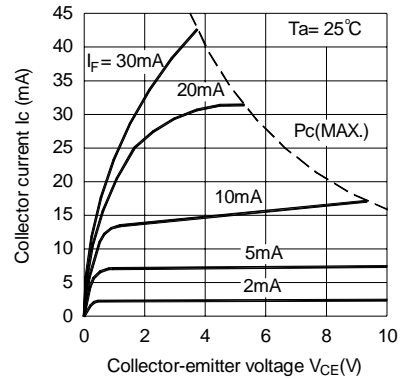


Fig.6 Collector Current vs. Collector-emitter Voltage



CHARACTERISTICS CURVES

Fig.7 Relative Current Transfer Ratio vs. Ambient Temperature

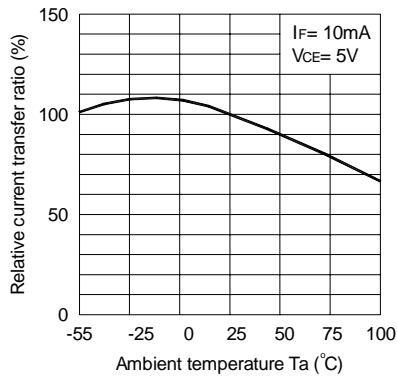


Fig.8 Collector-emitter Saturation Voltage vs. Ambient Temperature

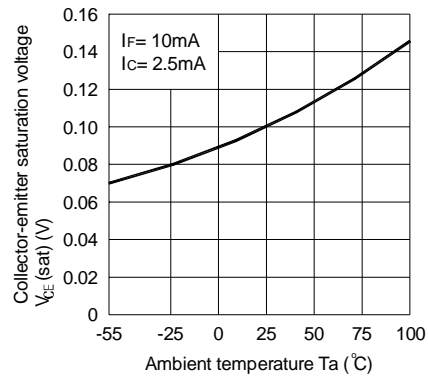


Fig.9 Collector Dark Current vs. Ambient Temperature

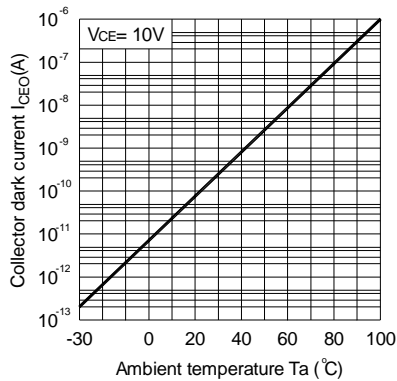


Fig.10 Response Time vs. Load Resistance

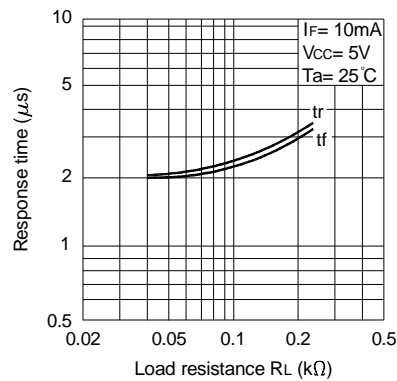
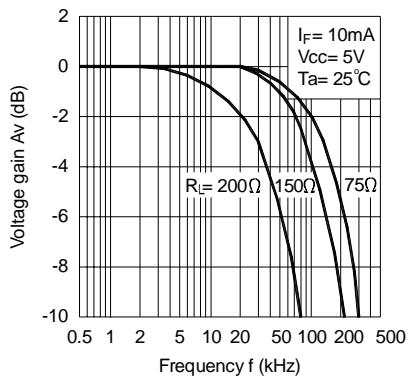
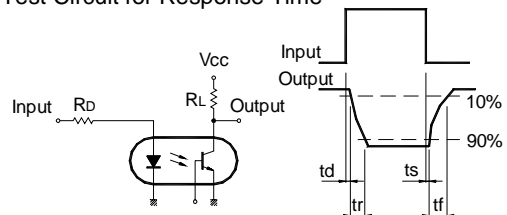


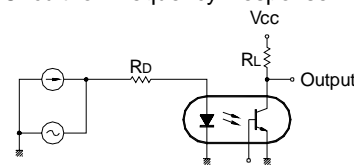
Fig.11 Frequency Response



Test Circuit for Response Time



Test Circuit for Frequency Response



RECOMMENDED FOOT PRINT PATTERNS (MOUNT PAD)

Unit : mm

