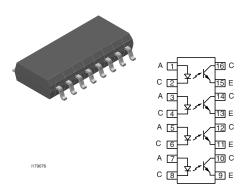




Optocoupler, Phototransistor Output, Quad Channel, SOP-16, Half Pitch Mini-Flat Package



DESCRIPTION

SFH6916

The SFH6916 has a GaAs infrared emitter, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a 16 pin 50 mil lead pitch miniflat package. It features a high current transfer ratio, low coupling capacitance, and high isolation voltage.

The coupling devices are designed for signal transmission

FEATURES

- SOP (small outline package)
- Isolation test voltage, 3750 V_{RMS} (1.0 s)
- High collector emitter voltage, V_{CEO} = 70 V
- · Low saturation voltage
- · Fast switching times
- Temperature stable
- · Low coupling capacitance
- End stackable, 0.050" (1.27 mm) spacing
- Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

CTR 50 to 300 %, SOP-16

AGENCY APPROVALS

- UL1577, file no. E52744 system code U
- DIN EN 60747-5-5 available with option 1

The coupling devices are designed for signal transmission	
between two electrically separated circuits.	
ORDER INFORMATION	
PART	REMARKS
PARI	REWARKS

ABSOLUTE MAXIMUM RATINGS (1)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
INPUT							
Reverse voltage		V _R	6.0	V			
DC forward current		I _F	50	mA			
Surge forward current	t _p ≤ 10 μs	I _{FSM}	2.5	Α			
Total power dissipation		P _{diss}	80	mW			
OUTPUT							
Collector emitter voltage		V _{CE}	70	٧			
Emitter collector voltage		V _{EC}	7.0	V			
Collector current		I _C	50	mA			
	$t_p = 1.0 \text{ ms}$	I _C	100	mA			
Total power dissipation per channel		P _{diss}	150	mW			
COUPLER							
Isolation test voltage between emitter and detector (1.0 s)		V _{ISO}	3750	V_{RMS}			
Isolation resistance	V _{IO} = 500 V, T _{amb} = 25 °C	R _{IO}	≥ 10 ¹²	Ω			
SOIATION TESISTANCE	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	≥ 10 ¹¹	Ω			
Storage temperature range		T _{stg}	- 55 to + 125	°C			

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ABSOLUTE MAXIMUM RATINGS ⁽¹⁾						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
COUPLER						
Ambient temperature range		T _{amb}	- 55 to +100	°C		
Junction temperature		Tj	100	°C		
Soldering temperature (2)	max. 10 s dip soldering distance to seating plane ≥ 1.5 mm		260	°C		
Total power dissipation		P _{tot}	700	mW		

Notes

⁽²⁾ Refer to reflow profile for soldering conditions for surface mounted devices.

ELECTRICAL CHARACTERISTICS (1)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT (2)							
Forward voltage	I _F = 5 mA	V_{F}		1.15	1.4	V	
Reverse current	$V_{R} = 6.0 \text{ V}$	I _R		0.01	10	μΑ	
Capacitance	Co	Co		14		pF	
Thermal resistance		R _{thja}		1000		K/W	
OUTPUT							
Collector emitter leakage current	V _{CE} = 20 V	I _{CEO}			100	nA	
Collector emitter capacitance	V _{CE} = 5.0 V, f = 1.0 MHz	C _{CE}		2.8		pF	
Thermal resistance		R _{thja}		500		K/W	
COUPLER							
Collector emitter saturation voltage	$I_F = 20 \text{ mA}, I_C = 1.0 \text{ mA}$	V _{CEsat}		0.1	0.4	V	
Coupling capacitance	f = 1.0 MHz	C _C		1.0		pF	

Notes

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

 $^{^{(2)}}$ T_{amb} = 25 $^{\circ}$ C (except where noted).

CURRENT TRANSFER RATIO						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Current transfer ratio	$I_F = 5.0 \text{ mA}, V_{CC} = 5.0 \text{ V}$	CTR	50		300	%

SWITCHING CHARACTERISTICS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
NON-SATURATED							
Rise time	I_C = 2.0 mA, V_{CC} = 10 V, R_L = 100 Ω	t _r		4.0		μs	
Fall time	I_C = 2.0 mA, V_{CC} = 10 V, R_L = 100 Ω	t _f		3.0		μs	
Turn-on time	I_C = 2.0 mA, V_{CC} = 10 V, R_L = 100 Ω	t _{on}		5.0		μs	
Turn-off time	I_C = 2.0 mA, V_{CC} = 10 V, R_L = 100 Ω	t_{off}		4.0		μs	
SATURATED	SATURATED						
Rise time	I_F = 16.0 mA, V_{CC} = 5.0 V, R_L = 1.9 $k\Omega$	t _r		15		μs	
Fall time	$I_F = 16.0 \text{ mA}, V_{CC} = 5.0 \text{ V}, R_L = 1.9 \text{ k}\Omega$	t _f		0.5		μs	
Turn-on time	I_F = 16.0 mA, V_{CC} = 5.0 V, R_L = 1.9 $k\Omega$	t _{on}		1.0		μs	
Turn-off time	$I_F = 16.0 \text{ mA}, V_{CC} = 5.0 \text{ V}, R_L = 1.9 \text{ k}\Omega$	t _{off}		30		μs	

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 $^{^{(1)}}$ $T_{amb} = 25$ °C, unless otherwise specified.

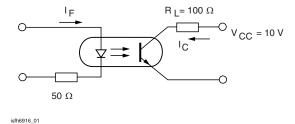
Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

 $^{^{(1)}}$ T_{amb} = 25 °C, unless otherwise specified.



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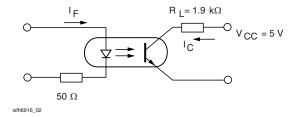


Fig. 1 - Switching Operation (Without Saturation)

Fig. 2 - Switching Operation (With Saturation)

SAFETY AND INSULATION RATINGS								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Climatic classification (according to IEC 68 part 1)				55/100/21				
Comparative tracking index		CTI	175		399			
Peak transient overvoltage		V _{IOTM}	6000			V		
Peak insulation voltage		V _{IORM}	707			V		
Safety rating - power output		P _{SO}			350	mW		
Safety rating - input current		I _{SI}			150	mA		
Safety rating - temperature		T _{SI}			175	°C		
Creepage distance			5			mm		
Clearance distance			5			mm		

Note

As per IEC 60747-5-2, § 7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

TYPICAL CHARACTERISTICS

T_{amb} = 25 °C, unless otherwise specified

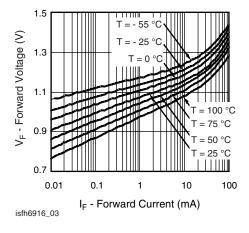
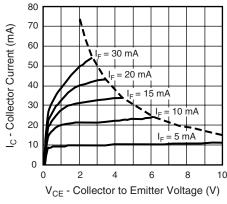


Fig. 3 - Diode Forward Voltage vs. Forward Current



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Fig. 4 - Collector Current vs. Collector Emitter Voltage

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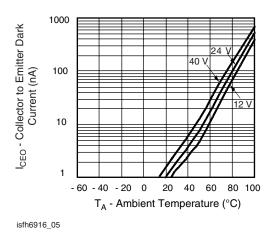


Fig. 5 - Collector to Emitter Dark Current vs. Ambient Temperature

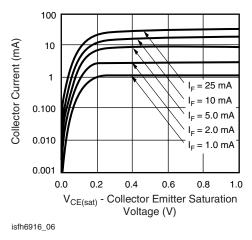


Fig. 6 - Collector Current vs. Collector Emitter Saturation Voltage

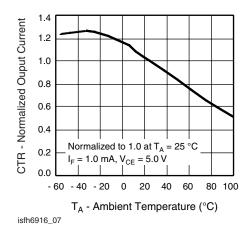


Fig. 7 - Normalized Output Current vs. Ambient Temperature

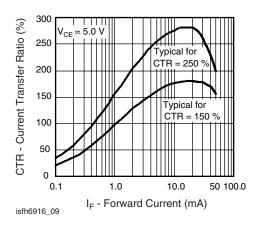
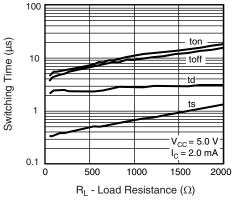
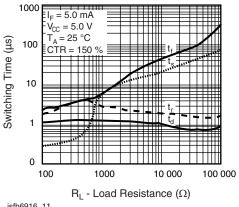


Fig. 8 - Current Transfer Ratio vs. Forward Current



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Fig. 9 - Switching Time vs. Load Resistance



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Fig. 10 - Switching Time vs. Load Resistance



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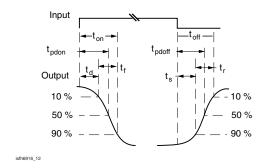
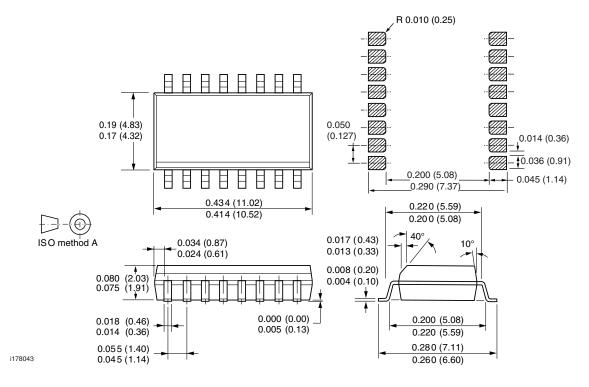


Fig. 11 - Switching Time Measurement

PACKAGE DIMENSIONS in inches (millimeters)



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OZONE DEPLETING SUBSTANCES POLICY STATEMENT

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA.
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany

www.vishay.com

For technical questions, contact: optocoupler.answers@vishay.com

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