TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC4538BP,TC4538BF

#### TC4538BP/TC4538BF Dual Precision

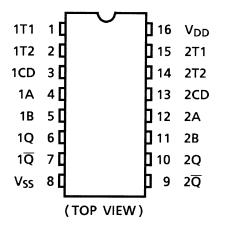
#### Retriggerable/Resettable Monostable Multivibrator

The TC4538BP/BF is the retriggerable/resettable monostable multivibrator and the trigger operation can be made at either the leading or trailing edge by 2 inputs of A and B. Since the output monostable pulse width is decided by time constant of the external resistor ( $R_X$ ) and the external capacitor ( $C_X$ ), it becomes possible to set a broad range of output pulse widths.

#### Features

•  $t_{wOUT} = 10 \text{ ms} \pm 5\%$  (at  $R_X = 100 \text{ k}\Omega \text{ C}X = 0.1 \mu\text{F}$ ,  $V_{DD} = 10$  V)

#### **Pin Assignment**

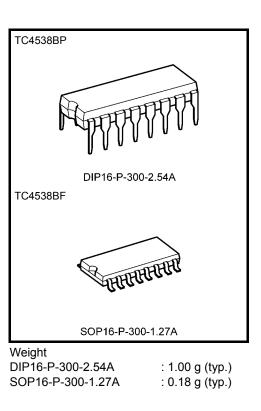


#### Truth Table (Note)

	Inputs		Out	puts	Nista		
А	В	CD	Q	Q	Note		
	Н	Н	$\Box$	Л	Output Enable		
	L	Н	L	Н	Inhibit		
Н		Н	L	Н	Inhibit		
L		Н		$\Box$	Output Enable		
*	*	L	L	Н	Inhibit		

\*: Don't care

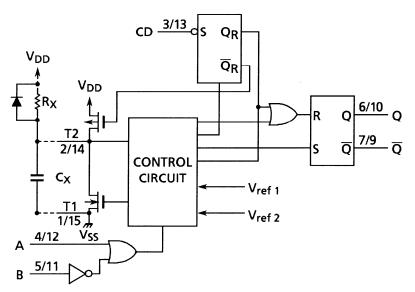
Note: In the case of using only one circuit, CD should be tied to GND, T<sub>2</sub>, T<sub>1</sub>, Q,  $\overline{Q}$  should be tied to OPEN, and the other inputs should be tied to V<sub>CC</sub> or GND.



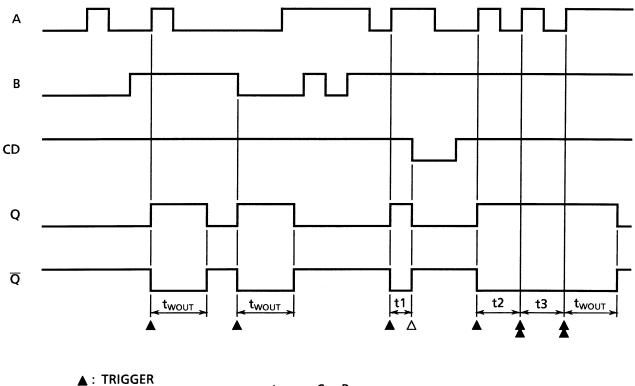
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### Logic Diagram

1/2 TC4538BP/BF



### **Timing Chart**



	t Courd Day
E RETRIGGER	$t_{WOUT} = C_X \cdot R_X$ t1 \cdot t2 \cdot t3 ; t1 \cdot t2 \cdot t3 < t_{WOUT}
∆: RESET	

#### **Absolute Maximum Ratings (Note)**

Characteristics	Symbol	Rating	Unit
DC supply voltage	V <sub>DD</sub>	$V_{SS}{-}0.5$ to $V_{SS}{+}20$	V
Input voltage	V <sub>IN</sub>	$V_{\mbox{\scriptsize SS}}$ – 0.5 to $V_{\mbox{\scriptsize DD}}$ + 0.5	V
Output voltage	V <sub>OUT</sub>	$V_{SS}-0.5$ to $V_{DD}+0.5$	V
DC input current	I <sub>IN</sub>	±10	mA
Power dissipation	PD	300 (DIP)/180 (SOIC)	mW
Operating temperature range	T <sub>opr</sub>	-40 to 85	°C
Storage temperature range	T <sub>stg</sub>	–65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### Operating Ranges (V<sub>SS</sub> = 0 V) (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	V <sub>DD</sub>	—	3	_	18	V
Input voltage	V <sub>IN</sub>	_	0	_	V <sub>DD</sub>	V
External resistance	R <sub>X</sub>		5	_	1000	kΩ
External capacitance	C <sub>X</sub>			No limits		μF

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{DD}$  or  $V_{SS}$ .

# Static Electrical Characteristics ( $V_{SS} = 0 V$ )

Characteristics Symbol		Svm-	Test Condition		-40°C		25°C			85°C		
			V <sub>DD</sub> (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit	
High-level output Voltage			I <sub>OUT</sub>  < 1 µА V <sub>IN</sub> = V <sub>SS</sub> , V <sub>DD</sub>	5	4.95	_	4.95	5.00	_	4.95	_	
		V <sub>OH</sub>		10	9.95	—	9.95	10.00	—	9.95	—	V
			VIN - VSS, VDD	15	14.95	_	14.95	15.00	_	14.95	_	
			I <sub>OUT</sub>   < 1 μΑ	5	—	0.05	—	0.00	0.05		0.05	
Low-level voltage	output	V <sub>OL</sub>	$V_{IN} = V_{SS}, V_{DD}$	10	—	0.05	—	0.00	0.05		0.05	V
Ū			VIN - VSS, VDD	15		0.05	—	0.00	0.05		0.05	
			V <sub>OH</sub> = 4.6 V	5	-0.61	—	-0.51	-1.0	—	-0.42	—	
			$V_{OH} = 2.5 V$	5	-2.50	—	-2.10	-4.0	—	-1.70	_	mA
Output hig	h current	IOH	V <sub>OH</sub> = 9.5 V	10	-1.50	—	-1.30	-2.2	—	-1.10	_	
			V <sub>OH</sub> = 13.5 V	15	-4.00	—	-3.40	-9.0	—	-2.80	—	
			$V_{IN}=V_{SS},\ V_{DD}$									
		1	V <sub>OL</sub> = 0.4 V	5	0.61		0.51	1.5	_	0.42		mA
Output Iou	( current		$V_{OL} = 0.5 V$	10	1.50	—	1.30	3.8	—	1.10	—	
Output low current		I <sub>OL</sub>	V <sub>OL</sub> = 1.5 V	15	4.00	—	3.40	15.0	—	2.80	—	ШA
			$V_{IN}=V_{SS},\ V_{DD}$									
		VIH	$V_{OUT} = 0.5 V, 4.5 V$	5	3.5	_	3.5	2.75	_	3.5	_	V
Input high	voltaga		V <sub>OUT</sub> = 1.0 V, 9.0 V	10	7.0	—	7.0	5.50	—	7.0	—	
input nigh	vollage		V <sub>OUT</sub> = 1.5 V, 13.5 V	15	11.0	—	11.0	8.25	—	11.0	—	
			$ I_{OUT}  < 1 \ \mu A$									
			$V_{OUT} = 0.5 V, 4.5 V$	5	_	1.5	_	2.25	1.5	_	1.5	
Input low	oltago		V <sub>OUT</sub> = 1.0 V, 9.0 V	10	—	3.0	—	4.50	3.0	_	3.0	
Input low voltage		VIL	V <sub>OUT</sub> = 1.5 V, 13.5 V	15	—	4.0	—	6.75	4.0	—	4.0	V
			$ I_{OUT}  < 1 \ \mu A$									
Input "	"H" level	IIН	V <sub>IH</sub> = 18 V	18	_	0.1	_	10 <sup>-5</sup>	0.1		1.0	
current	"L" level	١ <sub>١L</sub>	$V_{IL} = 0 V$	18	_	-0.1	_	-10 <sup>-5</sup>	-0.1		μA –1.0	μA
			$V_{IN} = V_{SS}, V_{DD}$	5	_	5	_	0.005	5		150	
Quiescent supply current		I <sub>DD</sub>		10	—	10	—	0.010	10		300	μA
			(Note)	15		20	_	0.015	20		600	

Note: All valid input combinations.

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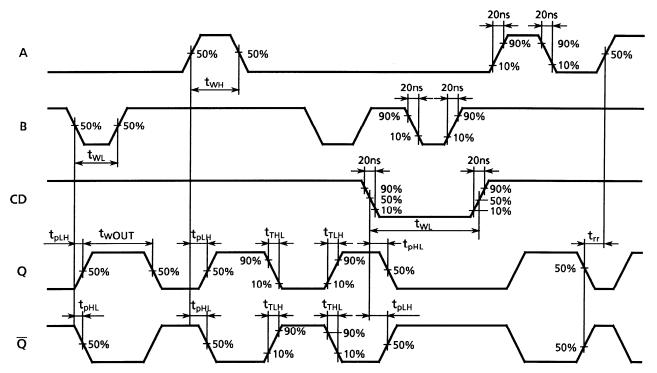
# Dynamic Electrical Characteristics (Ta = $25^{\circ}$ C, V<sub>SS</sub> = 0 V, C<sub>L</sub> = 50 pF)

Characteristics	Symbol	Test Condition		Min	Тур.	Max	Unit
Cital actensities	Symbol		V <sub>DD</sub> (V)				
			5	_	80	200	
Output transition time	t <sub>TLH</sub>	—	10	_	50	100	ns
(low to high)			15	_	40	80	
Output transition time			5	_	80	200	
Output transition time	t <sub>THL</sub>	—	10	_	50	100	ns
(high to low)			15	—	40	80	
Propagation delay time	<b>+</b>		5	_	380	760	
(A, B-Q, $\overline{Q}$ )	t <sub>pLH</sub>	_	10	—	150	300	ns
(A, b-Q, Q)	<sup>t</sup> pHL		15	—	100	220	
Propagation delay time	t		5	—	280	560	
(CD-Q, $\overline{Q}$ )	t <sub>pLH</sub>	—	10	—	110	250	ns
	<sup>t</sup> pHL		15	—	75	190	
Min input pulse width	twн	_	5	—	60	120	
(A, B)	t <sub>WL</sub>		10	—	30	60	ns
			15	—	25	50	
Min pulse width		_	5	—	95	190	
(CD)	t <sub>WL</sub>		10	—	45	90	ns
			15		35	70	
		_	5	—	0	—	
Min retrigger time	t <sub>rr</sub>		10	—	0	—	ns
			15	—	0	—	
		R <sub>X</sub> = 100 kΩ	5	—	206	—	
		$C_{X} = 0.002 \ \mu F$	10	—	204	—	μS
			15		205		
		R <sub>X</sub> = 100 kΩ	5	9.30	9.95	10.40	
Output pulse width	t <sub>wOUT</sub>	C <sub>X</sub> = 0.1 μF	10	9.50	10.00	10.50	ms
			15	9.55	10.05	10.65	
		R <sub>X</sub> = 100 kΩ	5	—	0.98	—	
		C <sub>X</sub> = 10 μF	10	—	1.00	—	S
			15	—	1.01	—	
Pulse width match between circuits in	∆t <sub>wOUT</sub>	$t_{WOUT}(Q2) - t_{W}(Q1)$	5	—	±1	—	
the same package		$\frac{t_{wOUT}(Q2) - t_w(Q1)}{t_{wOUT}(Q1)} \times 100$	10	—	±1	—	%
			15	—	±1	—	
Input capacitance	CIN	—		—	5	7.5	pF

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#### Waveform for Measurement of Dynamic Characteristics

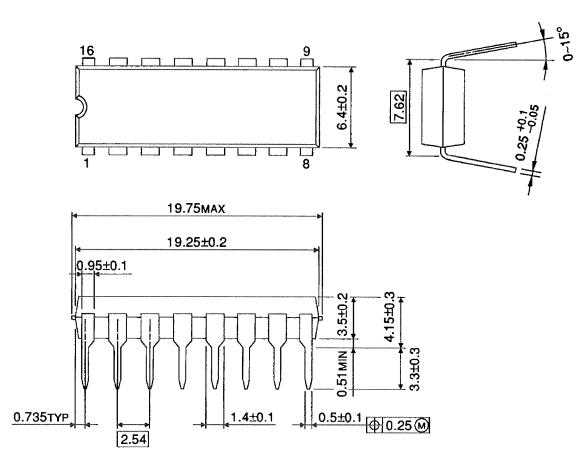
#### Waveform



#### **Package Dimensions**

DIP16-P-300-2.54A

Unit : mm



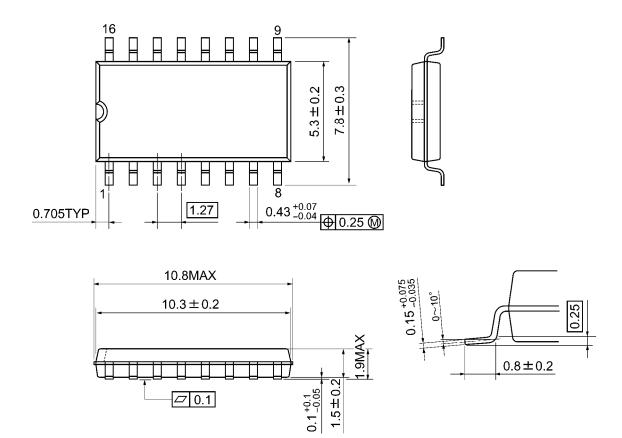
Weight: 1.00 g (typ.)



#### **Package Dimensions**

SOP16-P-300-1.27A

Unit: mm



Weight: 0.18 g (typ.)

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