



Dual N-/Dual P-Channel 30-V (D-S) MOSFETs

PRODUCT SUMMARY						
	V _{(BR)DSS} Min (V)	$r_{DS(on)}$ Max (Ω)	V _{GS(th)} (V)	I _D (A)		
N-Channel	30	1 @ V _{GS} = 12 V	0.8 to 2.5	0.85		
P-Channel	-30	2 @ V _{GS} = -12 V	−2 to −4.5	-0.6		

FEATURES

Low On-Resistance: 0.8/1.6 Ω
Low Threshold: 1.5/–3.1 V
Low Input Capacitance: 38/60 pF

Fast Switching Speed: 9/16 ns

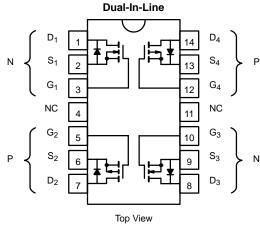
• Low Input and Output Leakage

BENEFITS

- Low Offset Voltage
- Low-Voltage Operation
- Easily Driven Without Buffer
- High-Speed Circuits
- Low Error Voltage

APPLICATIONS

- Direct Logic-Level Interface: TTL/CMOS
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.
- Battery Operated Systems
- Solid-State Relays



Plastic: VQ3001J Sidebraze: VQ3001P

Device Marking Top View	g
VQ3001J "S" fllxxyy	
	_
VQ3001P "S" fllxxyy	1

"S" = Siliconix Logo

f = Factory Code

ll = Lot Traceability

xxyy = Date Code

ABSOLUTE MAXIMUM RATINGS (T _A = 25°C UNLESS OTHERWISE NOTED)							
			Sir	igle			
Parameter		Symbol	N-Channel	P-Channel	Total Quad	Unit	
Drain-Source Voltage		V _{DS}	30	30			
Cata Carras Valtaria	VQ3001J	V _{GS}	±20	±20		V	
Gate-Source Voltage	VQ3001P		±20	±20			
Continuous Drain Current	T _A = 25°C	- I _D	0.85	-0.6		А	
(T _J = 150°C)	T _A = 100°C		0.52	-0.37			
Pulsed Drain Current ^a		I _{DM}	3	-2			
Down Discinsting	T _A = 25°C	Б	1.3	1.3	2	w	
Power Dissipation	T _A = 100°C	P _D	0.52	0.52	0.8		
Thermal Resistance, Junction-to-Ambient		R _{thJA}	96.2	96.2	62.5	°C/W	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150	-55 t	to 150	°C	

Notes

a. Pulse width limited by maximum junction temperature.



SPECIFICATIONS (T _A = 25°C UNLESS OTHERWISE NOTED)								
				Limits				
				N-Channel		P-Ch	P-Channel	
Parameter	Symbol	Test Condition	Typ ^a	Min	Max	Min	Max	Unit
Static			1		l		<u> </u>	<u> </u>
Drain-Source Breakdown Voltage	T., I	$V_{GS} = 0 \text{ V, } I_{D} = 10 \mu\text{A}$	55	30				
	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_{D} = -10 \mu\text{A}$	-55			-30		1
Gate-Source		$V_{DS} = V_{GS}$, $I_D = 1 \text{ mA}$	1.5	0.8	2.5			V
Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -1$ mA	-3.1			-2	-4.5	
Gate-Body Leakage		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100		±100	nA
	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}, T_{J} = 125^{\circ}\text{C}$			±500		±500	
		V _{DS} = 24 V, V _{GS} = 0 V			10			μΑ
Zero-Gate		$V_{DS} = -24 \text{ V}, V_{GS} = 0 \text{ V}$					-10	
Voltage Drain Current	I _{DSS}	V _{DS} = 24 V, V _{GS} = 0 V, T _J = 125°C			500			
		$V_{DS} = -24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$					-500	
		V _{DS} = 10 V, V _{GS} = 12 V	3	2				А
On-State Drain Current ^b	I _{D(on)}	$V_{DS} = -10 \text{ V}, V_{GS} = -12 \text{ V}$	-2			-1.5		
		$V_{GS} = 5 \text{ V}, I_D = 0.2 \text{ A}$	1.2		1.75			Ω
		V _{GS} = 12 V, I _D = 1 A	0.81		1.0			
Drain-Source On-State Resistance ^b	r _{DS(on)}	$V_{GS} = -12 \text{ V}, I_D = -1 \text{ A}$	1.6				2.0	
		$V_{GS} = 12 \text{ V}, I_D = 1 \text{ A}, T_J = 125 ^{\circ}\text{C}$	1.65		2.0			
		$V_{GS} = -12 \text{ V}, I_D = -1 \text{ A}, T_J = 125^{\circ}\text{C}$	2.7				4.0	
b	9fs -	$V_{DS} = 10 \text{ V}, I_D = 0.5 \text{ A}$	500	250				mS
Forward Transconductance ^b		$V_{DS} = -10 \text{ V}, I_D = -0.5 \text{ A}$	390			200		
Dynamic								
Lancet Occasiona	C _{iss}		38		110			pF
Input Capacitance		N-Channel $V_{DS} = 15 \text{ V, } V_{GS} = 0 \text{ V, } f = 1 \text{ MHz}$ $P\text{-Channel}$ $V_{DS} = -15 \text{ V, } V_{GS} = 0 \text{ V, } f = 1 \text{ MHz}$	60				150	
Output Compatitions	C _{oss}		33		110			
Output Capacitance			45				100	
D T (0 "	C _{rss}		8		35			
Reverse Transfer Capacitance			15				60	
	t _{ON}	N-Channel	9		30			ns
Turn-On Time		V_{DD} = 15 V, R_L = 23 Ω $I_D \cong 0.6$ A, V_{GEN} = 10 V, R_G = 25 Ω	19				30	
		P-Channel	14		30			
Turn-Off Time	t _{OFF}	$V_{DD} = -15 \ \ V, \ R_L = 23 \ \Omega$ $I_D \cong -0.6 \ A, \ V_{GEN} = -10 \ V, \ R_G = 25 \ \Omega$	16				30	

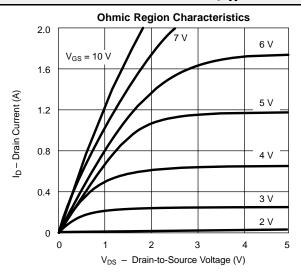
VNDQ03/VPEA03

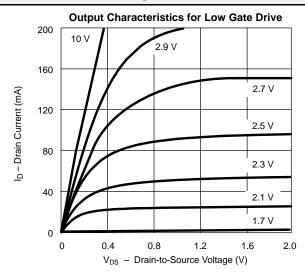
 $[\]begin{array}{ll} \text{Notes} \\ a. & \text{For DESIGN AID ONLY, not subject to production testing.} \\ b. & \text{Pulse test: } PW \leq 300~\mu s~\text{duty cycle} \leq 2\%. \end{array}$

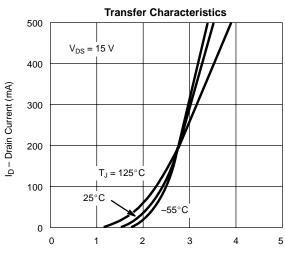


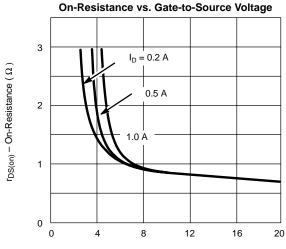
TYPICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ UNLESS OTHERWISE NOTED)

N-CHANNEL



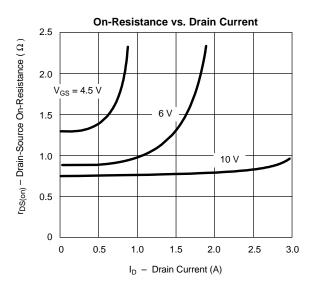


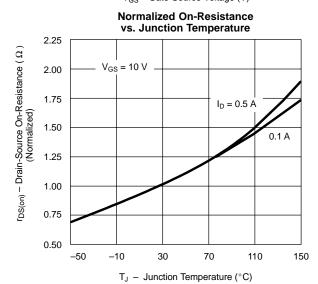






V_{GS} – Gate-Source Voltage (V)

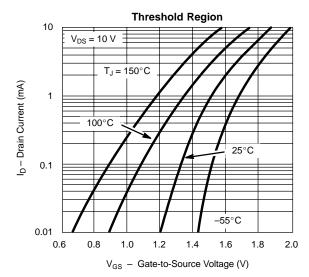


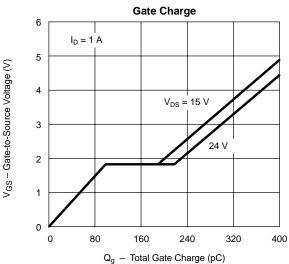


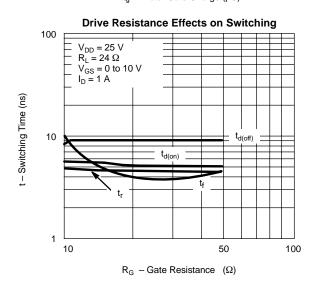


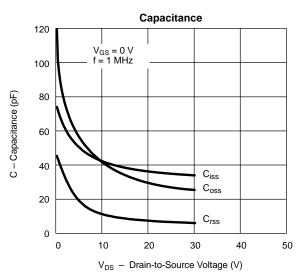
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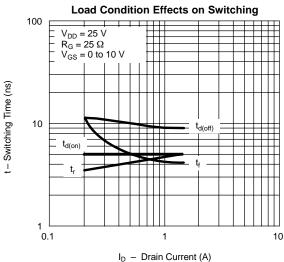
N-CHANNEL

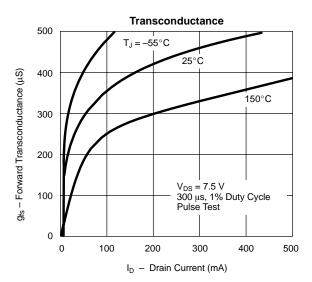








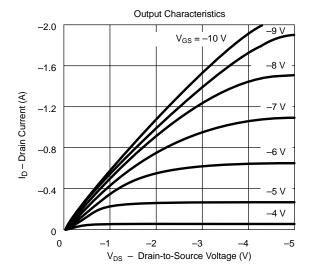


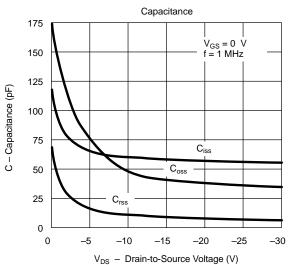


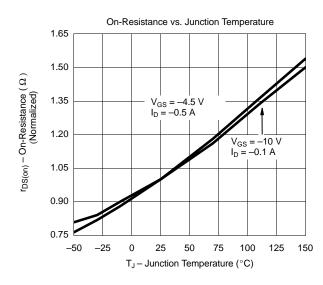


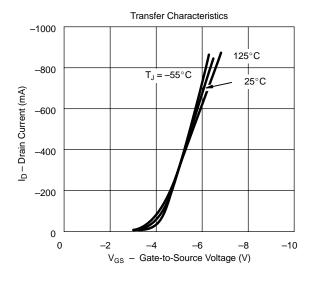
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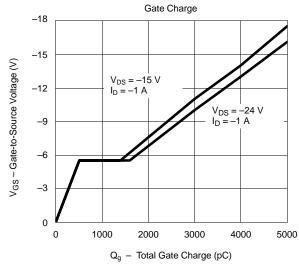
P-CHANNEL

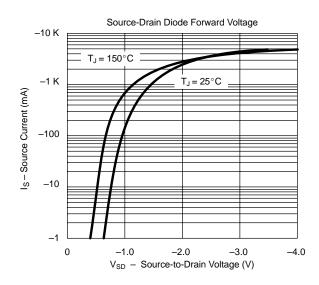








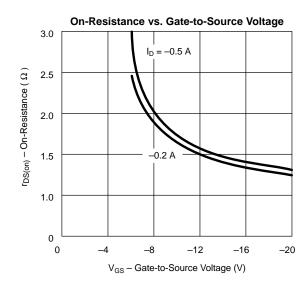


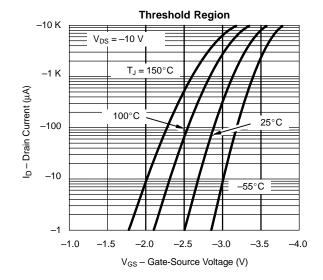




TYPICAL CHARACTERISTICS (T_A = 25°C UNLESS OTHERWISE NOTED)

P-CHANNEL









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