

### FEATURES

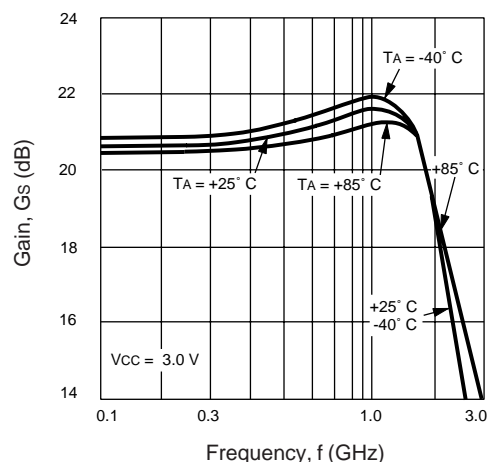
- **HIGH GAIN:** 20 dB at 900 to 1500 MHz Typical
- **HIGH OUTPUT POWER:**  $P_{SAT} = +12.5$  dBm at 900 MHz  
+11 dBm at 1500 MHz
- **LOW BIAS VOLTAGE:** 3.0 V Typical, 2.7 V Minimum
- **SUPER SMALL PACKAGE**
- **TAPE AND REEL PACKAGING OPTION AVAILABLE**

### DESCRIPTION

The UPC2771T is a Silicon Monolithic integrated circuit which is manufactured using the NESAT III process. The NESAT III process produces transistors with  $f_T$  approaching 20 GHz. This amplifier was designed as a driver amplifier for digital cellular applications. Operating on a 3 volt supply, this IC is ideally suited for hand-held, portable designs.

NEC's stringent quality assurance and test procedures ensure the highest reliability and performance.

GAIN vs. FREQUENCY AND TEMPERATURE



### ELECTRICAL CHARACTERISTICS (TA = 25°C, ZL = ZS = 50 Ω, VCC = 3.0 V)

PART NUMBER PACKAGE OUTLINE			UPC2771T T06		
SYMBOLS	PARAMETERS AND CONDITIONS	UNITS	MIN	TYP	MAX
Icc	Circuit Current (no signal)	mA		36	45
Gs	Small Signal Gain, f = 900 MHz f = 1500 MHz	dB	19	21	24
		dB	17	20	23
fu	Upper Limit Operating Frequency (The gain at fu is 3 dB down from the gain at 100 MHz)	GHz	1.7	2.1	
P1dB	1 dB Compressed Output Power, f = 900 MHz f = 1500 MHz	dBm	+9	+11.5	
		dBm	+7	+9.5	
PSAT	Saturated Output Power, f = 900 MHz f = 1500 MHz	dBm		+12.5	
		dBm		+11	
NF	Noise Figure, f = 900 MHz f = 1500 MHz	dB		6	7.5
		dB		6	7.5
RLIN	Input Return Loss, f = 900 MHz f = 1500 MHz	dB	10	14	
		dB	10	14	
RLOUT	Output Return Loss, f = 900 MHz f = 1500 MHz	dB	6.5	9.5	
		dB	5.5	8.5	
ISOL	Isolation, f = 900 MHz f = 1500 MHz	dB	25	30	
		dB	25	30	
OIP3	SSB Output Third Order Intercept Point f = 900, 902 MHz f = 1500, 1502 MHz	dBm		+16	
		dBm		+13	

ABSOLUTE MAXIMUM RATINGS<sup>1</sup> (T<sub>A</sub> = 25°C)

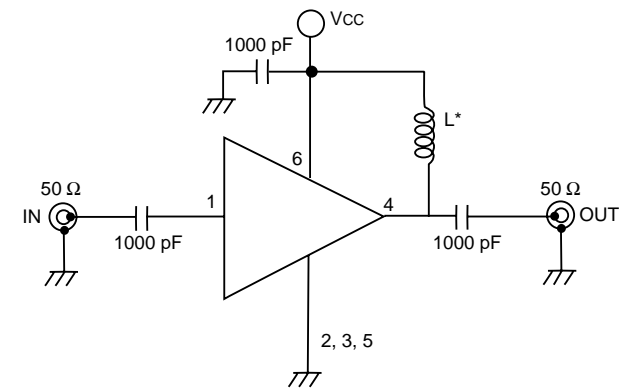
SYMBOLS	PARAMETERS	UNITS	RATINGS
V <sub>CC</sub>	Supply Voltage	V	3.6
I <sub>CC</sub>	Total Supply Current	mA	77.7
P <sub>IN</sub>	Input Power	dBm	+13
P <sub>T</sub>	Total Power Dissipation <sup>2</sup>	mW	280
T <sub>OP</sub>	Operating Temperature	°C	-40 to +85
T <sub>STG</sub>	Storage Temperature	°C	-55 to +150

- Notes:
1. Operation in excess of any one of these parameters may result in permanent damage.
  2. Mounted on a 50 X 50 X 1.6 mm epoxy glass PWB (T<sub>A</sub> = 85°C).

RECOMMENDED  
OPERATING CONDITIONS

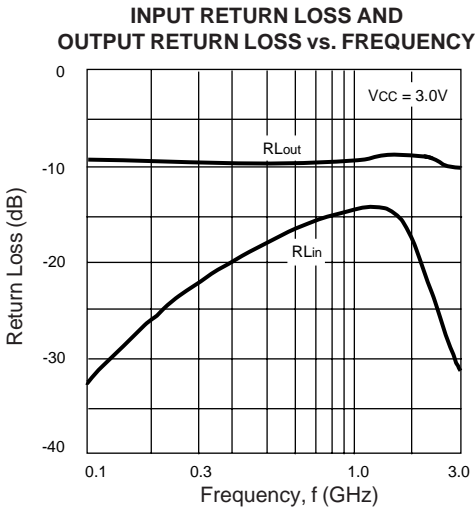
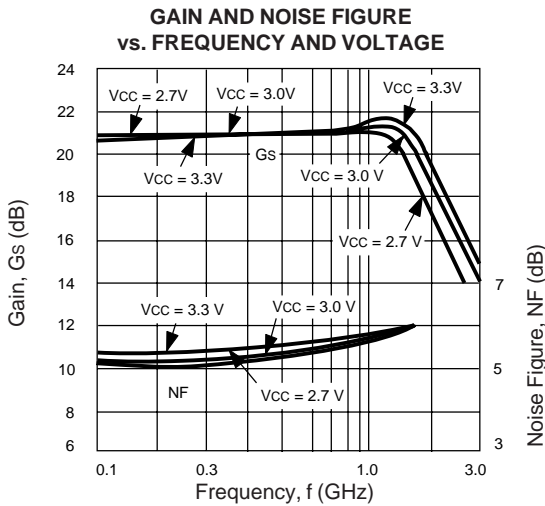
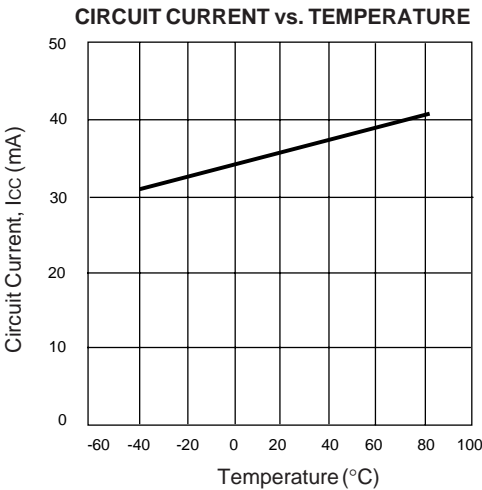
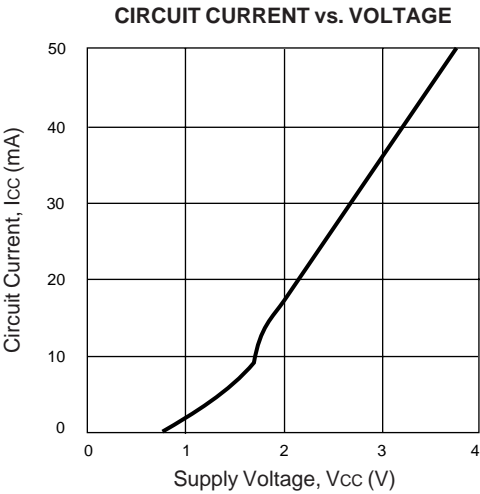
SYMBOLS	PARAMETERS	UNITS	MIN	TYP	MAX
V <sub>CC</sub>	Supply Voltage	V	2.7	3	3.3
T <sub>OP</sub>	Operating Temperature	°C	-40	+25	+85

TEST CIRCUIT

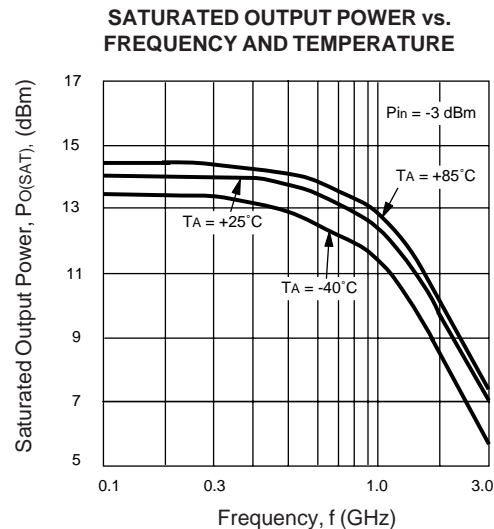
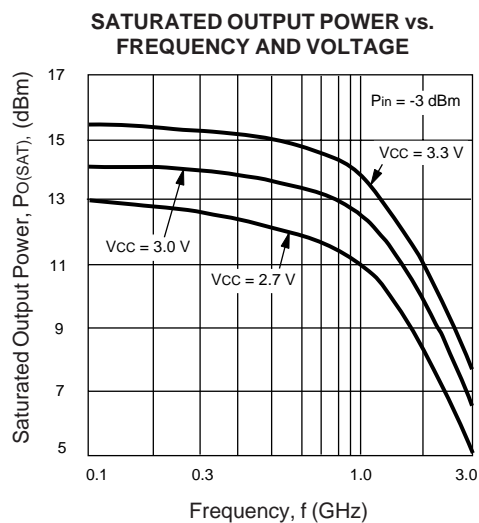
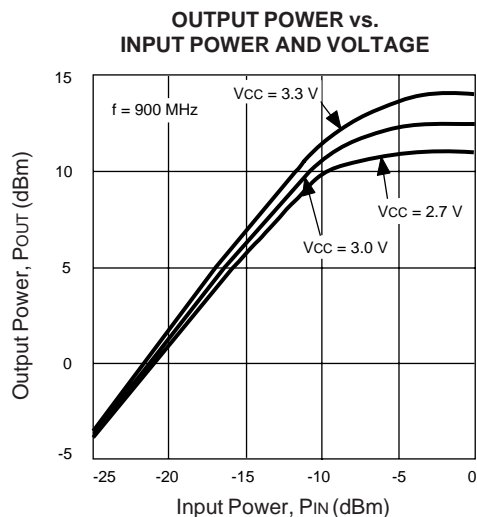
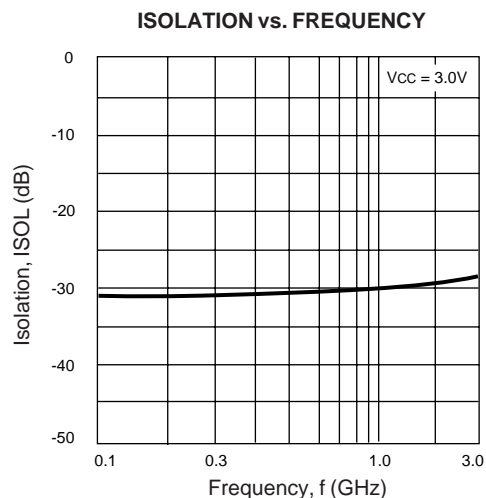


\* This device is tested using a bias tee with typical series inductance, L = 1000 nH. In circuit applications, L = 50 nH is satisfactory at 900 MHz, and L = 10 nH is satisfactory at 1500 MHz.

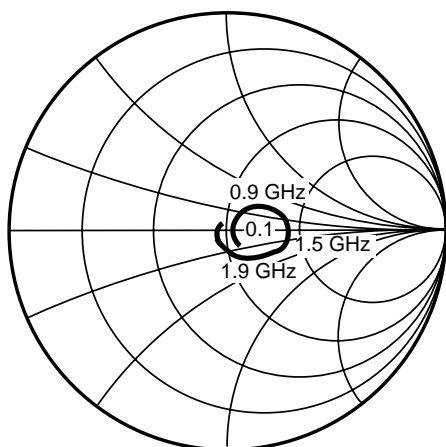
TYPICAL PERFORMANCE CURVES (T<sub>A</sub> = 25°C)



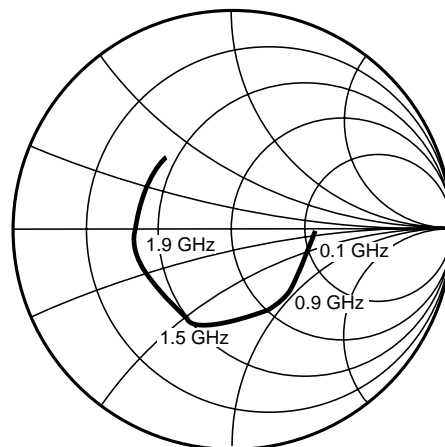
# TYPICAL PERFORMANCE CURVES ( $T_A = 25^\circ$ )



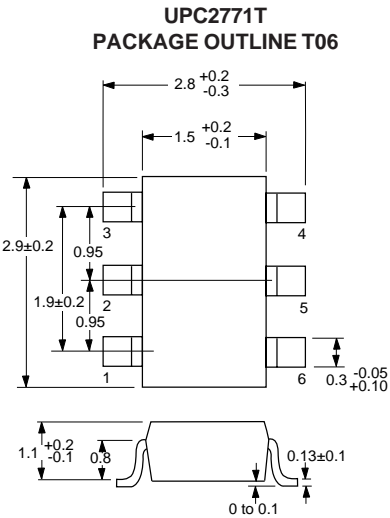
**$S_{11}$  vs. FREQUENCY**  
( $V_{CC} = 3.0\text{ V}$ )



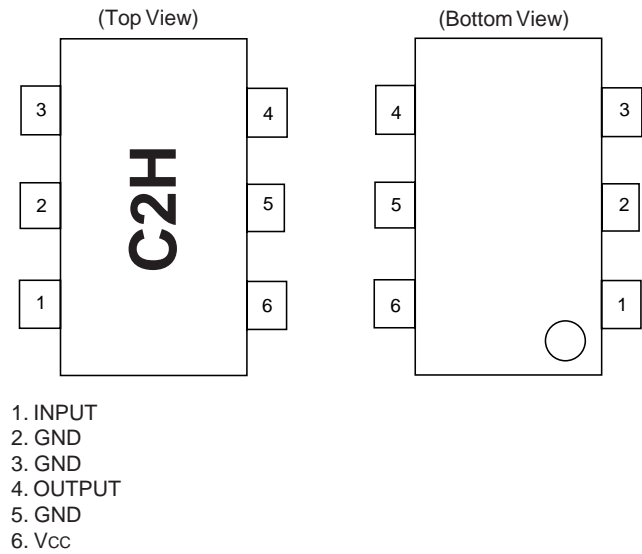
**$S_{22}$  vs. FREQUENCY**  
( $V_{CC} = 3.0\text{ V}$ )



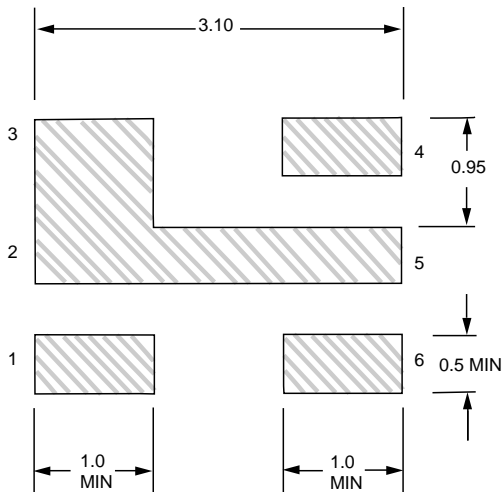
OUTLINE DIMENSIONS (Units in mm)



LEAD CONNECTIONS

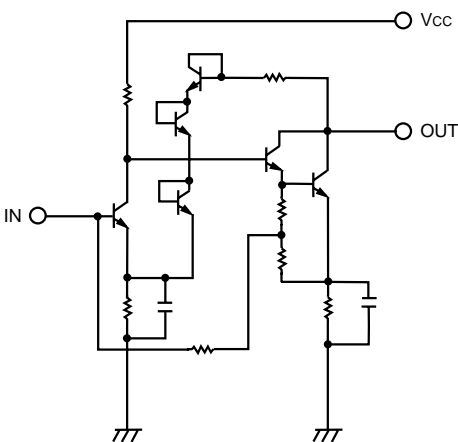


RECOMMENDED P.C.B. LAYOUT (Units in mm)



Note:  
All dimensions are typical unless otherwise specified.

EQUIVALENT CIRCUIT



ORDERING INFORMATION

PART NUMBER	QTY
UPC2771T-E3	3K/Reel

Note:  
Embossed Tape, 8 mm wide.

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