

## N-Channel 30 V (D-S) MOSFET

**PRODUCT SUMMARY**

$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)	$Q_g$ (Typ.)
30	0.093 at $V_{GS} = 10$ V	1.3 <sup>a</sup>	5.41
	0.129 at $V_{GS} = 4.5$ V	1.2	

**FEATURES**

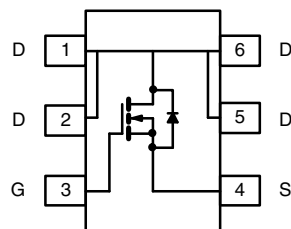
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFET
- 100 %  $R_g$  and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



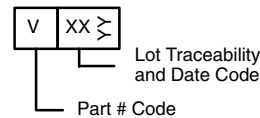
**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

**APPLICATIONS**

- Load Switch for Portable Devices

**SC-89 (6-LEADS)**


Top View

**Marking Code**

**Ordering Information:** Si1072X-T1-GE3 (Lead (Pb)-free and Halogen-free)

**ABSOLUTE MAXIMUM RATINGS** ( $T_A = 25$  °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current ( $T_J = 150$ °C) <sup>a</sup>	$I_D$	$T_A = 25$ °C	1.3 <sup>b, c</sup>
		$T_A = 70$ °C	1.03 <sup>b, c</sup>
Pulsed Drain Current	$I_{DM}$	6	A
Avalanche Current	$I_{AS}$	8	
Repetitive Avalanche Energy	$E_{AS}$	3.2	mJ
Continuous Source-Drain Diode Current	$I_S$	$T_A = 25$ °C	0.2 <sup>b, c</sup>
		$T_A = 70$ °C	0.151 <sup>b, c</sup>
Maximum Power Dissipation <sup>a</sup>	$P_D$	$T_A = 25$ °C	0.236 <sup>b, c</sup>
		$T_A = 70$ °C	0.151 <sup>b, c</sup>
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to 150	°C

**THERMAL RESISTANCE RATINGS**

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>b, d</sup>	$R_{thJA}$	$t \leq 5$ s	440	530
		Steady State	540	650

Notes:

- Based on  $T_C = 25$  °C.
- Surface mounted on 1" x 1" FR4 board.
- $t = 5$  s.
- Maximum under steady state conditions is 650 °C/W.

<b>SPECIFICATIONS</b> ( $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	30			V
$V_{DS}$ Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = 250\text{ }\mu\text{A}$		30.4		mV/ $^\circ\text{C}$
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			- 1.86		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	1		3	V
Gate-Source Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}$			1	$\mu\text{A}$
		$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}, T_J = 85\text{ }^\circ\text{C}$			10	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 10\text{ V}$	6			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 1.3\text{ A}$		0.077	0.093	$\Omega$
		$V_{GS} = 4.5\text{ V}, I_D = 1.2\text{ A}$		0.107	0.129	
Forward Transconductance	$g_{fs}$	$V_{DS} = 15\text{ V}, I_D = 1.3\text{ A}$		15		mS
<b>Dynamic<sup>b</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 15\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		280		pF
Output Capacitance	$C_{oss}$			55		
Reverse Transfer Capacitance	$C_{rss}$			35		
Total Gate Charge	$Q_g$	$V_{DS} = 15\text{ V}, V_{GS} = 10\text{ V}, I_D = 1.3\text{ A}$		5.5	8.3	nC
		$V_{DS} = 15\text{ V}, V_{GS} = 4.5\text{ V}, I_D = 1.3\text{ A}$		2.7	4.1	
Gate-Source Charge	$Q_{gs}$			1.1		
Gate-Drain Charge	$Q_{gd}$		0.8			
Gate Resistance	$R_g$	$f = 1\text{ MHz}$		3.5	4.6	$\Omega$
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 15\text{ V}, R_L = 13.6\text{ }\Omega$ $I_D \cong 1.1\text{ A}, V_{GEN} = 10\text{ V}, R_g = 1\text{ }\Omega$		7	11	ns
Rise Time	$t_r$			12	18	
Turn-Off Delay Time	$t_{d(off)}$			12	18	
Fall Time	$t_f$			6	9	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 15\text{ V}, R_L = 15.5\text{ }\Omega$ $I_D \cong 0.97\text{ A}, V_{GEN} = 10\text{ V}, R_g = 1\text{ }\Omega$		13	20	
Rise Time	$t_r$			31	47	
Turn-Off Delay Time	$t_{d(off)}$			9	14	
Fall Time	$t_f$			6	9	
<b>Drain-Source Body Diode Characteristics</b>						
Pulse Diode Forward Current <sup>a</sup>	$I_{SM}$				6	A
Body Diode Voltage	$V_{SD}$	$I_S = 0.7\text{ A}$		0.8	1.2	V
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = 1.2\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$		11.2	17	nC
Body Diode Reverse Recovery Charge	$Q_{rr}$			4.5	6.8	ns
Reverse Recovery Fall Time	$t_a$			7.5		
Reverse Recovery Rise Time	$t_b$			3.7		

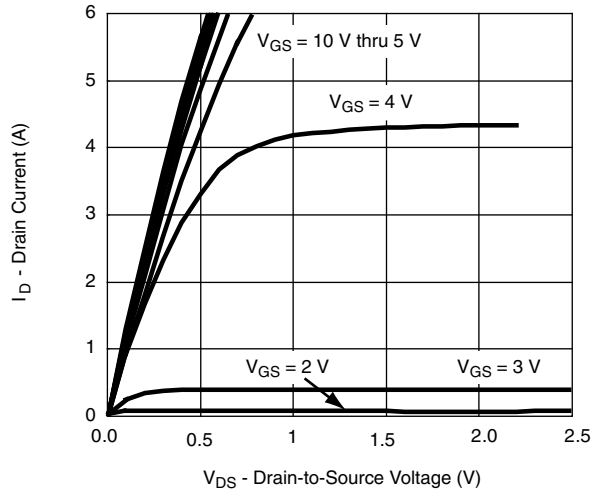
Notes:

a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .

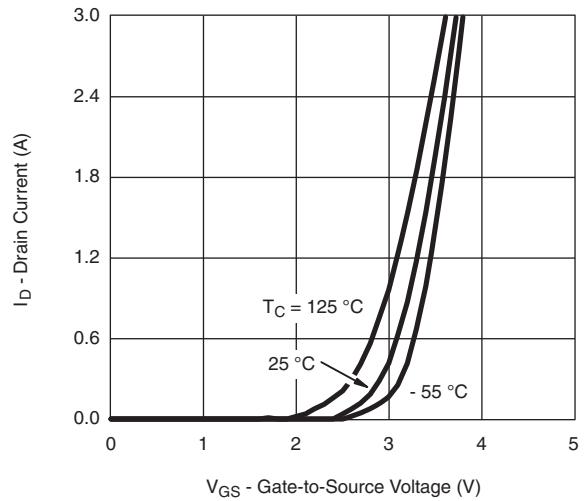
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

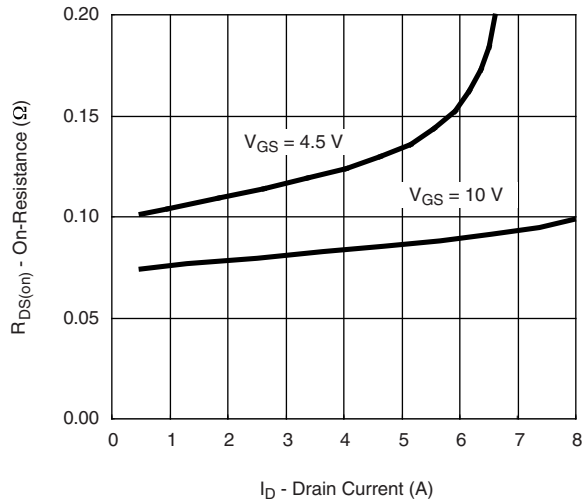
**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)



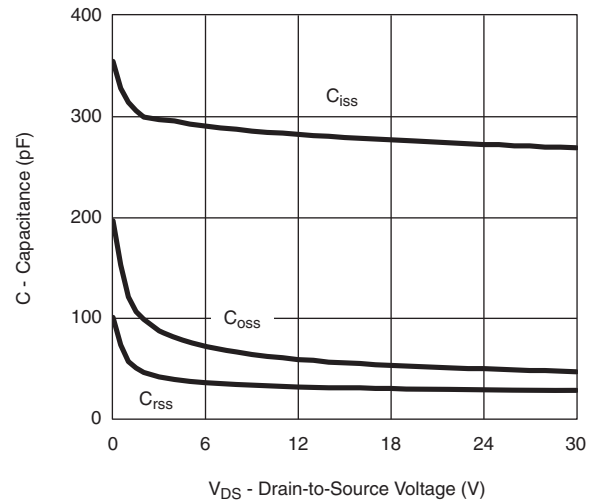
**Output Characteristics**



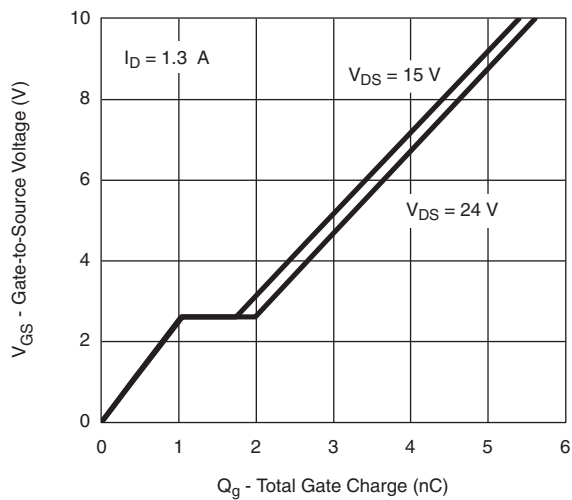
**Transfer Characteristics Curves vs. Temp.**



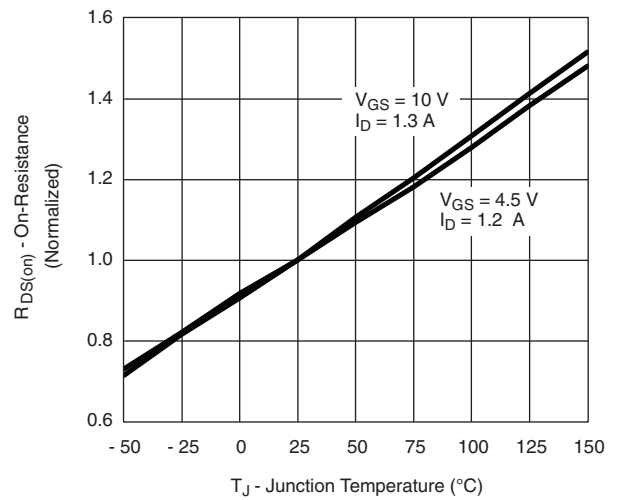
**On-Resistance vs. Drain Current**



**Capacitance**

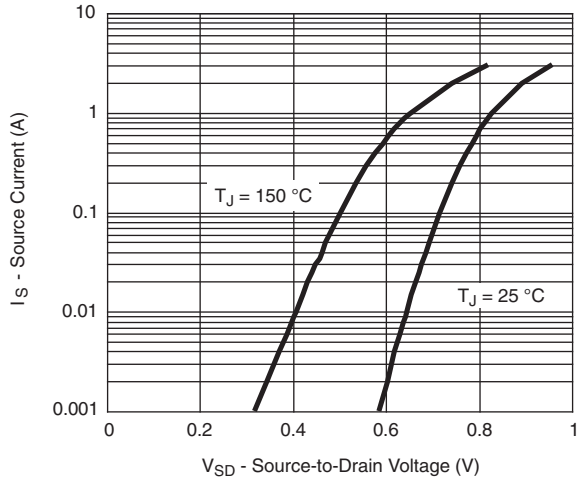


**Gate Charge**

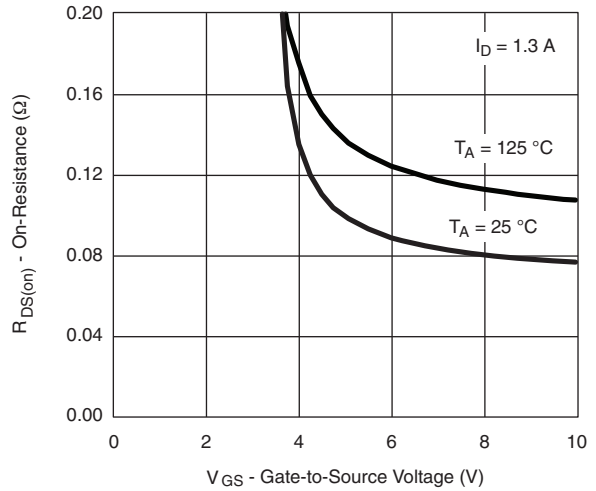


**On-Resistance vs. Junction Temperature**

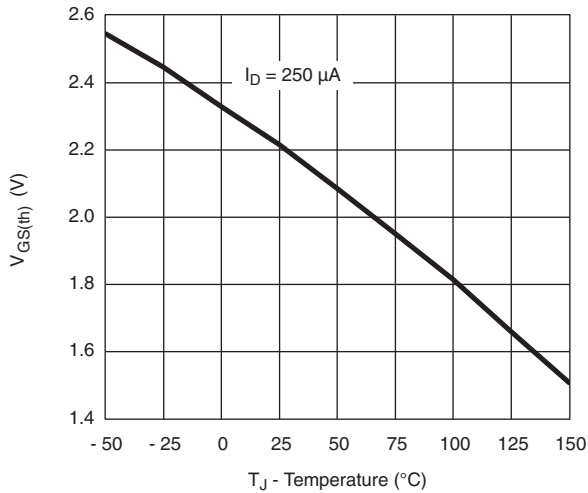
## TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise noted)



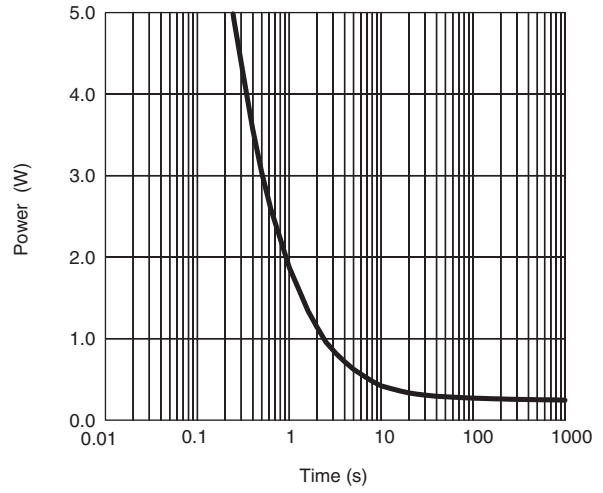
Source-Drain Diode Forward Voltage



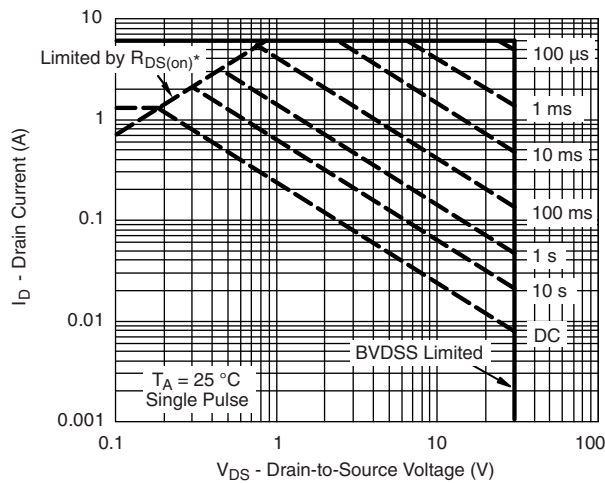
R<sub>DS(on)</sub> vs. V<sub>GS</sub> vs. Temperature



Threshold Voltage

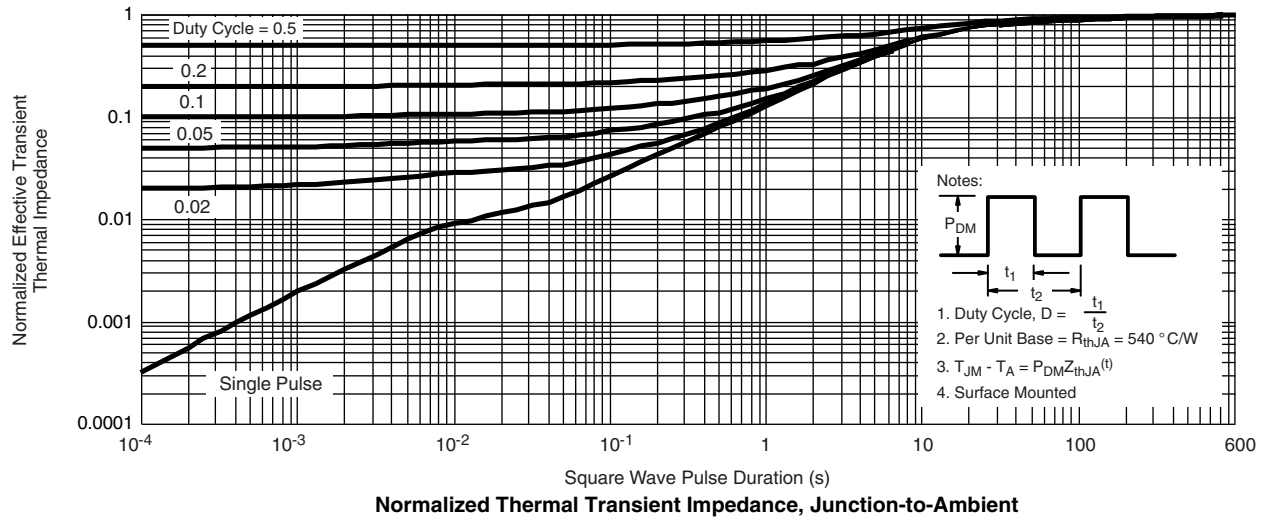


Single Pulse Power



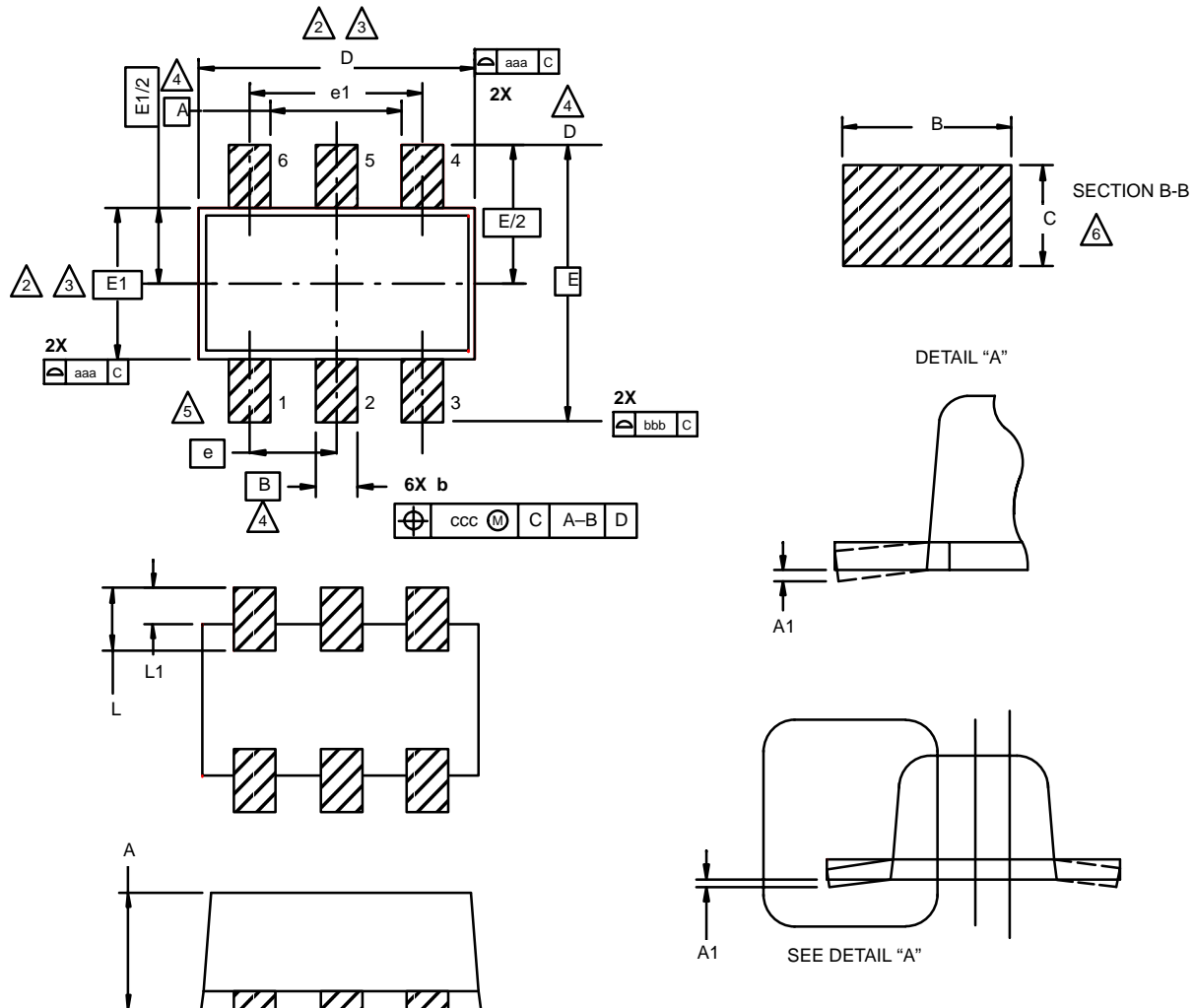
Safe Operating Area, Junction-to-Ambient

**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)



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### SC89: 6- LEADS (SOT-563F)



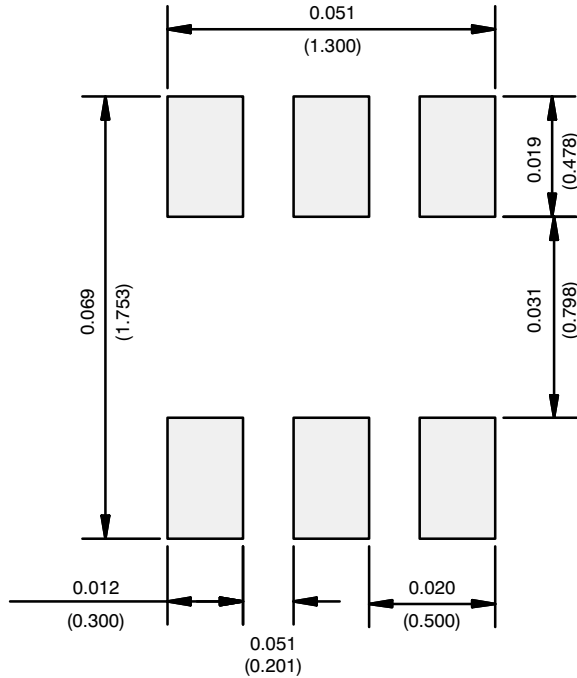
#### NOTES:

1. Dimensions in millimeters.

- 2 Dimension D does not include mold flash, protrusions or gate burrs. Mold flash, protrusions or gate burrs shall not exceed 0.15 mm per dimension E1 does not include interlead flash or protrusion, interlead flash or protrusion shall not exceed 0.15 mm per side.
- 3 Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, the bar burrs, gate burrs and interlead flash, but including any mismatch between the top and the bottom of the plastic body.
- 4 Datums A, B and D to be determined 0.10 mm from the lead tip.
- 5 Terminal numbers are shown for reference only.
- 6 These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.

Dim	MILLIMETERS		Note	Symbol	Tolerances Of Form And Position
	Min	Max			
A	0.56	0.60		aaa	0.10
A1	0.00	0.10		bbb	0.10
b	0.15	0.30		ccc	0.10
c	0.10	0.18			
D	1.50	1.70	2, 3		
E	1.55	1.70			
E1	1.20 BSC		2, 3		
e	0.50 BSC				
e1	1.00 BSC				
L	0.35 BSC				
L1	0.20 BSC				
ECN: E-00499—Rev. B, 02-Jul-01 DWG: 5880					

RECOMMENDED MINIMUM PADS FOR SC-89: 6-Lead



Recommended Minimum Pads  
Dimensions in Inches/(mm)

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