# Bi-directional TVS Array for High-Speed Data Line Protection

The SMDA05C transient voltage suppressor series is designed to protect equipment attached to up to four high speed communication lines from ESD, EFT, and lightning.

#### Features:

- SO-8 Package
- Peak Power 300 W 8 x 20 μS
- ESD Rating:

IEC 61000-4-2 (ESD) ±15 kV (air) ±8 kV (contact)

IEC 61000-4-4 (EFT) 40 A (5/50 ns)

IEC 61000-4-5 (lightning) 12 A (8/20 μs)

- UL Flammability Rating of 94 V-0
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

# **Typical Applications:**

- High Speed Communication Line Protection
- Data and I/O Lines
- Microprocessor Based Equipment
- LAN/WAN Equipment
- Servers
- Notebook and Desktop PC
- Serial and Parallel Ports
- Peripherals

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Peak Power Dissipation 8 x 20 μs @ T <sub>A</sub> = 25°C (Note 1)	P <sub>pk</sub>	300	W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C
Lead Solder Temperature – Maximum 10 Seconds Duration	TL	260	°C

<sup>1.</sup> Non-repetitive current pulse 8 x 20  $\mu S$  exponential decay waveform

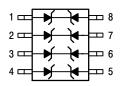


# ON Semiconductor®

http://onsemi.com

# SO-8 VOLTAGE SUPPRESSOR 300 WATTS PEAK POWER

# PIN CONFIGURATION AND SCHEMATIC





SO-8 CASE 751

#### **MARKING DIAGRAM**



A = Assembly Location

Y = Year WW = Work Week

■ = Pb-Free Package (Note: Microdot may be in either location)

# ORDERING INFORMATION

See detailed ordering, marking and shipping information in the package dimensions section on page 3 of this data sheet.

# SMDA05C ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Working Voltage	V <sub>RWM</sub>	_	-	5.0	V
Reverse Breakdown Voltage @ I <sub>t</sub> = 1.0 mA	$V_{BR}$	6.0	-	-	V
Reverse Leakage Current @ V <sub>RWM</sub> = 5 Volts	I <sub>R</sub>	N/A	-	20	μΑ
Maximum Clamping Voltage @ I <sub>PP</sub> = 1.0 A, 8 x 20 μs	V <sub>C</sub>	N/A	-	9.8	V
Maximum Clamping Voltage @ I <sub>PP</sub> = 5.0 A, 8 x 20 μs	V <sub>C</sub>	N/A	-	11	V
Maximum Peak Pulse Current, 8 x 20 μs	I <sub>PP</sub>	-	-	17	Α
Junction Capacitance @ V <sub>R</sub> = 0 V, f = 1 MHz	CJ	-	-	350	pF

## **SMDA12C ELECTRICAL CHARACTERISTICS**

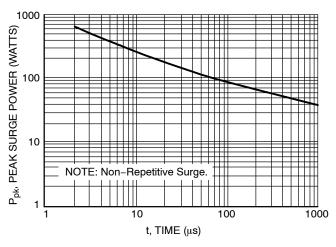
Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Working Voltage	V <sub>RWM</sub>	-	-	12	V
Reverse Breakdown Voltage @ I <sub>t</sub> = 1.0 mA	V <sub>BR</sub>	13.3	-	-	V
Reverse Leakage Current @ V <sub>RWM</sub> = 12 Volts	I <sub>R</sub>	N/A	-	1.0	μΑ
Maximum Clamping Voltage @ I <sub>PP</sub> = 1.0 A, 8 x 20 μs	V <sub>C</sub>	N/A	-	19	V
Maximum Clamping Voltage @ I <sub>PP</sub> = 5.0 A, 8 x 20 μs	V <sub>C</sub>	N/A	-	24	V
Maximum Peak Pulse Current, 8 x 20 μs	I <sub>PP</sub>	_	-	12	Α
Junction Capacitance @ V <sub>R</sub> = 0 V, f = 1 MHz	CJ	-	_	120	pF

# **SMDA15C ELECTRICAL CHARACTERISTICS**

Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Working Voltage	V <sub>RWM</sub>	-	=	15	V
Reverse Breakdown Voltage @ I <sub>t</sub> = 1.0 mA	V <sub>BR</sub>	16.7	=	-	V
Reverse Leakage Current @ V <sub>RWM</sub> = 15 Volts	I <sub>R</sub>	N/A	=	1.0	μΑ
Maximum Clamping Voltage @ I <sub>PP</sub> = 1.0 A, 8 x 20 μs	V <sub>C</sub>	N/A	=	24	V
Maximum Clamping Voltage @ I <sub>PP</sub> = 5.0 A, 8 x 20 μs	V <sub>C</sub>	N/A	-	30	V
Maximum Peak Pulse Current, 8 x 20 μs	I <sub>PP</sub>	-	-	10	Α
Junction Capacitance @ V <sub>R</sub> = 0 V, f = 1 MHz	CJ	-	-	75	pF

# **SMDA24C ELECTRICAL CHARACTERISTICS**

Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Working Voltage	$V_{RWM}$	-	-	24	V
Reverse Breakdown Voltage @ I <sub>t</sub> = 1.0 mA	$V_{BR}$	26.7	-	-	V
Reverse Leakage Current @ V <sub>RWM</sub> = 24 Volts	I <sub>R</sub>	N/A	-	1.0	μА
Maximum Clamping Voltage @ I <sub>PP</sub> = 1.0 A, 8 x 20 μs	V <sub>C</sub>	N/A	-	43	V
Maximum Clamping Voltage @ I <sub>PP</sub> = 5.0 A, 8 x 20 μs	V <sub>C</sub>	N/A	-	55	V
Maximum Peak Pulse Current, 8 x 20 μs	I <sub>PP</sub>	-	-	5.0	Α
Junction Capacitance @ V <sub>R</sub> = 0 V, f = 1 MHz	CJ	-	-	50	pF



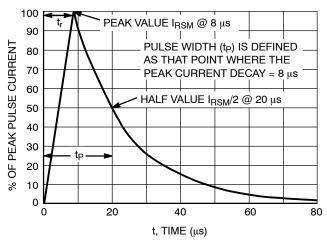


Figure 1. Pulse Width

Figure 2.  $8 \times 20 \mu s$  Pulse Waveform

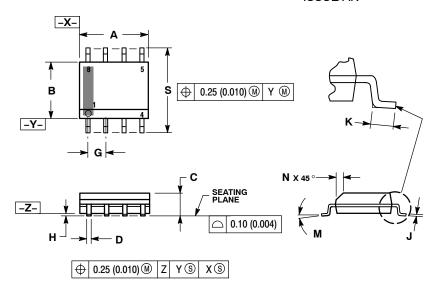
#### **ORDERING INFORMATION**

Device	Marking	Package	Shipping <sup>†</sup>
SMDA05CDR2G	AAA	SO-8 (Pb-Free)	2500 / Tape & Reel
SMDA12CDR2G	AAC	SO-8 (Pb-Free)	2500 / Tape & Reel
SMDA15CDR2G	AAD	SO-8 (Pb-Free)	2500 / Tape & Reel
SMDA24CDR2G	AAE	SO-8 (Pb-Free)	2500 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### PACKAGE DIMENSIONS

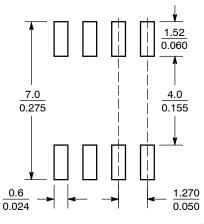
#### SOIC-8 NB CASE 751-07 **ISSUE AK**



- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: MILLIMETER.
- DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION.
- MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.
- DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.
- 751-01 THRU 751-06 ARE OBSOLETE. NEW STANDARD IS 751-07.

	MILLIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
Α	4.80	5.00	0.189	0.197
В	3.80	4.00	0.150	0.157
C	1.35	1.75	0.053	0.069
D	0.33	0.51	0.013	0.020
G	1.27	1.27 BSC		0 BSC
H	0.10	0.25	0.004	0.010
7	0.19	0.25	0.007	0.010
Κ	0.40	1.27	0.016	0.050
М	0 °	8 °	0 °	8 °
N	0.25	0.50	0.010	0.020
S	5.80	6.20	0.228	0.244

### SOLDERING FOOTPRINT\*



(mm) SCALE 6:1

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and un are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada

Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Japan Customer Focus Center Phone: 81-3-5773-3850

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative