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## DESCRIPTION

Demonstration circuit DC1198A-A features the LTM ${ }^{\oplus} 4605 E V$, the high efficiency, high density switch mode buck-boost $\mu$ Module® regulator. The LTM4605EV can regulate its output voltage from an input voltage above, below, or equal to the output. The input voltage range of DC1198A-A is from 4.5V to 20 V with a preset output voltage of 12 V . The rated load current is 5 A , although derating is necessary for certain $V_{I N}, V_{\text {OUT }}$, and thermal conditions. The switching frequency may be synchronized to an external clock from 200 kHz to 400 kHz . This PLL
can be used not only to reduce undesirable frequency harmonics but also to parallel LTM4605 to provide high output current. The LTM4605 data sheet must be read in conjunction with this demo manual prior to working on or modifying DC1198A-A.
Design files for this circuit board are available at http://www.linear.com/demo
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## PGRFORMANCE SUMMARY $\left(T_{A}=25^{\circ} \mathrm{C}\right)$

| PARAMETER | CONDITIONS | VALUE |
| :---: | :---: | :---: |
| Input Voltage Range $\mathrm{V}_{\text {IN }}$ |  | 4.5 V to 20V |
| Output Voltage $\mathrm{V}_{\text {OUT }}$ | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{I}_{\text {OUT }}=5 \mathrm{~A}$. | $12 \mathrm{~V} \pm 2 \%$ |
| Maximum Continuous Output Current | Derating Is Necessary for Certain $\mathrm{V}_{\text {IN }}, \mathrm{V}_{\text {OUT }}$, and Thermal Conditions | $\begin{aligned} & 5 A_{D C} \text { at } 5 V_{I N} \\ & 12 A_{D C} \text { at } V_{I N}>V_{0 U T} \end{aligned}$ |
| Default Operating Frequency |  | 300 kHz |
| Efficiency | $\mathrm{V}_{\text {IN }}=12 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=12 \mathrm{~V}, \mathrm{I}_{\text {OUT }}=5 \mathrm{~A}$ | 97.6\%, See Figure 3 for More Information |
| Load Transient | $\mathrm{V}_{\text {IN }}=5 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=12 \mathrm{~V}$ | See Figure 4 for Details |

## BOARD PHOTO



## DEMO MANUAL DC1198A-A

## QUICK START PROCEDURE

Demonstration circuitDC1198A-A is an easy way to evaluate the performance of the LTM4605EV. Please refer to Figure 1 for proper measurement equipment set-up and follow the procedure below:

1. Place jumpers in the following positions for a typical $12 \mathrm{~V}_{\text {OUT }}$ application:

| RUN | CLOCK | MODE | START |
| :---: | :---: | :---: | :---: |
| ON | PROG | CCM | SSO |

2. With power off, connect the input power supply, load and meters as shown in Figure 1. Preset the load to 0A and $\mathrm{V}_{\text {IN }}$ supply to be less than 20 V .
3. Turn on the power at the input. The output voltage should be $12 \mathrm{~V} \pm 2 \%$.
4. Once the proper output voltage is established, adjust the load within the operating range and observe the output voltage regulation, ripple voltage, efficiency and other parameters. Cooling fan and lower current sensing resistor are necessary for $\mathrm{V}_{\mathrm{IN}}<5 \mathrm{~V}$ and $\mathrm{I}_{\text {OUT }}=5 \mathrm{~A}$.
5. To measure input and output ripple, please refer to Figure 2 for proper setup.
6. The voltage of pin PLLFLTR determines the switching frequency. Modify R6 and R7 to adjust the switching frequency. Turn off the power supply before attempting to remove and replace R6 and R7. Do not allow voltage at pin PLLFLTR exceed 2.4V.
7. The Inductor, RS1 and RS2 should be modified to accommodate certain input and output condition. Please refer to the LTM4605 data sheet for recommended component values.


Figure 1. Test Setup of DC1198A-A

## PUICK START PROCEDURE



INPUT OR OUTPUT CAPACITOR
DC1198A F02
Figure 2. Proper Scope Probe Placement for Measuring Input or Output Ripple


Figure 3. Measured Supply Efficiency with Different $\mathrm{V}_{\mathrm{IN}} \cdot\left(\mathrm{V}_{\text {OUT }}=12 \mathrm{~V}\right)$


Figure 4. Measured Load Transient Response (2.5A Step, 25\% to 75\%)

## DEMO MANUAL DC1198A-A

PARTS LIST

| ITEM | QTY | REFERENCE | PART DESCRIPTION | MANUFACTURER/PART NUMBER |
| :---: | :---: | :---: | :---: | :---: |
| Required Circuit Components |  |  |  |  |
| 1 | 1 | CIN1 | CAP, ALUM, 150 ${ }^{\text {F, } 20 \% \text {, 35V }}$ | SANYO, 35ME150WXV+TS (NOW SUNCON 35ME150WXV) |
| 2 | 2 | CIN2, CIN3 | CAP, X7R, 10^F, 20\%, 35V, 1206 | TAIYO YUDEN, GMK316BJ106ML-T |
| 3 | 1 | CSS | CAP, X7R, $0.01 \mu \mathrm{~F}, 25 \mathrm{~V}, 10 \%, 0603$ | AVX 06033C103KAT4A |
| 4 | 2 | C03, CO4 | CAP, X7R, 22 $\mu \mathrm{F}, 16 \mathrm{~V}, 20 \%$, 1812 | TDK C4532X7R1C226M |
| 5 | 2 | C05, C06 | CAP, OS-CON, 180^F, 16V | SANYO 16SVP180MX |
| 6 | 1 | L1 | IND IRON POWER SMT FIXED, $3.3 \mu \mathrm{H}$ | TOK0 FD1254-3R3M=P3 |
| 7 | 1 | R5 | RES, CHIP, 7.15k, 1/16W, 1\%, 0603 | VISHAY CRCW06037K15FKEA |
| 8 | 1 | R6 | RES, CHIP, 4.64k, 1/16W, 1\%, 0603 | VISHAY CRCW06034K64FKEA |
| 9 | 1 | R7 | RES, CHIP, 1.21k, 1/16W, 1\%, 0603 | VISHAY CRCW06031K21FKEA |
| 10 | 2 | RS1, RS2 | RES, CHIP, 0.015 1/2W, 1\%, 1206 | IRC LRC-LRF1206-01-R015-F |
| 11 | 1 | U1 | I.C., LTM4605EV\#PBF | LINEAR TECHNOLOGY CORPORATION LTM4605EV\#PBF |

Additional Demo Board Circuit Components

| 1 | 0 | CIN4, CIN5 (OPT) | CAP, 1206 |  |
| :---: | :--- | :--- | :--- | :--- |
| 2 | 0 | C2, C4, C5, CP, CFF <br> (OPT) | CAP, 0603 |  |
| 3 | 0 | CO1, CO2 (OPT) | CAP, SVP, 100 $\mu$, D3L |  |
| 4 | 0 | CO7 (OPT) | CAP, 1206, 16V |  |
| 5 | 0 | C08 (OPT) | POSCAP, D3L | CENTRAL SEMICONDUCTOR CMDZ5230B-7-F |
| 6 | 2 | D1, D2 | ZENER DIODE, 4.7V | VISHAY CRCW0603100KJNEA |
| 7 | 1 | R1 | RES, CHIP, 100k, $1 / 16 \mathrm{~W}, 5 \%, 0603$ | VISHAY CRCW060351KOJNEA |
| 8 | 1 | R2 | RES, CHIP, 51k, $1 / 16 \mathrm{~W}, 5 \%, 0603$ | VISHAY CRCW060320KOFKEA |
| 9 | 1 | R8 | RES, CHIP, 20k, $1 / 16 \mathrm{~W}, 1 \%, 0603$ |  |
| 10 | 1 | RS3 (OPT) | RES,1206 |  |
| 11 | 1 | R9, RUVLO (OPT) | RES, 0603 |  |

Hardware/Components (For Demo Board Only)

| 1 | 2 | JP1, JP2 | 2mm SINGLE ROW HEADER, 3 PIN | SAMTEC TMM-103-02-L-S |
| :---: | :---: | :--- | :--- | :--- |
| 2 | 2 | JP3, JP4 | 2mm SINGLE ROW HEADER, 4 PIN | SAMTEC TMM-104-02-L-S |
| 3 | 4 | JP1, JP2, JP3, JP4 | SHUNT | SAMTEC 2SN-BK-G |
| 4 | 10 | TP1, TP4, TP5, TP7, <br> TP9, TP11 TO TP15 | TESTPOINT, TURRET, 0.095" | MILL-MAX 2501-2-00-80-00-00-07-0 |
| 5 | 4 | TP2, TP3, TP8, TP10 | BANANA JACK | KEYSTONE 575-4 |
| 6 | 4 | STAND OFF | STAND OFF, NYLON 0.50" TALL | KEYSTONE 8833 (SNAP 0N) |

## SCHEMATIC DIAGRAM



## DEMO MANUAL DC1198A-A

## DEMONSTRATION BOARD IMPORTANT NOTICE

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LTC currently services a variety of customers for products around the world, and therefore this transaction is not exclusive.
Please read the DEMO BOARD manual prior to handling the product. Persons handling this product must have electronics training and observe good laboratory practice standards. Common sense is encouraged.
This notice contains important safety information about temperatures and voltages. For further safety concerns, please contact a LTC application engineer.

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