V-INFINITY
a division of CUI INC

PART NUMBER: VHB75W

DESCRIPTION: half-brick dc-dc converter

description

The VHB75W series of DC-DC converters are designed for high reliability applications, featuring over-temperature protection, over-current protection. The wide 4:1 input range (9-36V or 18-75V) is very useful to stabilize an input source like batteries in various discharging and charging conditions. Additionally, high efficiency, fast response, tight regulations, remote sense and remote On/Off control make these converters very useful in many applications.

features

·37.5-75W isolated output
·Efficiency to 85%
·300 kHz switching frequency
·regulated output
·continuous short circuit
 protection
·industry standard half-brick size
·remote on/off
·over-temperature protection
·over-current protection

RoHS

MODEL

MODEL	input voltage nominal (V dc)	input voltage range (V dc)	output voltage (V dc)	output current [1] (A)	input current no load[2] (mA)	input current full load[2] (mA)	efficiency typ.[3] (%)
VHB75W-Q24-S3R3	24	9.0~36.0	3.3	15	50	2611	79
VHB75W-Q24-S5	24	9.0~36.0	5	15	50	3811	82
VHB75W-Q24-S12	24	9.0~36.0	12	6.25	50	3765	83
VHB75W-Q24-S15	24	9.0~36.0	15	5	50	3720	84
VHB75W-Q24-S24	24	9.0~36.0	24	3.12	50	3720	84
VHB75W-Q24-S48	24	9.0~36.0	48	1.56	50	3811	82
VHB75W-Q48-S3R3	48	18.0~75.0	3.3	15	50	1320	80
VHB75W-Q48-S5	48	18.0~75.0	5	15	50	1905	83
VHB75W-Q48-S12	48	18.0~75.0	12	6.25	50	1860	84
VHB75W-Q48-S15	48	18.0~75.0	15	5	50	1860	85
VHB75W-Q48-S24	48	18.0~75.0	24	3.12	50	1860	85
VHB75W-Q48-S48	48	18.0~75.0	48	1.56	50	1860	84

notes:
1. see output derating (page 4)
2. input currents are measured at nominal input voltage
3. efficiency is measured at nominal line, full load

INPUT

parameter	conditions/description	min	nom	max	units
input voltage range		9	24	36	V dc
		18	48	75	V dc
under voltage lockout	24 Vin power up		8.8		V dc
	24 Vin power down		8		V dc
	48 Vin power up		17		V dc
	48 Vin power down		16		V dc
remote on/off control[4]	section 15 in the application notes				
input filter	PI type				

notes:
4. add suffix "N" to the model number for negative logic on/off control

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OUTPUT

parameter	conditions/description	min	nom	max	units
voltage accuracy				±1	%
transient response	25% step load change			<500	μ sec
external trim adj. range				±10	%
ripple & noise (20MHz BW)	3.3V, 5V			40	mV RMS
				100	mV p-p
	12V& 15V			60	mV RMS
				150	mV p-p
	24V			100	mV RMS
				240	mV p-p
	48V			200	mV RMS
				480	mV p-p
temperature coefficient			±0.03		%/°C
short circuit protection	continuous				
line regulation[5]				±0.2	%
load regulation[6]				±0.2	%
over voltage protection trip range, % Vo nom.		115		140	%
over current protection	% nominal output current	110		160	%

GENERAL SPECIFICATIONS

parameter	conditions/description	min	nom	max	units
switching frequency			300		KHz
operating ambient temp.[7]		-40		100	°C
storage temperature		-55		105	°C
thermal shutdown case temp.			100		°C
case material	aluminum				

ISOLATION SPECIFICATIONS

parameter	conditions/description	min	nom	max	units
isolation voltage	input/output	1500			V dc
	input/case	1500			V dc
	output/case	1500			V dc
isolation resistance		100			MΩ

notes:
5. measured from high line to low line at full load
6. measured from full load to zero load at nominal input
7. see output derating curve (page 4)

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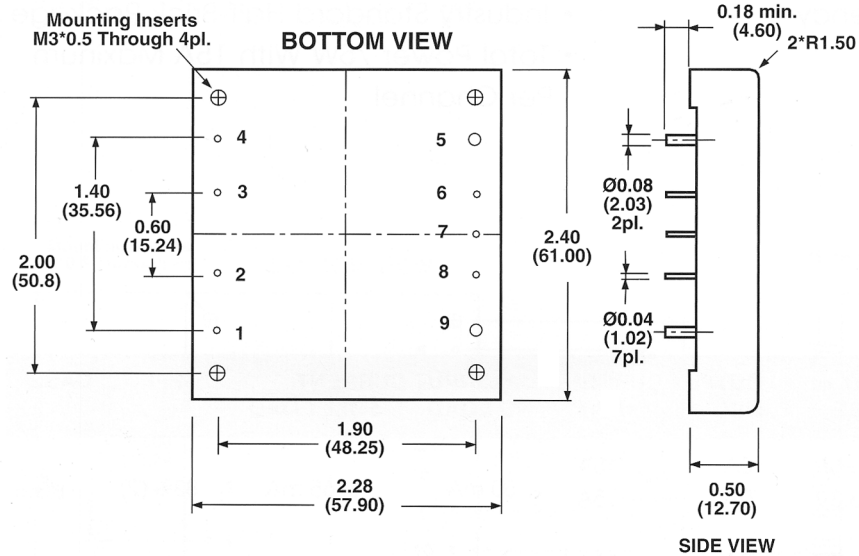
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MECHANICAL DRAWING

All Dimensions In Inches(mm)

Tolerances	Inches	.XX±.02	.XXX±.010	Pin ±0.02
	Millimeters	.X±.5	.XX±.25	±0.5

Mounting Inserts
M3*0.5 Through 4pl.

BOTTOM VIEW

1.40 (35.56)

0.60 (15.24)

2.00 (50.8)

1.90 (48.25)

2.28 (57.90)

2.40 (61.00)

0.18 min. (4.60) 2*R1.50

Ø0.08 (2.03) 2pl.

Ø0.04 (1.02) 7pl.

0.50 (12.70)

SIDE VIEW

PIN CONNECTION

Pin	Function
1.	+Vin
2.	ON/OFF
3.	CASE
4.	-Vin
5.	-Vout
6.	-Sense
7.	Trim
8.	+Sense
9.	+Vout

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APPLICATION NOTES

1. OUTPUT DE-RATING

The operating case temperature range for VHB75W is -40 ~ +100°C. When operating the VHB75W series, proper derating or cooling is needed. The maximum case temperature under any operating condition should not exceed 100°C.
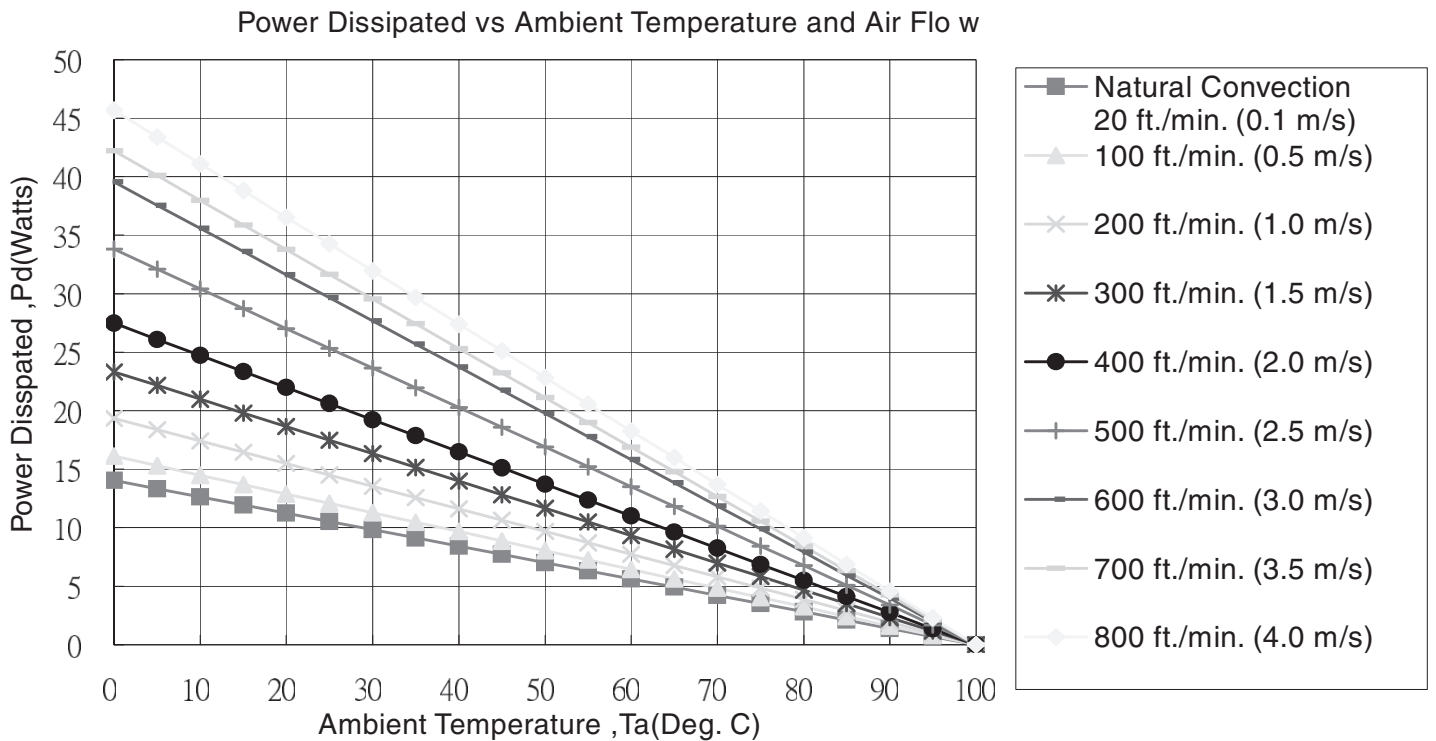

FIGURE 1. OUTPUT DERATING (FORCED CONVECTION WITH NO HEAT SINK)

Example:

What is the minimum airflow necessary for a VHB75W-Q48-S12 operating at nominal line, an output current of 6.25 A, and a maximum ambient temperature of 40°C?

Solution:

Given: Vin=48 V dc, Vo=12 V dc, Io=6.25 A

Determine Power dissipation (Pd):

$Pd=Pi-Po=Po(1-\eta)/\eta$

$Pd=12 \times 6.25 \times (1-0.84)/0.84 = 14.29$ W

Determine airflow:

Given: Pd=14.29 W and Ta=40°C

Check above Power de-rating curve:

minimum airflow= 400 ft./min.

Verifying:

The maximum temp. rise $\Delta T = Pd \times Rca = 14.29 \times 3.64 = 52.02°C$. The maximum case temperature Tc=Ta+ΔT=92.02°C<100°C

Where:

The Rca is thermal resistance from case to ambience. The Ta is ambient temperature and the Tc is case temperature.

AIR FLOW RATE	TYPICAL Rca
Natural Convection 20ft./min. (0.1m/s)	7.12 ℃/W
100 ft./min. (0.5m/s)	6.21 ℃/W
200 ft./min. (1.0m/s)	5.17 ℃/W
300 ft./min. (1.5m/s)	4.29 ℃/W
400 ft./min. (2.0m/s)	3.64 ℃/W
500 ft./min. (2.5m/s)	2.96 ℃/W
600 ft./min. (3.0m/s)	2.53 ℃/W
700 ft./min. (3.5m/s)	2.37 ℃/W
800 ft./min. (4.0m/s)	2.19 ℃/W

Chart of Thermal Resistance vs Air Flow

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2. INPUT VOLTAGE RANGE

It is important to ensure the input voltage measured at the converter input pins is within the range for that converter. Make sure wire losses and voltage ripples are accounted for. One possible problem is driving the converter with a linear unregulated power supply. For example, if the average voltage measured by a DMM is 9V, with a voltage ripple of 3Vpp, the actual input can swing from 7.5V to 10.5V. This will be outside the specified input range of 9-36V and the converter may not function properly. On the other end, make sure the actual input voltage does not exceed the highest voltage of 36V or 75V.

3. LEAD WIRES

Make sure the input and output wires are of adequate AWG size to minimize voltage drop, and ensure the voltage across the input terminals is above the converter's rated minimum voltage at all times. It is recommended to have the wire pairs twisted, respectively for the input pair and the output pair, so as to minimize noise pickup.

4. INPUT CURRENT

The input voltage source must be able to provide enough current to the converter, otherwise it may not start up or operate properly. A typical symptom is not starting or unusually low output voltage. In general, it is recommended to be able to provide at least: **Ipeak = 150%*Pout/(η*Vmin)** where Pout is the maximum output power, Vmin is the minimum input voltage and η is the converter's efficiency. As an example, for VHB75W-Q24-S5 to operate with 9~36 V input, 75 W output and an efficiency of 82%, the minimum source current is recommended to be: **Ipeak = 150% * 75 / (82% * 9) = 15.24 A**.
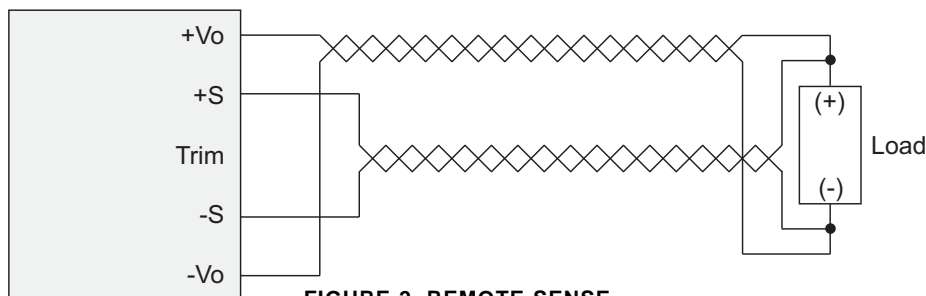
5. INPUT FUSE

To limit the input current and to facilitate input reversal protection and input OVP protection, a fast-acting input fuse is recommended for the input line. The fuse rating will depend on the input range and should allow for the maximum current at the lowest input voltage, as shown in this equation: **Ipeak = 150%*Pout/(η*Vmin)**.

In the previous example of VHB75W-Q24-S5, the peak input current at 9V was calculated to be 15.24 A. A 20 A fuse may be suitable for this application. Make sure the fuse voltage rating is higher than the maximum input voltage.

6. REMOTE SENSE

The converter provides regulated outputs at the output terminals. When there is a large current and/or the output cable is of some length, the voltage at the end of the output cable may be noticeably lower than at the terminals. The converter can compensate up to 0.5V of voltage drop through remote sense terminals. To ensure accurate regulation, run two separate wires (twisted) from the desired regulation points to the remote sense terminals, as shown below. Even if the load current is low, still connect +Vo to +S and -Vo to -S.


FIGURE 2. REMOTE SENSE

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7. OUTPUT TRIMMING (OPTIONAL)

The output voltages are preset to nominal values as indicated by the models table at the factory. If desired, the output voltage may optionally be trimmed to a different value (+/- 10%) with external resistors and/or potentiometer as shown below.
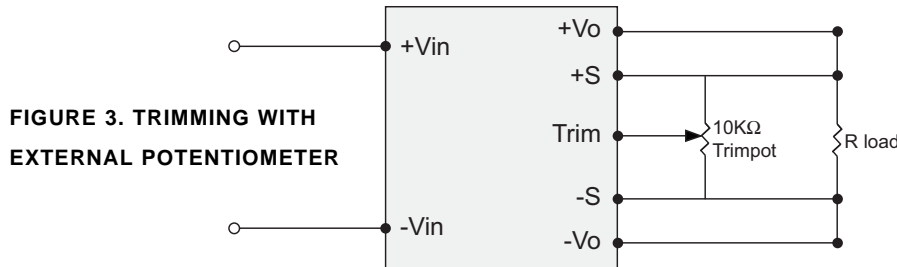
FIGURE 3. TRIMMING WITH EXTERNAL POTENTIOMETER

To trim the output voltage with fixed resistors, the output voltage can be calculated as follows.
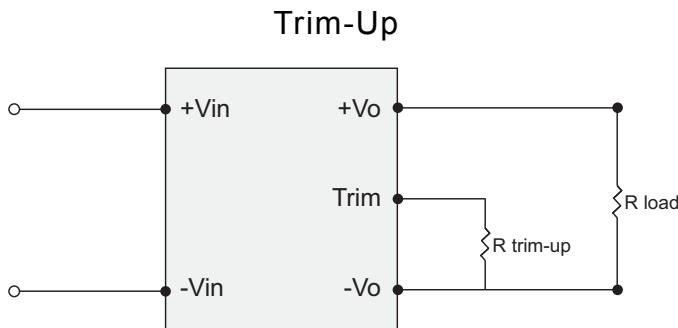
Trim-Up

FIGURE 4: TRIM-UP VOLTAGE SETUP

The value of $R_{trim-up}$ is defined as:

$$R_{trim-up} = \frac{R1 - R2 \times (Vo - Vo, nom)}{Vo - Vo, nom} (K\Omega)$$

Where: $R_{trim-up}$ is the external resistor in $K\Omega$. $V_{o,nom}$ is the nominal output voltage. V_o is the desired output voltage. R_1, R_2, R_3, V_r, and V_f are internal to the unit and are defined in Table 1. For example, to trim-up the output voltage of 5.0V module (VHB75W-Q48-S5) by 8% to 5.4V, $R_{trim-up}$ is calculated as follows:
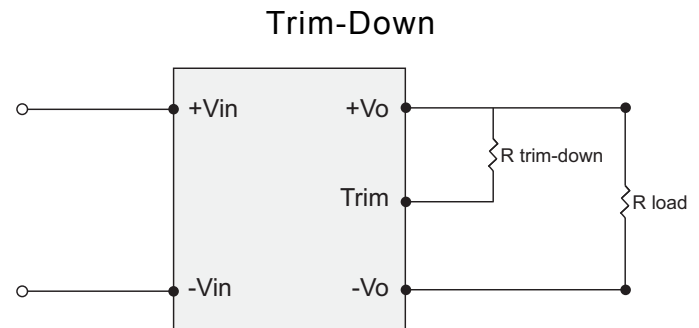
$$Vo - Vo, nom = 5.4 - 5.0 = 0.4 \text{ V}$$
$$R1 = 5.8 \text{ K}\Omega$$
$$R2 = 8.2 \text{ K}\Omega$$

$$R_{trim-up} = \frac{5.8 - 8.2 \times 0.4}{0.4} = 6.3 \text{ (K}\Omega)$$

Trim-Down

FIGURE 5: TRIM-DOWN VOLTAGE SETUP

The value of $R_{trim-down}$ is defined as:

$$R_{trim-down} = \frac{R1 - R2 \times (Vo, nom - Vo)}{Vo, nom - Vo} (K\Omega)$$

Where: $R_{trim-down}$ is the external resistor in $K\Omega$. $V_{o,nom}$ is the nominal output voltage. V_o is the desired output voltage. R_1, R_2, and V_r are internal to the unit and are defined in Table 1. For example, to trim-up the output voltage of 5.0V module (VHB75W-Q48-S5) by 8% to 4.6V, $R_{trim-down}$ is calculated as follows:

$$Vo, nom - Vo = 5.0 - 4.6 = 0.4 \text{ V}$$
$$R1 = 5.8 \text{ K}\Omega$$
$$R2 = 10.52 \text{ K}\Omega$$

$$R_{trim-down} = \frac{5.8 - 10.52 \times 0.4}{0.4} = 3.98 \text{ (K}\Omega)$$

Table 1

Output Voltage(V)	R1(KΩ)	R2(KΩ)
3.3V	3.168	7.2
5V	5.8	8.2
12V	19.656	13.304
15V	25.474	14.76
24V	42.33	16.67
48V	74.514	13.0476

Table 2

Output Voltage(V)	R1(KΩ)	R2(KΩ)
3.3V	6.18	15
5V	5.8	10.52
12V	86.45	60.1
15V	150	94
24V	430	120
48V	1569.6	295.527

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8. OUTPUT OCP AND SHORT-CIRCUIT PROTECTION

Output overload and short circuit conditional will cause the output voltage to decline or shutdown altogether.   If the case temperature is not over 100°C, the output recovers automatically when the short or OCP conditions are removed. In the case of slight overloading, the output voltage may not shut down, but the converter may build up heat over time, causing over-temperature shutdown.

9. OVER-TEMPERATURE PROTECTION

When the case temperature reaches about 100°C, the converter's built-in protection circuit will shut down the output. When the temperature is reduced enough to a safe operating level, the converter will recover to normal operations automatically.

10. OUTPUT OVP

In case the output voltage exceeds the OVP threshold, the converter shuts down.

11. OUTPUT PARALLEL CONNECTIONS

The converter is not designed for load share on the output. One may be inclined to use this circuit to force current sharing by trimming the output voltages for each converter. However, this circuit may not reliably or accurately divide the load current, as the device characteristics of the converters or the diodes may not be balanced over a range of operating conditions. We generally do not recommend this circuit to increase power output over a single converter. Instead, we recommend this setup for redundancy only, having one converter as a backup in case of a failure. Make sure the OR-ing diodes can handle the voltage and full load current.

**FIGURE 6. OUTPUT
PARALLEL CONNECTIONS**
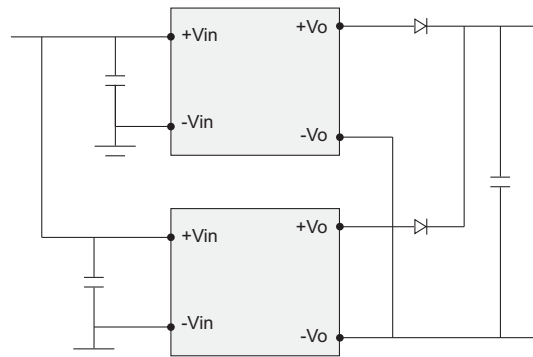
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12. OUTPUT SERIES CONNECTIONS

Two or more converters can be connected in series to obtain a higher output voltage. To prevent output reverse biasing each other in case of a short, add a Schottky diode on each output in reverse polarity, as shown in the diagram. In the event of a short, the converters will forward-bias the diodes and the output reversal will be limited to one diode drop (about 0.5V) only, so as not to damage the converters. The forward current will cause each converter to go into short-circuit protection.   For proper diode selection, make sure that: 1) the diodes voltage rating is higher than each converter output; 2) the rated diode current can carry the short-circuit current; 3) the diodes do not overheat before the short is removed.

FIGURE 7. OUTPUT SERIES CONNECTIONS
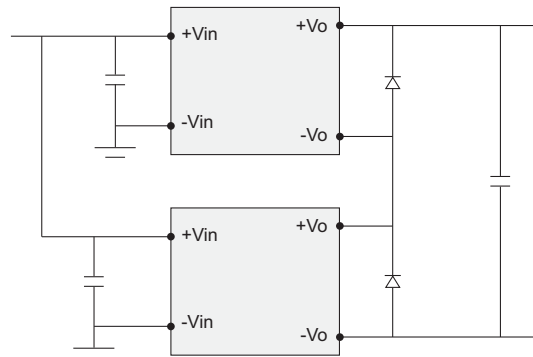
13. REMOTE OUTPUT ON/OFF CONTROL

The converter output can be enabled or disabled through the On/Off pin. The control logic is shown in this table. A common control circuit is shown below. The standard version defaults to positive logic. For negative logic, indicate the selection when ordering.
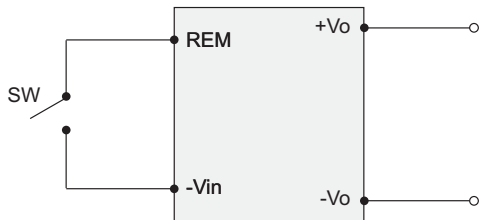
FIGURE 8. REMOTE ON/OFF CONTROL
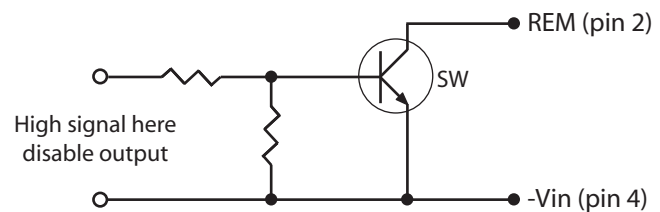
FIGURE 9. REMOTE ON/OFF CONTROL WITH TRANSISTOR SWITCH

High signal here disable output

Logic Table	Negative logic	Positive logic
SW Closed (V_{REM} <0.8 V)	Output on	Output off
SW Open (V_{REM} >2.4 V)	Output off	Output on

14. ISOLATION

The input and output of the converter are electrically isolated. If needed, an output terminal can be connected to an input terminal, resulting in the converter non-isolated.

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15. INPUT FILTERING AND EMI INTERFERENCE

The VHB75W converters have input capacitors that control input current ripple and the associated EMI interference. However, it has not been tested to a formal standard for conducted emissions. Additional filtering may be needed to ensure compliance to an EMI standard. Refer to FIGURE 10 for a reference circuit.

V-Infinity offers a line of dc input filter modules designed to help reduce common and differential mode noise. The table below provides a list of dc-dc converter models within the series and the mating dc input filter.

Series	Mating Filter Module	Notes
VHB75W-Q24-SXX	VFM-10A	
VHB75W-Q48-SXX	VFM-10A	

FIGURE 10. EMI FILTER FOR VHB75W
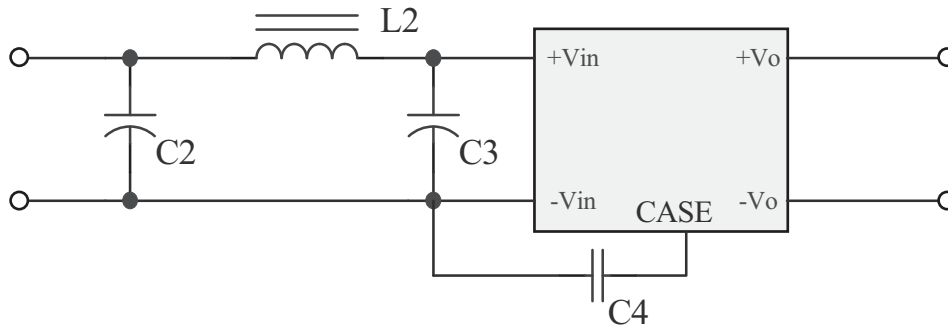
VHB75W Series	C2	C3	C4	L2
EMI to meet class A	47µF/50V ESR<0.60	47µF/50V ESR<0.60	2200pF/2KV	2.5µH
EMI to meet class B	220µF/50V ESR<0.120	220µF/50V ESR<0.120	2200pF/2KV	1.5µH

note: The recommended components are a starting point <u>but not guaranteed</u> for meeting these EMI requirements. More or less filtering may be required for a specific application or to meet other EMI standards, e.g.MIL-STD-461

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16. OUTPUT FILTERING

The converter has built-in capacitors at the output to control ripple and noise. If desired, additional output capacitance can be introduced at the input to the powered equipment. Recommended starting point: one 10μF tantalum and one 1μF ceramic capacitor in parallel at the output.

17. OUTPUT NOISE MEASUREMENT SCHEMATIC

For proper output ripple and noise measurement, connect a 10μF tantalum and a 1μF ceramic capacitor across the output. Set the scope bandwidth to 20MHz. Probe directly off of one of the capacitors, using a small ground clip to minimize measurement error.
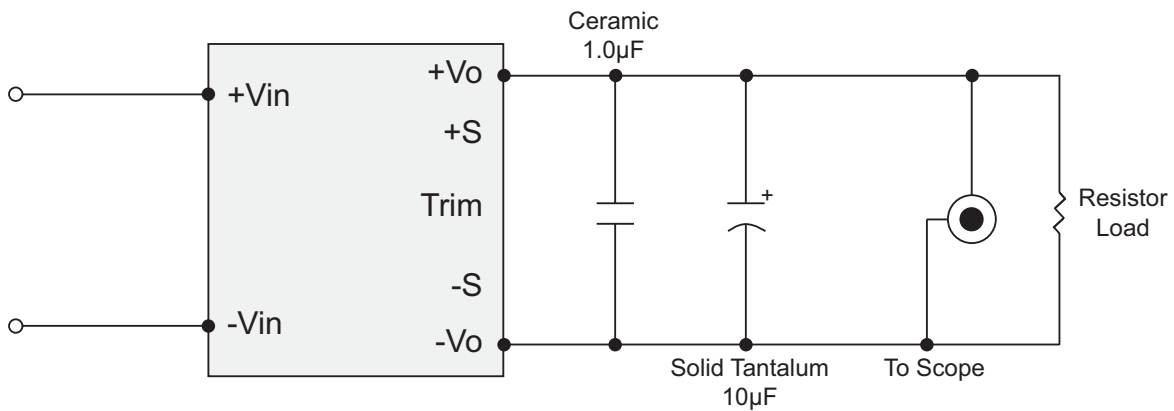
FIGURE 11. OUTPUT NOISE MEASUREMENT CIRCUIT