

Vishay Semiconductors

Standard Diodes, 600 A (SUPER MAGN-A-PAK Power Modules)

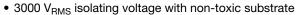


SUPER MAGN-A-PAK

PRODUCT SUMMARY				
I _{F(AV)}	600 A			
Туре	Modules - Diode, High Voltage			

FEATURES

- High current capability
- High surge capability
- High voltage ratings up to 2000 V



- Industrial standard package
- UL approved file E78996
- Compliant to RoHS directive 2002/95/EC

TYPICAL APPLICATIONS

- Rectifying bridge for large motor drives
- Rectifying bridge for large UPS

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
1		600	A		
I _{F(AV)}	T _C	100	°C		
1		942	A		
I _F (RMS)	T _C	100	°C		
I _{FSM}	50 Hz	19 000	A		
	60 Hz	20 100	A		
l ² t	50 Hz	1805	kA ² s		
1-1	60 Hz	1683	KA-S		
l²√t		18 050	kA²√s		
V _{RRM}	Range	800 to 2000	V		
T _{Stg} , T _J	Range	- 40 to 150	°C		

ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS						
TYPE NUMBER	VOLTAGE CODE	V _{RRM} , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} MAXIMUM AT T _J MAXIMUM mA		
	08	800	900			
12		1200	1300	50		
VSKD600	16	1600	1700	50		
	20	2000	2100			

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VSKD600 Series

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FORWARD CONDUCTION						
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNITS
Maximum average forward current		180° conduction, half sine wave		600	Α	
at case temperature	I _{F(AV)}	160 Conduc	ction, nan sine w	ave	100	°C
Maximum RMS forward current	I _{F(RMS)}	180° conduc	ction, half sine w	ave at T _C = 100 °C	942	Α
		t = 10 ms	No voltage		19.0	- kA
Maximum peak, one-cycle forward,	I	t = 8.3 ms	reapplied		20.1	
non-repetitive surge current	I _{FSM}	t = 10 ms	100 % V _{RRM} reapplied	Sinusoidal half wave,	16.2	
		t = 8.3 ms			17.2	
M	l ² t	t = 10 ms	No voltage	initial $T_J = T_J$ maximum	1805	- kA ² s
		t = 8.3 ms	reapplied		1683	
Maximum I ² t for fusing		t = 10 ms	100 % V _{RRM}		1319	
		t = 8.3 ms	reapplied		1230	
Maximum I ² √t for fusing	l²√t	t = 0.1 ms to 10 ms, no voltage reapplied		18 050	kA²√s	
Low level value of threshold voltage	V _{F(TO)1}	$(16.7 \% \times \pi \times I_{F(AV)} < I < \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$		0.70	V	
High level value of threshold voltage	V _{F(TO)2}	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$		0.77	V	
Low level value of forward slope resistance	r _{f1}	(16.7 % x π x $I_{F(AV)}$ < I < π x $I_{F(AV)}$), $T_J = T_J$ maximum		0.28	m0	
High level value of forward slope resistance	r _{f2}	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum} $ 0.25			0.25	mΩ
Maximum forward voltage drop	V_{FM}	$I_{pk} = 1800 \text{ A}, T_J = 25 \text{ °C}, t_p = 10 \text{ ms sine pulse}$ 1.45 V			V	

BLOCKING				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
RMS insulation voltage	V _{INS}	t = 1 s	3000	V
Maximum peak reverse and off-state leakage current	I _{RRM}	$T_J = T_J$ maximum, rated V_{RRM} applied	50	mA

THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction op temperature range	erating and storage	T _J , T _{Stg}		- 40 to 150	ç
Maximum thermal resistance, junction to case per junction		R _{thJC}	DC operation	0.065	K/W
Maximum thermal resistance, case to heatsink		R _{thC-hs}		0.02	N/ VV
Mounting torque	SMAP to heatsink		A mounting compound is recommended and the torque should be rechecked after a period of 3 hours to allow		Nm
± 10 %	busbar to SMAP		for the spread of the compound.	12 to 15	INIII
Approximate weight				1500	g
Case style			See dimensions - link at the end of datasheet	SUPER MAG	N-A-PAK



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△R _{thJC} CONDUCTION					
CONDUCTION ANGLE	SINUSOIDAL CONDUCTION	RECTANGULAR CONDUCTION	TEST CONDITIONS	UNITS	
180°	0.009	0.006			
120°	0.011	0.011			
90°	0.014	0.015	$T_J = T_J$ maximum	K/W	
60°	0.021	0.022			
30°	0.037	0.038			

Note

• The table above shows the increment of thermal resistance RthJC when devices operate at different conduction angles than DC

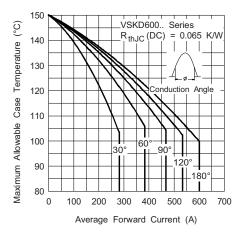


Fig. 1 - Current Ratings Characteristics

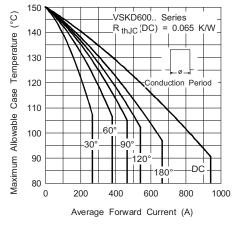


Fig. 2 - Current Ratings Characteristics

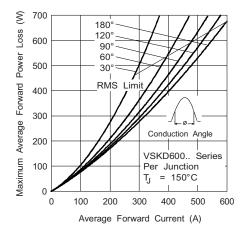


Fig. 3 - Forward Power Loss Characteristics

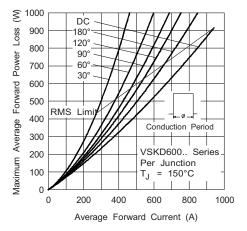


Fig. 4 - Forward Power Loss Characteristics

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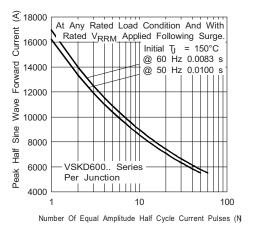


Fig. 5 - Maximum Non-Repetitive Surge Current

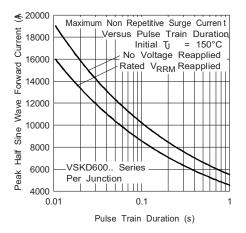


Fig. 6 - Maximum Non-Repetitive Surge Current

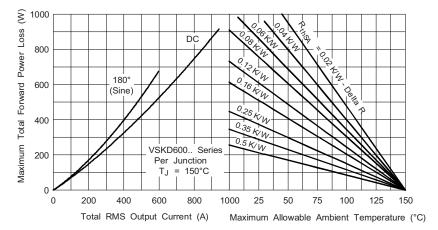


Fig. 7 - Forward Power Loss Characteristics

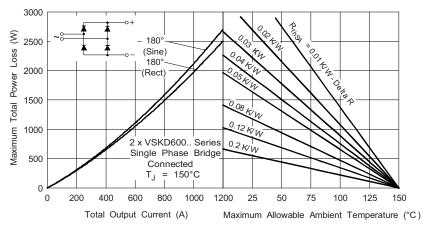


Fig. 8 - Forward Power Loss Characteristics



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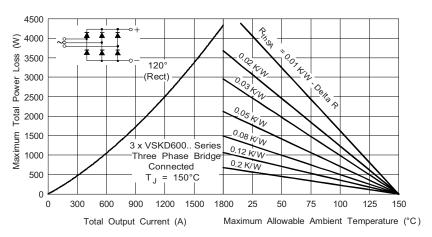


Fig. 9 - Forward Power Loss Characteristics

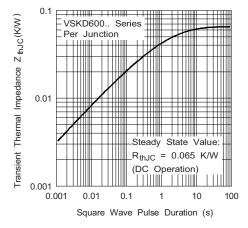


Fig. 10 - Thermal Impedance Z_{thJC} Characteristic

VSKD600 Series

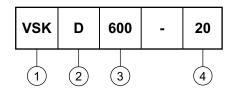
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ORDERING INFORMATION TABLE

Device code



1 - Module type

Circuit configuration D = 2 diodes in series
(see Circuit Configuration table)

- Current rating

- Voltage code x 100 = V_{RRM} (see Voltage Ratings table)

CIRCUIT CONFIGURATION				
CIRCUIT DESCRIPTION	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING		
Two diodes doubler circuit	D	20 0 1		

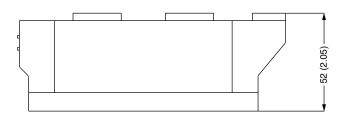
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95088		

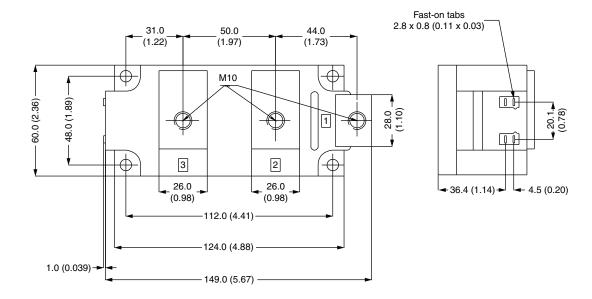


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Super MAGN-A-PAK Diode

DIMENSIONS in millimeters (inches)







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