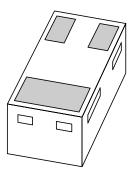
# **DISCRETE SEMICONDUCTORS**

# DATA SHEET



# **2PC4617M series**NPN general purpose transistors

Product data sheet 2003 Jul 15



# **NPN** general purpose transistors

#### 2PC4617M series

#### **FEATURES**

- Leadless ultra small plastic package (1 mm × 0.6 mm × 0.5 mm)
- Board space 1.3 × 0.9 mm
- · Power dissipation comparable to SOT23.

#### **APPLICATIONS**

- General purpose small signal DC applications
- Low and medium frequency AC applications
- Mobile communications, digital (still) cameras, PDAs, PCMCIA cards.

#### **DESCRIPTION**

NPN general purpose transistor in a SOT883 leadless ultra small plastic package.

PNP complement: 2PA1776M series.

#### **MARKING**

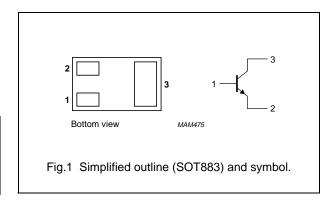
TYPE NUMBER	MARKING CODE
2PC4617QM	D7
2PC4617RM	D8
2PC4617SM	D9

#### **QUICK REFERENCE DATA**

SYMBOL	PARAMETER	MAX.	UNIT
$V_{CEO}$	collector-emitter voltage	50	V
I <sub>C</sub>	collector current (DC)	100	mA
I <sub>CM</sub>	peak collector current	200	mA

#### **PINNING**

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	



#### **LIMITING VALUES**

In accordance with the Absolute Maximum System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	50	V
$V_{CEO}$	collector-emitter voltage	open base	_	50	٧
$V_{EBO}$	emitter-base voltage	open collector	_	5	V
I <sub>C</sub>	collector current (DC)		_	100	mA
I <sub>CM</sub>	peak collector current		_	200	mA
I <sub>BM</sub>	peak base current		_	200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C			
		note 1	-	250	mW
		note 2	_	430	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

#### **Notes**

- 1. Refer to SOT883 standard mounting conditions (footprint), FR4 with 60  $\mu$ m copper strip line.
- 2. Device mounted on a FR4 printed-circuit board, single-sided copper, mounting pad for collector 1 cm<sup>2</sup>.

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# NPN general purpose transistors

# 2PC4617M series

#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-a</sub>	thermal resistance from junction to ambient	in free air		
		note 1	500	K/W
		note 2	290	K/W

#### **Notes**

- 1. Refer to SOT883 standard mounting conditions (footprint), FR4 with 60  $\mu m$  copper strip line.
- 2. Device mounted on a FR4 printed-circuit board, single-sided copper, mounting pad for collector 1 cm<sup>2</sup>.

#### **CHARACTERISTICS**

T<sub>amb</sub> = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I <sub>CBO</sub>	collector-base cut-off current	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0	_	100	nA
		$V_{CB} = 30 \text{ V}; I_E = 0; T_j = 150 ^{\circ}\text{C}$	_	5	μΑ
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 4 V; I <sub>C</sub> = 0	_	100	nA
h <sub>FE</sub>	DC current gain	$V_{CE} = 6 \text{ V}; I_{C} = 1 \text{ mA}$			
	2PC4617QM		120	270	
	2PC4617RM		180	390	
	2PC4617SM		270	560	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = 50 \text{ mA}; I_B = 5 \text{ mA}; \text{ note 1}$	_	200	mV
C <sub>c</sub>	collector capacitance	$I_E = i_e = 0$ ; $V_{CB} = 12 \text{ V}$ ; $f = 1 \text{ MHz}$	_	1.5	pF
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 12 V; I <sub>C</sub> = 2 mA; f = 100 MHz	100	_	MHz

#### Note

1. Pulse test:  $t_p \le 300~\mu s;~\delta \le 0.02.$ 

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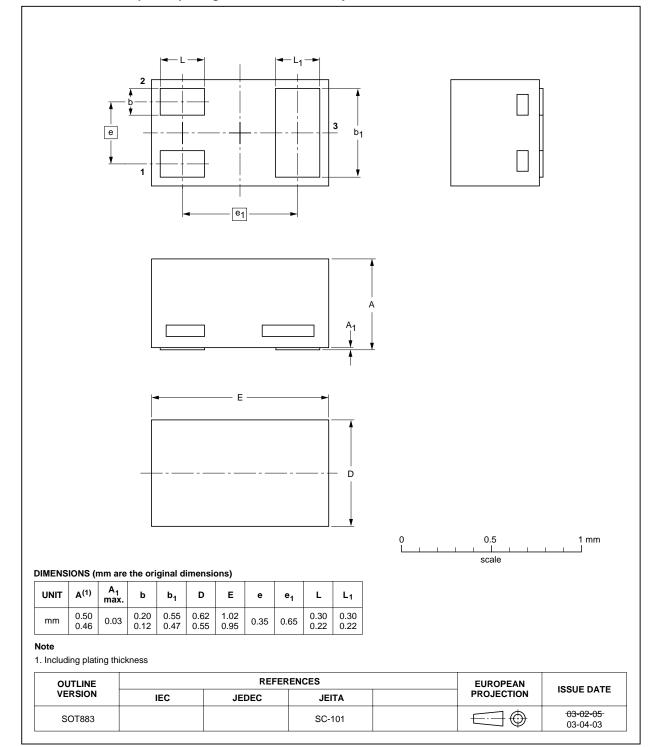
# NPN general purpose transistors

# 2PC4617M series

#### **PACKAGE OUTLINE**

Leadless ultra small plastic package; 3 solder lands; body 1.0 x 0.6 x 0.5 mm

**SOT883** 



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### NPN general purpose transistors

#### 2PC4617M series

#### **DATA SHEET STATUS**

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

#### **Notes**

- 1. Please consult the most recently issued document before initiating or completing a design.
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