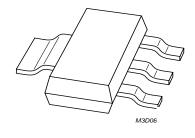
## **DISCRETE SEMICONDUCTORS**

# DATA SHEET



## BSP41; BSP43 NPN medium power transistors

Product data sheet Supersedes data of 1997 Sep 05 1999 Apr 26



## **NPN** medium power transistors

**BSP41**; **BSP43** 

#### **FEATURES**

- High current (max. 1 A)
- Low voltage (max. 80 V).

#### **APPLICATIONS**

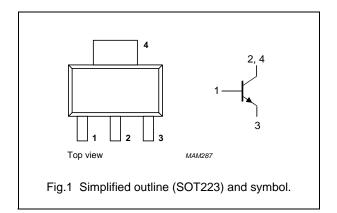
- Telephony and general industrial applications
- Thick and thin-film circuits.

#### **DESCRIPTION**

NPN medium power transistor in a SOT223 plastic package. PNP complements: BSP31; BSP32 and BSP33.

#### **PINNING**

PIN	DESCRIPTION	
1	base	
2,4	collector	
3	emitter	



#### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter			
	BSP41		_	70	V
	BSP43		_	90	V
V <sub>CEO</sub>	collector-emitter voltage	open base			
	BSP41		_	60	V
	BSP43		_	80	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	5	V
I <sub>C</sub>	collector current (DC)		-	1	Α
I <sub>CM</sub>	peak collector current		_	2	Α
I <sub>BM</sub>	peak base current		_	0.2	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C; note 1	-	1.3	W
T <sub>stg</sub>	storage temperature		-65	+150	°C
T <sub>j</sub>	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

#### Note

1. Device mounted on a printed-circuit board, single sided copper, tinplated, mounting pad for collector 1 cm<sup>2</sup>. For other mounting conditions, see "Thermal considerations for SOT223 in the General Part of associated Handbook".

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## NPN medium power transistors

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#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-a</sub>	thermal resistance from junction to ambient	note 1	93	K/W
R <sub>th j-s</sub> thermal resistance from junction to soldering point			12	K/W

#### Note

1. Device mounted on a printed-circuit board, single sided copper, tinplated, mounting pad for collector 1 cm<sup>2</sup>. For other mounting conditions, see "Thermal considerations for SOT223 in the General Part of associated Handbook".

#### **CHARACTERISTICS**

 $T_{amb}$  = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current	I <sub>E</sub> = 0; V <sub>CB</sub> = 60 V	_	100	nA
		I <sub>E</sub> = 0; V <sub>CB</sub> = 60 V; T <sub>j</sub> = 150 °C	-	50	μΑ
I <sub>EBO</sub>	emitter cut-off current	$I_C = 0; V_{EB} = 5 V$	_	100	nA
h <sub>FE</sub>	DC current gain	$I_C = 100 \mu A; V_{CE} = 5 V; note 1$	30	_	
		$I_C = 100 \text{ mA}; V_{CE} = 5 \text{ V}; \text{ note 1}$	100	300	
		$I_C = 500 \text{ mA}$ ; $V_{CE} = 5 \text{ V}$ ; note 1	50	_	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = 150 \text{ mA}$ ; $I_B = 15 \text{ mA}$ ; note 1	_	0.25	٧
		I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA; note 1	_	0.5	V
V <sub>BEsat</sub>	base-emitter saturation voltage	IC = 150 mA; IB = 15 mA; note 1	_	1	V
		$I_C = 500 \text{ mA}$ ; $I_B = 50 \text{ mA}$ ; note 1	_	1.2	V
f <sub>T</sub>	transition frequency	$I_C = 50 \text{ mA}; V_{CE} = 10 \text{ V}; f = 100 \text{ MHz}$	100	_	MHz

#### Note

1. Pulse test:  $t_p \leq 300~\mu s;~\delta \leq 0.01.$ 

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## NPN medium power transistors

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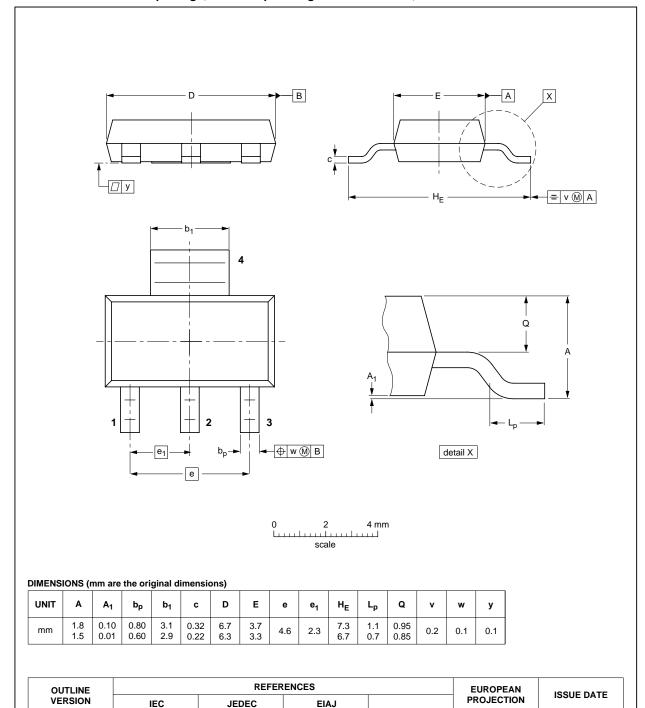
#### **PACKAGE OUTLINE**

Plastic surface mounted package; collector pad for good heat transfer; 4 leads

**SOT223** 

97-02-28

99-09-13



SC-73

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SOT223

## NPN medium power transistors

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#### **DATA SHEET STATUS**

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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#### **Customer notification**

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#### **Contact information**

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