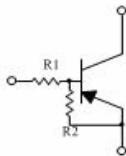


**Small Signal Diode**

**Features**

- ◆ Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistor (see equivalent circuit).
- ◆ The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- ◆ Only the on/off conditions need to be set for operation, making device design easy.
- ◆ Green compound (Halogen free) with suffix "G" on packing code and prefix "G" on date code.

**Ordering Information**

| Package | Part No.   | Packing      | Marking |
|---------|------------|--------------|---------|
| SOT-723 | DTA114 EM  | 8K / 7" Reel | 14      |
| SOT-523 | DTA114 EE  | 3K / 7" Reel | 14      |
| SOT-323 | DTA114 EUA | 3K / 7" Reel | 14      |
| SOT-23  | DTA114 ECA | 3K / 7" Reel | 14      |
| TO-92S  | DTA114 ESA | 3K / 7" Reel |         |

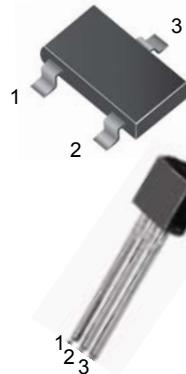
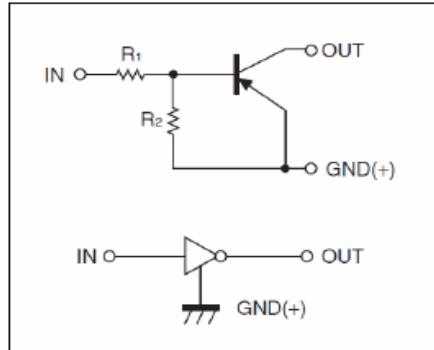
**Maximum Ratings and Electrical Characteristics**

Rating at 25°C ambient temperature unless otherwise specified.

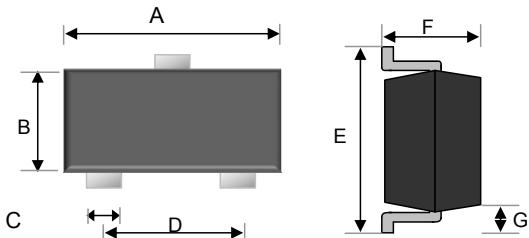
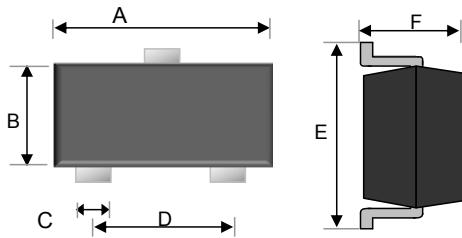
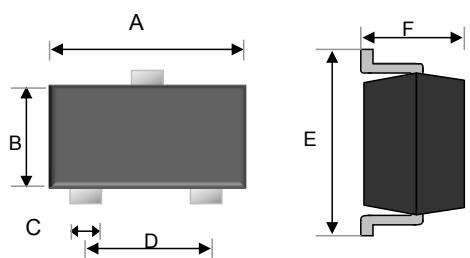
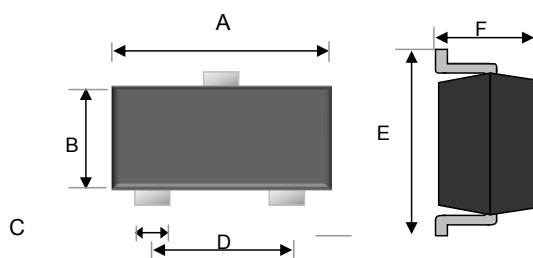
| Type Number                            | Symbol                            | Value |     |              |     |     | Units |
|--|-----------------------------------|-------|-----|--------------|-----|-----|-------|
|  |                                   | EM    | EE  | EUA          | ECA | ESA |       |
| Power Dissipation                      | PD                                | 100   | 150 | 200          | 200 | 300 | mW    |
| Supply Voltage                         | V <sub>CC</sub>                   |       |     | -50          |     |     | V     |
| Input Voltage                          | V <sub>IN</sub>                   |       |     | -40~10       |     |     | V     |
| Output Current                         | I <sub>O</sub>                    |       |     | -50          |     |     | mA    |
| Peak Collector Current                 | I <sub>C(MAX)</sub>               |       |     | -100         |     |     |       |
| Junction and Storage Temperature Range | T <sub>J</sub> , T <sub>STG</sub> |       |     | -55 to + 150 |     |     | °C    |

Notes: 1. Valid provided that electrodes are kept at ambient temperature

| Parameter            | Symbol                         | Min  | Typ | Max   | Condition  | Unit |
|----------------------|--------------------------------|------|-----|-------|--|------|
| Input Voltage        | V <sub>I(on)</sub>             |      |     | -3    | V <sub>CC</sub> =-5V, I <sub>O</sub> =-100μA         | V    |
|                      | V <sub>I(off)</sub>            | -0.5 |     |       | V <sub>O</sub> =-0.3V, I <sub>O</sub> =-10mA         |      |
| Output Voltage       | V <sub>O(on)</sub>             |      |     | -0.3  | I <sub>O</sub> /I <sub>i</sub> =-10mA/-5mA           | V    |
| Input Current        | I <sub>i</sub>                 |      |     | -0.88 | V <sub>i</sub> =-5V                                  | mA   |
| Output Current       | I <sub>O(off)</sub>            |      |     | -0.5  | V <sub>CC</sub> =-50V, V <sub>i</sub> =0             | μA   |
| DC Current Gain      | G <sub>i</sub>                 | 30   |     |       | V <sub>O</sub> =-5V, I <sub>O</sub> =-5mA            |      |
| Input Resistance     | R <sub>i</sub>                 | 7    | 10  | 13    |  | KΩ   |
| Resistance Ratio     | R <sub>2</sub> /R <sub>1</sub> | 0.8  | 1   | 1.2   |  |      |
| Transition Frequency | f <sub>T</sub>                 |      |     | 250   | V <sub>O</sub> =-10V, I <sub>O</sub> =-5mA, f=100MHz | MHz  |

**DTA114 EM/EE/EUA/ECA/ESA  
PNP Digital Transistor**
**• Equivalent Circuit**

**SOT-723/SOT-523  
SOT-323/SOT-23**
**1.IN  
2.GND  
3.OUT**

**TO-92S**
**1.GND  
2.OUT  
3.IN**

**DTA114 EM/EE/EUA/ECA/ESA  
PNP Digital Transistor**
**SOT-23**

**SOT-323**

**SOT-523**

**SOT-723**

**TO-92S**
