BTA208-800B



3Q Hi-Com Triac Rev. 02 — 12 April 2011

Product data sheet

Product profile

1.1 General description

Planar passivated high commutation triac in a SOT78 plastic package intended for use in circuits where high static and dynamic dV/dt and high dI/dt can occur. This "series B" triac will commutate the full rated RMS current at the maximum rated junction temperature without the aid of a snubber.

1.2 Features and benefits

- 3Q technology for improved noise immunity
- High commutation capability with maximum false trigger immunity
- High immunity to false turn-on by dV/dt
- High voltage capability
- Planar passivated for voltage ruggedness and reliability
- Triggering in three quadrants only

1.3 Applications

- Electronic thermostats
- General purpose motor controls
- Rectifier-fed DC inductive loads e.g. DC motors and solenoids

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{DRM}	repetitive peak off-state voltage		-	-	800	V
I _{TSM}	non-repetitive peak on-state current	full sine wave; $T_{j(init)} = 25 \text{ °C}$; $t_p = 20 \text{ ms}$; see Figure 4; see Figure 5	-	-	65	Α
I _{T(RMS)}	RMS on-state current	full sine wave; $T_{mb} \le 102 ^{\circ}\text{C}$; see <u>Figure 1</u> ; see <u>Figure 2</u> ; see <u>Figure 3</u>	-	-	8	Α
Static characteristics						
Іст	gate trigger current	$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; } T2+G+;$ $T_j = 25 \text{ °C; see } \frac{\text{Figure 6}}{\text{ C}}$	2	18	50	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T2+ G-;$ $T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure 6}}{\text{C}}$	2	21	50	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T2- \text{G-};$ $T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure 6}}{\text{C}}$	2	34	50	mA



2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1		N I
2	T2	main terminal 2	mb	T2—T1
3	G	gate		`G sym051
mb	T2	mounting base; main terminal 2	1 2 3	
			SOT78 (TO-220AB)	

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BTA208-800B	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78
BTA208-800B/DG	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78
BTA208-800B/L01	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DRM}	repetitive peak off-state voltage		-	800	V
I _{T(RMS)}	RMS on-state current	full sine wave; $T_{mb} \le 102 \text{ °C}$; see <u>Figure 1</u> ; see <u>Figure 2</u> ; see <u>Figure 3</u>	-	8	Α
I _{TSM}	non-repetitive peak on-state current	full sine wave; $T_{j(init)} = 25 \text{ °C}$; $t_p = 20 \text{ ms}$; see Figure 4; see Figure 5	-	65	Α
		full sine wave; $T_{j(init)} = 25 \text{ °C}$; $t_p = 16.7 \text{ ms}$	-	71	Α
I ² t	I ² t for fusing	t _p = 10 ms; sine-wave pulse	-	21	A ² s
dI _T /dt	rate of rise of on-state current	$I_T = 12 \text{ A}$; $I_G = 0.2 \text{ A}$; $dI_G/dt = 0.2 \text{ A/}\mu\text{s}$	-	100	A/µs
I _{GM}	peak gate current		-	2	Α
V_{GM}	peak gate voltage		-	5	V
P_{GM}	peak gate power		-	5	W
$P_{G(AV)}$	average gate power	over any 20 ms period	-	0.5	W
T _{stg}	storage temperature		-40	150	°C
T _j	junction temperature		-	125	°C

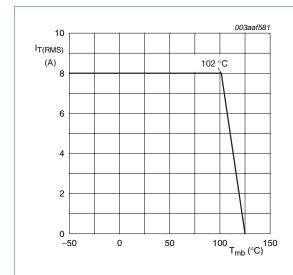
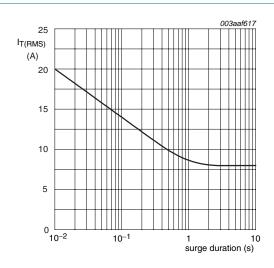


Fig 1. RMS on-state current as a function of heatsink temperature; maximum values



 $f = 50 \text{ Hz}; T_{mb} = 102 \,{}^{\circ}C$

Fig 2. RMS on-state current as a function of surge duration; maximum value

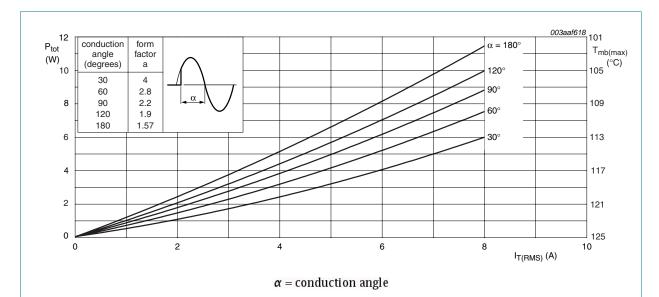


Fig 3. Total power dissipation as a function of RMS on-state current; maximum values

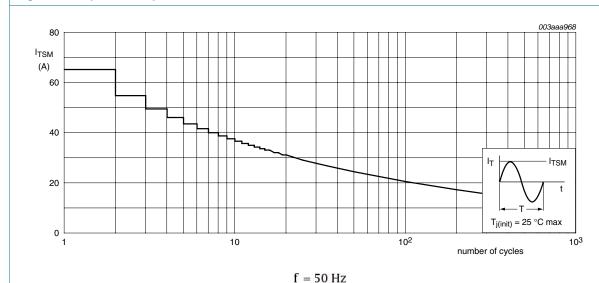
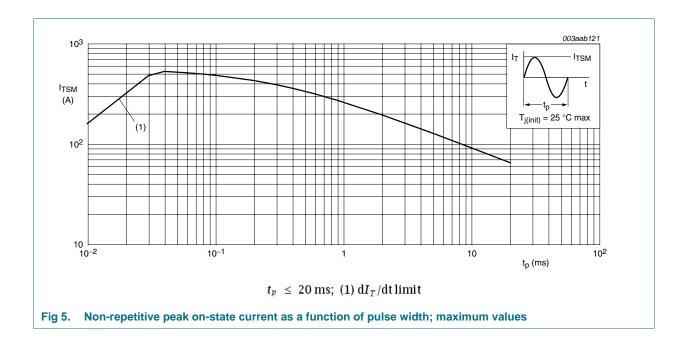


Fig 4. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values



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5. Thermal characteristics

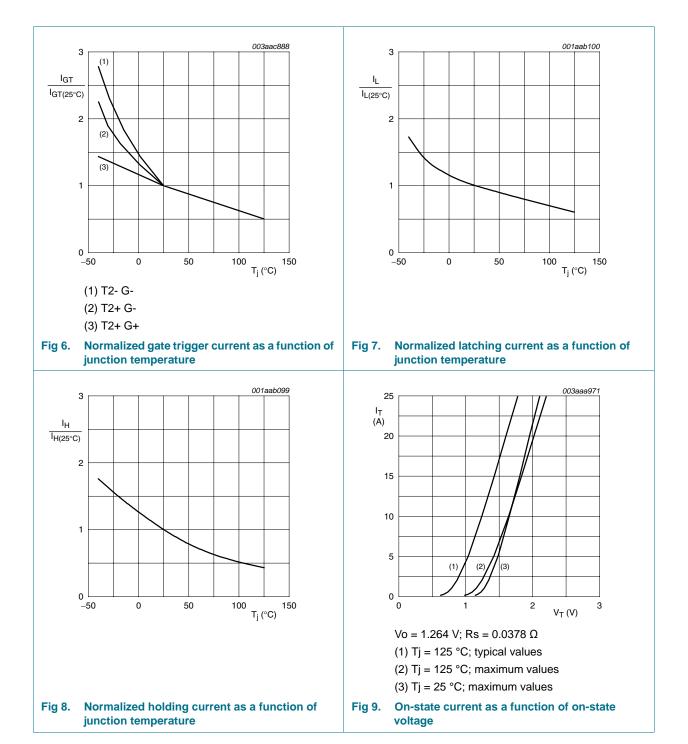
Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	full cycle	-	-	2	K/W
		half cycle	-	-	2.4	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	-	60	-	K/W

6. Characteristics

Table 6. Characteristics

		A		_		
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	cteristics					
I _{GT}	gate trigger current	$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; } T2+G+; T_j = 25 \text{ °C;}$ see Figure 6	2	18	50	mA
		$V_D = 12 \text{ V; } I_T = 0.1 \text{ A; } T2 + G-; T_j = 25 \text{ °C;}$ see Figure 6	2	21	50	mA
		$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T2- G-; T_j = 25 °C;$ see Figure 6	2	34	50	mA
I _L	latching current	$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; T2+ G+;$ $T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure 7}}{}$	-	31	60	mA
		$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; T2+ G-; T_j = 25 ^{\circ}\text{C};$ see Figure 7	-	34	90	mA
		$V_D = 12 \text{ V}; I_G = 0.1 \text{ A}; T2- G-; T_j = 25 ^{\circ}C;$ see Figure 7	-	30	60	mA
I _H	holding current	$V_D = 12 \text{ V}; T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure 8}}{}$	-	31	60	mA
V_{T}	on-state voltage	I _T = 10 A; T _j = 25 °C; see <u>Figure 9</u>	-	1.3	1.65	V
V_{GT}	gate trigger voltage	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}; T_j = 25 \text{ °C};$ see Figure 10	-	0.7	1.5	V
		$V_D = 400 \text{ V}; I_T = 0.1 \text{ A}; T_j = 125 ^{\circ}\text{C};$ see Figure 10	0.25	0.4	-	V
I _D	off-state current	V _D = 800 V; T _j = 125 °C	-	0.1	0.5	mA
Dynamic cha	aracteristics					
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 535 V; T_j = 125 °C; exponential waveform	1000	4000	-	V/µs
dl _{com} /dt	rate of change of commutating current	V_D = 400 V; T_j = 125 °C; $I_{T(RMS)}$ = 8 A; dV_{com}/dt = 20 V/ μ s; gate open circuit; snubberless condition; see Figure 11	-	14	-	A/ms
t _{gt}	gate-controlled turn-on time	$I_{TM} = 12 \text{ A}; V_D = 800 \text{ V}; I_G = 0.1 \text{ A};$ $dI_G/dt = 5 \text{ A/}\mu\text{s}$	-	2	-	μs



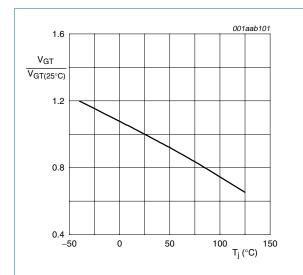


Fig 10. Normalized gate trigger voltage as a function of junction temperature

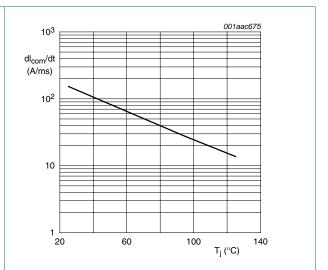


Fig 11. Rate of rise of commutating current as a function of junction temperature; typical values

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7. Package outline

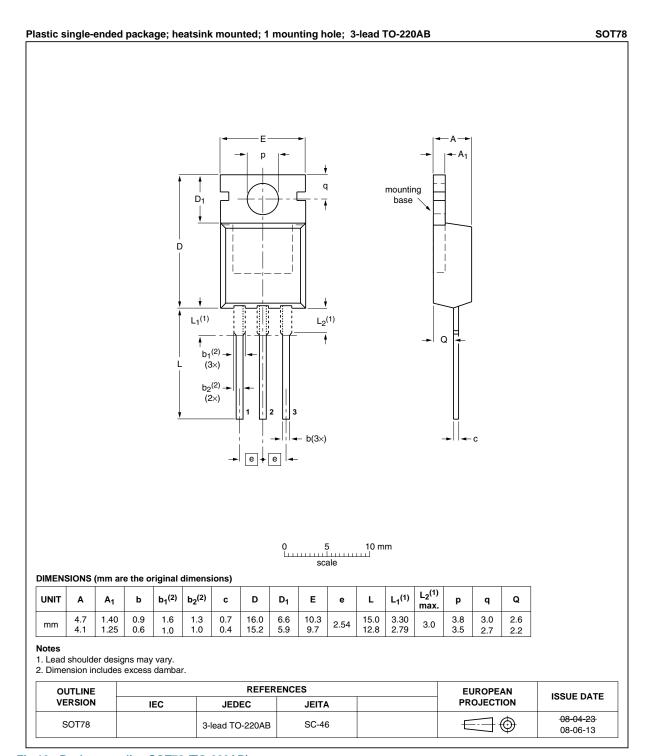


Fig 12. Package outline SOT78 (TO-220AB)

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8. Revision history

Table 7. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BTA208-800B v.2	20110412	Product data sheet	-	BTA208_SERIES_B v.1
Modifications:		e format of this data sheet has been redesigned to comply with the new identi idelines of NXP Semiconductors.		
	 Legal texts ha 	ave been adapted to the r	ew company name	where appropriate.
	 Type number 	BTA208-800B separated	from data sheet BT	A208_SERIES_B v.1.
BTA208_SERIES_B v.1	19970901	Product specification	-	-

9. Legal information

9.1 Data sheet status

Document status [1] [2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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