

Dual N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY

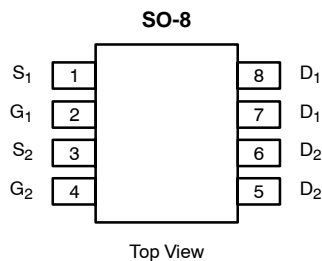
V _{DS} (V)	r _{DS(on)} (Ω)	I _D (A)
30	0.022 @ V _{GS} = 10 V	7.5
	0.030 @ V _{GS} = 4.5 V	6.5

FEATURES

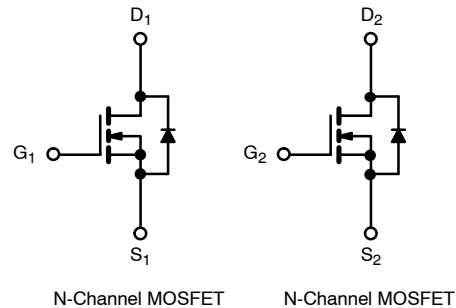
- TrenchFET® Power MOSFET
- 100% R_g Tested



Pb-free
Available



Ordering Information: Si4804DY
Si4804DY-T1 (with Tape and Reel)
Si4804DY—E3 (Lead (Pb)-Free)
Si4804DY-T1—E3 (Lead (Pb)-Free with Tape and Reel)



ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C UNLESS OTHERWISE NOTED)

Parameter		Symbol	10 secs	Steady State	Unit
Drain-Source Voltage		V _{DS}	30		V
Gate-Source Voltage		V _{GS}	± 20		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	I _D	7.5	5.7	A
	T _A = 70 °C		6.0	4.6	
Pulsed Drain Current		I _{DM}	20		
Continuous Source Current (Diode Conduction) ^a		I _S	1.7	0.9	
Single Avalanche Current	L = 0.1 mH	I _{AS}	10		mJ
Single Avalanche Energy		E _{AS}	5		
Maximum Power Dissipation ^a	T _A = 25 °C	P _D	2.0	1.1	W
	T _A = 70 °C		1.3	0.7	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150		°C

THERMAL RESISTANCE RATINGS

Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	t ≤ 10 sec	R _{thJA}	52	62.5	°C/W
	Steady State		93	110	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	35	40	

Notes

a. Surface Mounted on 1" x 1" FR4 Board.

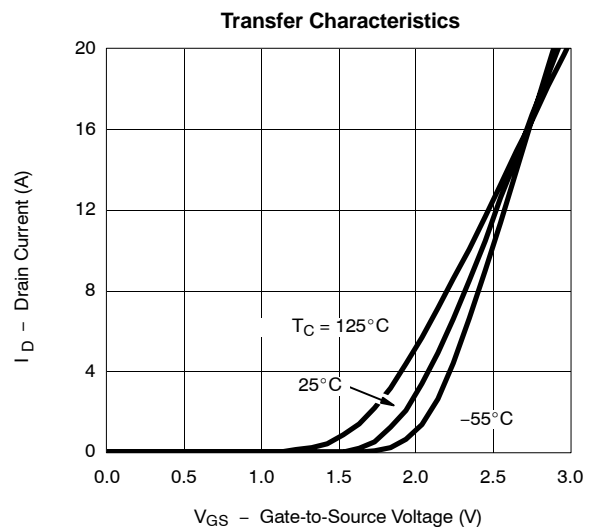
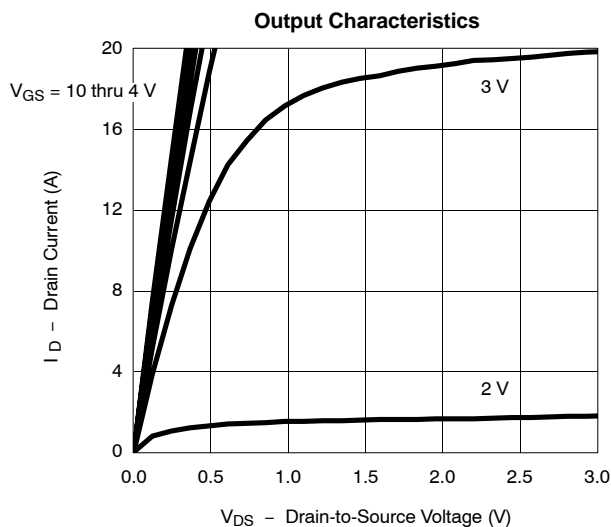
SPECIFICATIONS (T_J = 25 °C UNLESS OTHERWISE NOTED)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	0.8		1.9	V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±20 V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V			1	μA
		V _{DS} = 30 V, V _{GS} = 0 V, T _J = 55 °C			5	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	20			A
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = 10 V, I _D = 7.5 A		0.018	0.022	Ω
		V _{GS} = 4.5 V, I _D = 6.5 A		0.024	0.030	
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 7.5 A		22		S
Diode Forward Voltage ^a	V _{SD}	I _S = 1.7 A, V _{GS} = 0 V		0.8	1.2	V
Dynamic^b						
Total Gate Charge	Q _g	V _{DS} = 15 V, V _{GS} = 10 V, I _D = 7.5 A		13	20	nC
Gate-Source Charge	Q _{gs}			2		
Gate-Drain Charge	Q _{gd}			2.7		
Gate Resistance	R _g		0.5	1.9	4	Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 15 Ω I _D ≅ 1 A, V _{GEN} = 10 V, R _g = 6 Ω		8	16	ns
Rise Time	t _r			10	20	
Turn-Off Delay Time	t _{d(off)}			21	40	
Fall Time	t _f			10	20	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 1.7 A, di/dt = 100 A/μs		40	80	

Notes

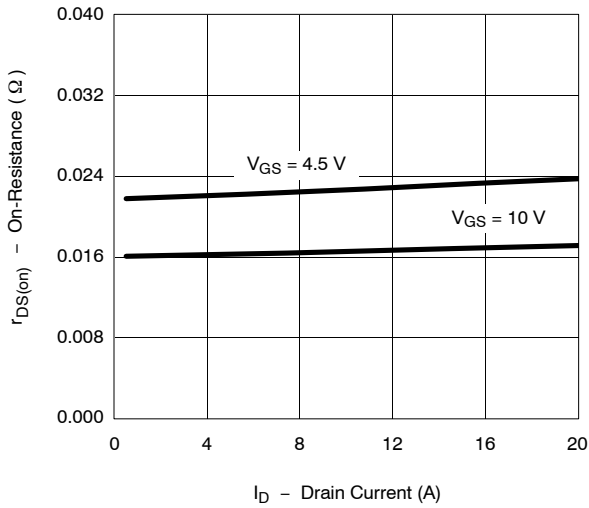
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

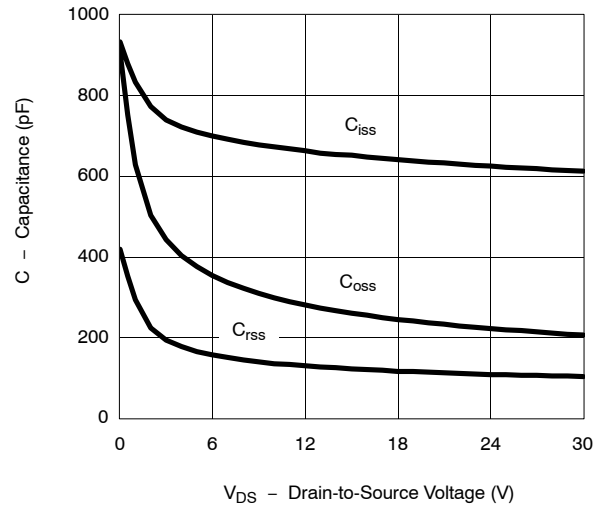
TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)

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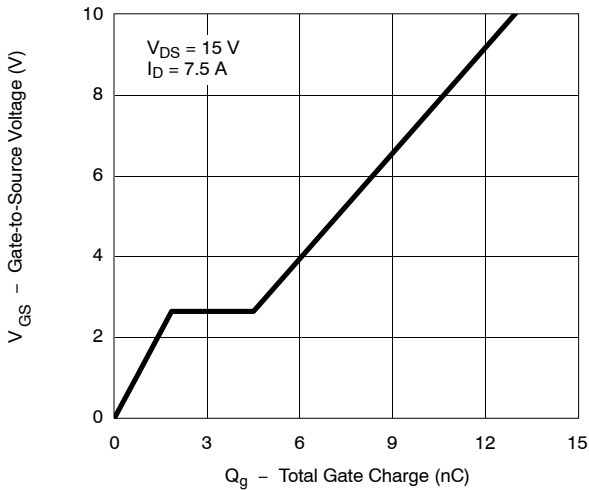
On-Resistance vs. Drain Current



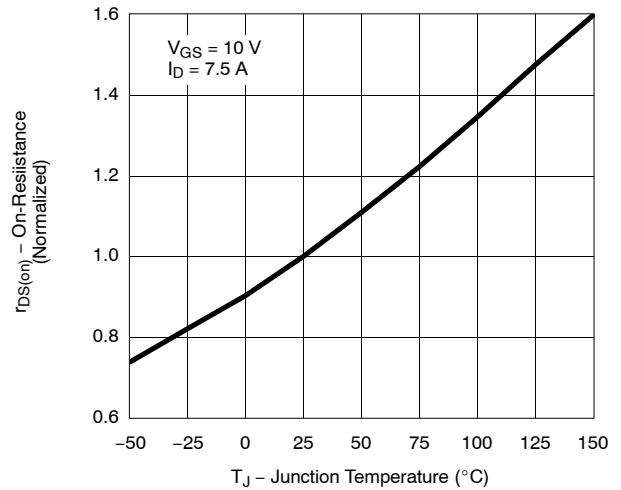
Capacitance



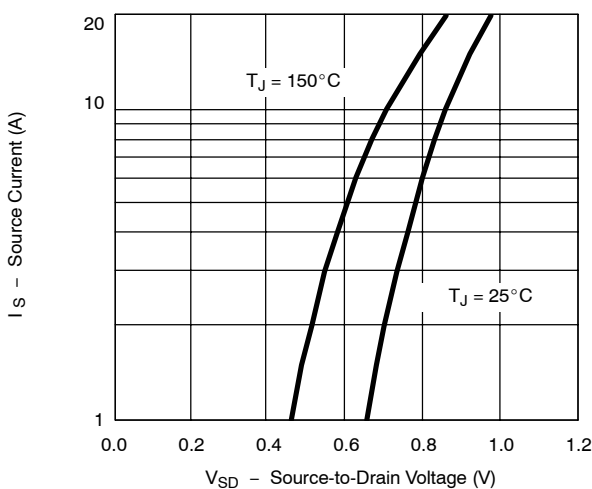
Gate Charge



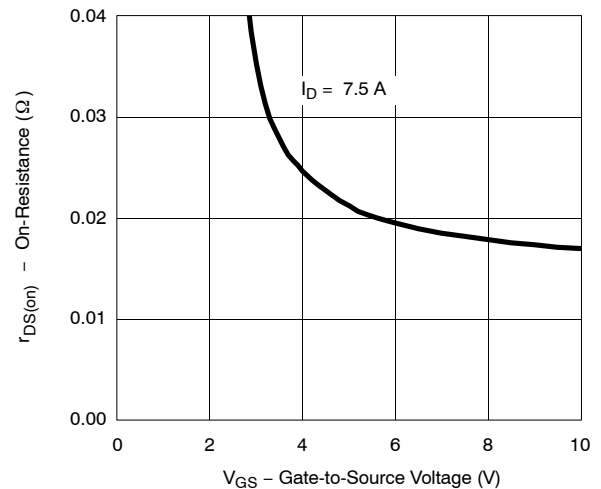
On-Resistance vs. Junction Temperature



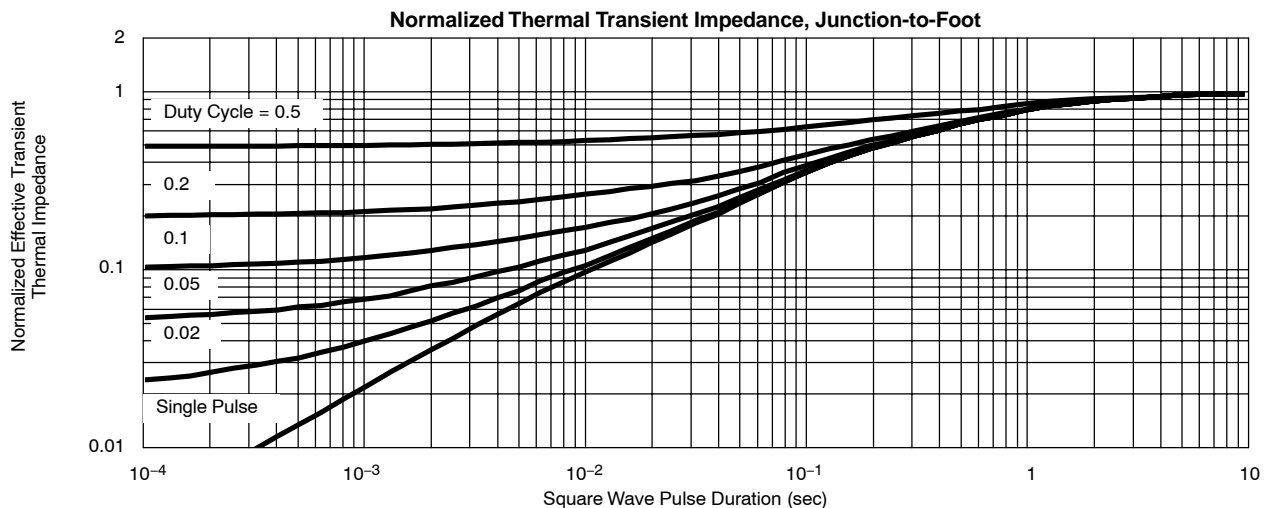
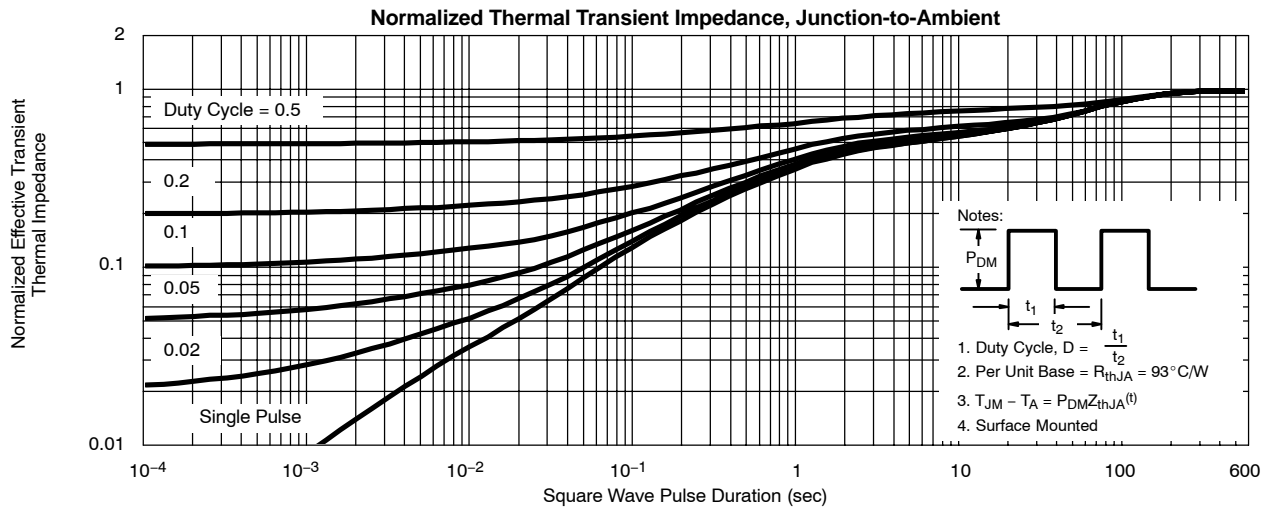
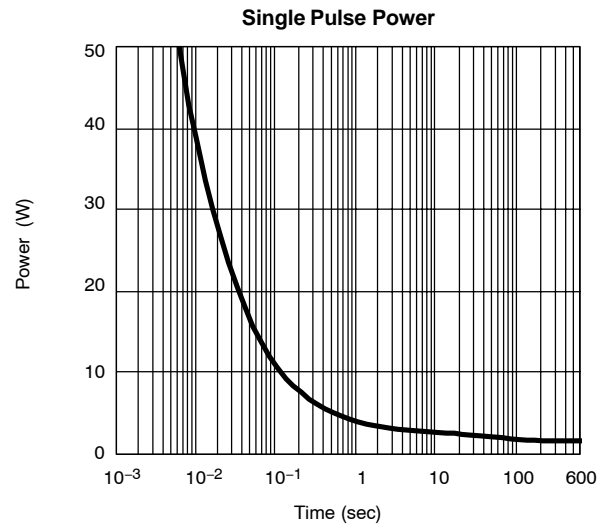
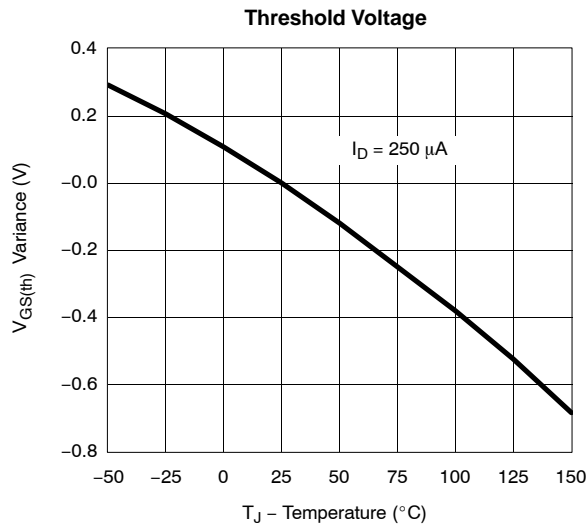
Source-Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)



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