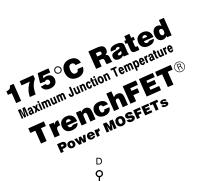
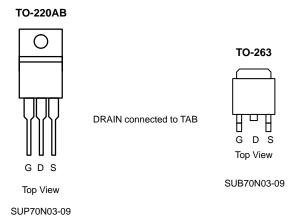


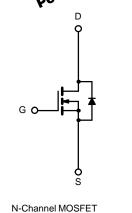


# N-Channel 30-V (D-S), 175°C, MOSFET PWM Optimized

PRODUCT SUMMARY			
V <sub>(BR)DSS</sub> (V)	$r_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)	
30	0.009 @ V <sub>GS</sub> = 10 V	±70 <sup>a</sup>	
	0.015 @ V <sub>GS</sub> = 4.5 V	± 55	







ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> = 25°C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	±30	V	
Gate-Source Voltage		$V_{GS}$	±20	<b>7</b> '	
Continuous Drain Current (T <sub>J</sub> = 175°C)	T <sub>C</sub> = 25°C	-	±70 <sup>a</sup>		
	T <sub>C</sub> = 100°C	<b>─</b>	±50		
Pulsed Drain Current		I <sub>DM</sub>	± 180	<b> </b>	
Avalanche Current		I <sub>AR</sub>	±45		
Repetitive Avalanche Energy <sup>b</sup>	L = 0.1 mH	E <sub>AR</sub>	101	mJ	
Power Dissipation	T <sub>C</sub> = 25°C	P <sub>D</sub>	93c	W	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Limit	Unit	
Junction-to-Ambient	PCB Mount (TO-263) <sup>d</sup>	$R_{thJA}$	40	°C/W	
	Free Air (TO-220AB)		62.5		
Junction-to-Case		R <sub>thJC</sub>	1.6		

#### Notes:

- Package limited.

- See SOA curve for voltage derating.
  When mounted on 1" square PCB (FR-4 material).

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Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Static			•		•	
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{DS} = 250 \mu\text{A}$	1	2		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS}$ = 0 V, $V_{GS}$ = $\pm 20$ V			±100	nA
Zero Gate Voltage Drain Current		$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ
	I <sub>DSS</sub>	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$			50	
		$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 ^{\circ}\text{C}$			150	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	70			Α
Drain-Source On-State Resistance <sup>a</sup>		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A		0.007	0.009	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 125°C		<u> </u>	0.0135	
	r <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 175°C			0.017	Ω
		$V_{GS} = 4.5 \text{ V}, I_D = 20 \text{ A}$		0.011	0.015	1
Forward Transconductancea	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A	30	60		S
Dynamic <sup>b</sup>	•				•	
Input Capacitance	C <sub>iss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		2700		pF
Output Capacitance	C <sub>oss</sub>			680		
Reversen Transfer Capacitance	C <sub>rss</sub>			360		
Total Gate Charge <sup>c</sup>	Qg	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 70 \text{ A}$		45	70	nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>			8.5		
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			11		
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>	$V_{DD}$ = 15 V, R <sub>L</sub> = 0.21 $\Omega$ I <sub>D</sub> $\simeq$ 70 A, V <sub>GEN</sub> = 10 V, R <sub>G</sub> = 2.5 $\Omega$		13	20	ns
Rise Time <sup>c</sup>	t <sub>r</sub>			7	15	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			35	60	
Fall Time <sup>c</sup>	t <sub>f</sub>			12	20	
Source-Drain Diode Ratings a	nd Characteristic	s (T <sub>C</sub> = 25°C) <sup>b</sup>				
Continuous Current	I <sub>S</sub>				70	
Pulsed Current	I <sub>SM</sub>				180	A
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 70 A, V <sub>GS</sub> = 0 V		1.2	1.5	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 70 A, di/dt = 100 A/μs		35	70	ns

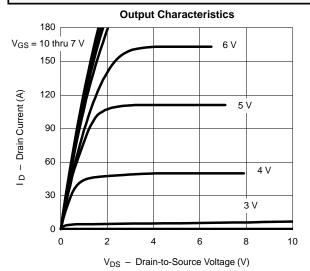
Notes: a. Pulse test; pulse width  $\leq 300~\mu s$ , duty cycle  $\leq 2\%$ . b. Guaranteed by design, not subject to production testing. c. Independent of operating temperature.

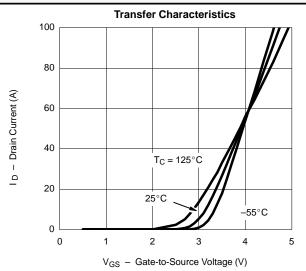


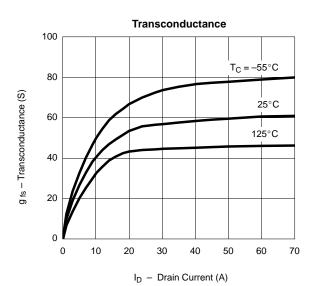


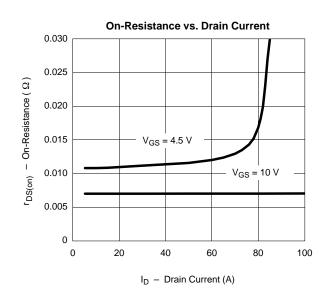
# **Vishay Siliconix**

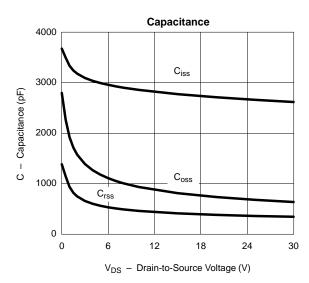
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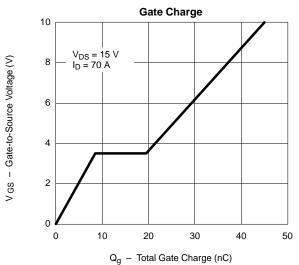










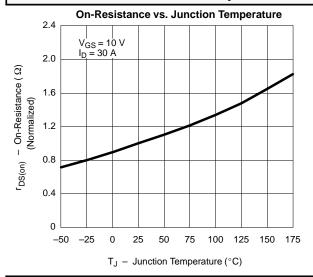


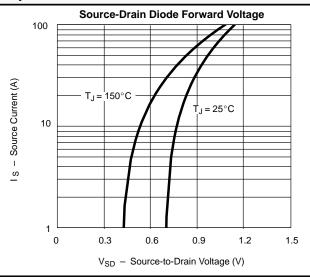
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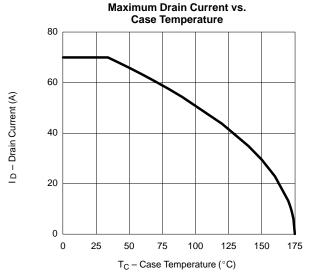


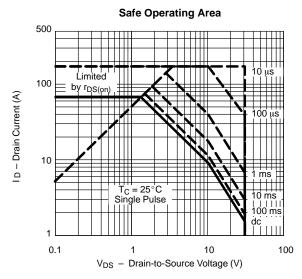
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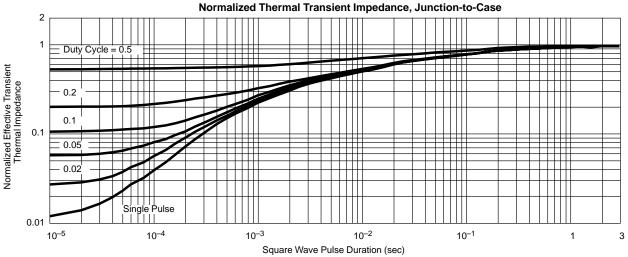




#### THERMAL RATINGS







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