ConnectCore® for i.MX53

High-End Core Modules with Wired and Wireless Network Connectivity

High-end Cortex-A8 System-on-Module solution delivers industry-leading performance, low-power operation, and fully integrated 802.11a/b/g/n + Ethernet networking.

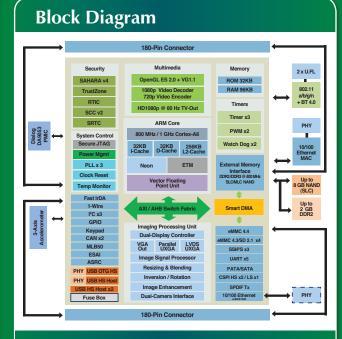
Overview

The network-enabled ConnectCore for i.MX53 module family is a highly integrated and future-proof System-on-Module (SoM) solution based on the new Freescale[®] i.MX53 application processor. It offers a high-performance 1 GHz ARM[®] Cortex[™]-A8 core, wired and wireless connectivity options, powerful 1080p/720p video encoding/decoding capabilities and a complete peripheral set.

The ConnectCore for i.MX53 family builds on the successful ConnectCore for i.MX51 modules by providing a form factor compatible option with significantly improved processing, memory, video and connectivity capabilities. It is a scalable and energy-efficient module family, ideal for medical devices, security/surveillance equipment, industrial applications and digital signage.

Complete and cost-efficient Digi JumpStart Kits[®] for Digi Embedded Linux, Timesys LinuxLink, Android and Microsoft Windows Embedded Compact 7 allow immediate and professional embedded product development with dramatically reduced design risk and time-to-market.





Features/Benefits

- High-performance 32-bit System-on-Module
- Long-term product availability solution
- Single and dual 10/100 Mbit Ethernet networking
- Pre-certified 802.11a/b/g/n Wi-Fi interface
- High-performance 2D/3D Graphics Processing Unit
- Hardware video processing with 1080p decoding
- Low-emission design with FCC Class B compliance
- ZigBee, cellular and satellite connectivity options
- Industrial operating temperature support

| Digi JumpStart Ki | it for Embedded Linux | Digi JumpSt | tart Kit for Microsoft Windows | Digi | JumpStart Kit for Android |
|---|--|--|--|---|---|
| Built around a standard Lim JumpStart KI for Enhedded samp for sur, compilet of OFL and a standard and a standard and a build secure network-new to build secure network-new formeticine for 10053. The His Includes Digi ESP ¹⁰ If and fully Limux-hoted Inter- mentation of the standard and and fully Limux-hoted Inter- setions and the standard and and standard and and and spanical limux develops papalical interface. | x 2.6 kernel distribution, the Digi Linux is tailored to the specific velopment and provides an te-shelf embedded development ponents that are required | Microsoft Windows componentized op technology compo embedded applica It includes a wide as graphical use applications using The Digi JumpStar Compact 7 provide development not includes support f as power manager development on the includes support f as power manager development on the compact 7 dev f Compact 7 dev Samless inter Compact av Compact av Samless inter Compact av Samless inter Compact av Samless inter Compact av Samless inter Compact av Samless inter Compact av Samless inter Samless inter S | Enbadded Compact 7 is n highly ersting system, offering pre-tacted enests designed to create sophisticated tions with minimized design effort and risk, and the system of the system of the system and the system of the system of the system offersional Microsoft Visual Studio 2008 also support native and managed code various programming languages. It & fin Microsoft Visual Studio 2008 at a complete kit with all hardware and the connectione for LNXG3 modules. This and an occessor platform fratures such ent all mocessor platform fratures such ent and mocessor platform fratures such ent and studies the studies of the system of the system of the system of immediate Windows Embedded without the studies with the studies of the student studies with the studies of the system of immediate Windows Embedded | Android is an idd and feature-com software develop market. The Digi builds on the sit eco-system by pr application deve the specific need Ready to use rigi family with Digi design Android Ł and often diffeo Ecomplete ou Embedded sp Digi ESP IDE development | al offbaue platform to create perfectional plate products with significantly reduced met effort an imposed overall time to- Application Development Nit for Android on Android adVenue for Marchael and the optimized and the signature of Android on an and the back, the site signature and the optimized and the site of the site of the optimized and the site of the site of the optimized and the site of the site of the creation of the back, the site signature to a site of the back of the site of the site of the creation of the back, the site site of the creation of the back of the site of the site of the creation of the back of the site of the site of the creation of the site of the site of the site of the creation of the site of the site of the site of the creation of the site of the site of the site of the creation of the site |
| Development Ki Software Platform | Digi Embedded L | inux | Microsoft Windows Embedded Com | pact 7 | Android |
| Module | 1 GHz Connect/ore Wi-LMX33 with 512 MB NAND flash, 512 MB D0R2, dual Ethernet, accelerometer 3 serial ports (1 x 85-232/422/48, 1 x 85-232 Ty/Rx, 1 x TTL), VGA connector, HDMI connector, CUD/fouchscreen connectors, external camera connectors, use/application connectors, Ethernet R-J-5 connector (primary). Ethernet header (secondary), WLA naterna connectors (RF-SMA), SD/MA, Edx, MicroSD Sut, (CAN bus, SAK, US 80TG, 4, x 186 bott, TC/SPT header. SHIP connector, ethernal camera connectors, (RF-SMA), SD/MA, Edx, MicroSD Sut, (CAN bus, SAK, US 80TG, 4, x 186 bott, TC/SPT header. SHIP connectors, REV, 802.337 (PcE) model societ (module sold separately), (PD screw terminal, user push-buttors, user EEb, battery, 802.337 (PcE) model societ (module sold separately), -JTJAG connectors, SHIP connecto | | | accelerometer | |
| Development Board | | | | | |
| | Digi Embedded Linux with Liv | Digi Windows Compact 7 CD: Microsoft Windows | | d 2.3.4 (Gingerbread), Eclipse-based Digi ES | |

| CD/DVD | Digi Embedded Linux with Live DVD support, Eclipse-based Digi ESP IDE, Linux and platform specific source code, Universal boot loader source code (U-Boot), sample code, documentation | Embedded Compact 7 BSP w/source code, UniBoot Loader (U-Boot) source code, sample code, documentation; Microsoft Windows Embedded Compact 7 evaluation DVD: 180-day trial of Microsoft Embedded Compact 7, Platform Builder, Visual Studio 2008 | Android 2.3.4 (Gingerbread), Eclipse-based Digits IDE w/ADT extensions (Windows/Linux), Universal boot loader source code (U-Boot), Kernel and rootfs customzation option (Linux), sample code, documentation |
|---------------|--|--|---|
| Documentation | Quick start guide, Digi Embedded Linux user's guide, hardware reference manual, development board schematics | Quick start guide, Digi Windows Compact 7, BSP user's guide, hardware reference manual, development board schematics | Getting started guide, Digi Android API extensions, hardware reference manual, development board schematics |
| Accessories | 7" WVGA Sharp LED (LQ070Y3DG3B) with touch screen, External wall power supply with interchangeable outlet adapters (North America, EU, UK, and Australia). Ethernet cable, antennas, serial cable | | |
| Part Numbers | CC-WMX53-LX | CC-WMX53-CE | CC-WMX53-ANDRD |

Please refer to the feature specs on the Digi website for detailed information about the specific software platform capabilities. Additional platform support for Timesys LinuxLink available. Please contact Digi or Timesys directly.

| Specifications | ConnectCore® i.MX53 ConnectCore® Wi-i.MX53 | | |
|---------------------------------------|--|--|--|
| Processor | | | |
| Processor Model | Freescale® i.MX53 (i.MX535/i.MX537) | | |
| Speed Grades | 800/1000 MHz | | |
| Core Type | ARM® Cortex [™] -A8 | | |
| Cache Memory | 32k L1 I-Cache, 32k L1 D-Cache, 256k L2-Cache (unified) | | |
| Internal RAM | 128 KB (secure/non-secure) | | |
| Vector Floating Point | • | | |
| NEON Media Acceleration | • | | |
| | | | |
| Flash | Up to 8 GB NAND flash | | |
| RAM | Up to 2 GB DDR2 | | |
| Debug | | | |
| Secure JTAG | • | | |
| ETM/ETB | • | | |
| Power Management | | | |
| Power Modes | Run, Wait, Stop, Low-power screen refresh | | |
| Wake-up Events | GPIO, keypad, RTC (day/time of day), SD card/USB cable insertion, battery/charger attach | | |
| Dynamic Voltage and Frequency Scaling | • | | |
| Backlight Drivers | 3 | | |
| Battery Management | • | | |
| Real-Time Clock | | | |
| Battery Backup (External) | • | | |
| | | | |
| Hardware Encryption/Decryption | AES, DES/30ES, RC4, C2 IGSA, ECC MD5, SIA-1/224/256 | | |
| Random Number Generator | • | | |
| Run Time Integrity Checker | • | | |
| Secure RAM (internal) | • | | |
| Fuse Box (e-Fuses) | 64 Bits (application-specific use) | | |
| Physical Tamper Detectors | • | | |
| Timers | | | |
| General Purpose Timer | 32-bit up-counter with clock source selection 2 Input capture channels 3 output compare channels, forced compare | | |
| Enhanced Periodic Interrupt Timer | 32-bit down-counter with clock source selection Set-and-forget/free-running modes Precision interrupt generation | | |
| Watchdog | • | | |
| Thermal Management | | | |
| Temperature Monitor | On-chip sensor, precision 0 to 135°C $\pm5°C$ Software support for thermal-aware Dyamic Frequency and Voltage Scaling (DFVS) | | |

Module Feature

| Specifications | ConnectCore® i.MX53 ConnectCore® Wi-i.MX53 | | |
|--------------------------------|---|--|--|
| Connectivity | | | |
| UART | Up to 3 channels with bit rates up to 4 MHz, IrDA 1.0 support | | |
| IrDA Infrared | Medium InfraRed (0.576/1.152 Mbps), Fast InfraRed (4 Mbps) | | |
| CAN | CAN 2.0b, up to 2 channels, up to 1 Mbps each | | |
| CSPI | Master and slave mode Bit rate up to 25 Mbps (master) | | |
| eCSPI | Up to 2 eCSPI channels, master and slave mode Bit rates up to 66.5 Mbps (master) | | |
| PC | Up to 3 channels, master/slave (7-/10-bit addressing) All: Standard (100 kbps) and fast (400 kbps) mode | | |
| SD/SDI0/MMC/eMMC | Up to 4 ports, 1-/4-/8-bit modes MMC: Up to 416 Mbps (8-bit mode), SO/SOUD: Up to 2000 Mbps (4-bit mode) eMMC 4.4 (Utra high speed, up to 832 Mbps | | |
| P-ATA | Up to 66 MB/s data rate PIO mode (0,1,2,3,4), multi-word DMA mode (0,1,2), Ultra DMA mode (0,1,2,3,4,5) | | |
| SATA | SATA II, up to 1.5 Gbps | | |
| USB 2.0 High-Speed | Up to 3 USB 2.0 High-Speed Host ports, one with integrated PHY Up to 1 USB 2.0 OTG port with integrated PHY | | |
| Media Local Bus (MLB) | MOST (Media Oriented Systems Transport) interface, up to 50 Mbps | | |
| 1-Wire | • | | |
| ISO 7816 (SIM/Smart Card) | • | | |
| Keypad | 8x8 keypad matrix | | |
| PWM | 2 | | |
| ADC (10-bit) | Up to 4 channels | | |
| GPIO | Up to 128 GPIOs | | |
| External Memory Bus | 16-bit data/28-bit address in non-multiplexed address/data mode 16-bit or 32-bit data/28-bit address in multiplexed address/data mode | | |
| Multimedia | | | |
| Camera | Two parallel camera ports, up to 20-bit, up to 120 MHz peak | | |
| Display | Five interfaces available - with total rate of all interfaces up to 100 Mytaiol(svc, 24 bpp Up to two displays can be divers imultaneously (corrent refersh) Concurrent apprichmonous access to two additional devices, e.g., display controllers and smart displays Parellelit: Two 24-bit display orsts, up to 155 Mytaiol(svc, e.g., UKGA @ 60 bt UVDS: One port up to 155 Mytaiol(svc, e.g., UKGA @ 60 bt One IV-evu/VKA port, up to 150 Mytaiol(svc, e.g., 0060pt) One IV-evu/VKA port, up to 150 Mytaiol(svc, e.g., 0060pt) | | |
| Image Processing Unit | Image enhancements, video/graphics combining, resizing, rotation/inversion, color conversion/correction | | |
| Video Processing Unit | MPEG-4, H.263, H.264, MPEG-2, VC-1, DivX, RV10, MJPEG 1080p30 decode, 720p30 encode | | |
| GPU (2D/3D) | 33 million triangles/sec, 200 million pixels/sec raw OpenVG 1.0, OpenGL ES Common Profile v1.0/v1.1/Direct3D Mobile, OpenGL ES Profile v2.0 | | |
| Touchscreen Interface (4-wire) | • | | |
| SPDIF (Tx) | • | | |
| I ² S/AC97/SSI | Up to 3 channels | | |
| ESAI | Multi-channel digital audio, up to 1.4 Mbps each channel | | |
| ASRC | • | | |

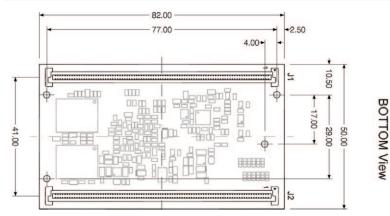
| Specifications | ConnectCore® i.MX53 | ConnectCore® Wi-i.MX53 | |
|--|---|---|--|
| Ethernet | | | |
| Physical Layer | 10/10 | DBase-T | |
| Data Rates | 10/100 Mbps, auto-sensing | | |
| Duplex Mode | Full or half duplex, auto-sensing | | |
| IEEE 1588 | Yes, primary interface only | | |
| Power over Ethernet (802.3af) | | | |
| Power over Ethernet | Development board ready for 802.3a | f PoE application kit (sold separately) | |
| Accelerometer | | | |
| Three Axis Accelerometer | ±2g/±4g/±8g Three Axis Low-g Freescale MA7455L | | |
| | | | |
| Standard | N/A | 802.11a/b/g/n (2.4/5 GHz) | |
| Antenna Connectors | N/A | 2 x U.FL | |
| Dual Diversity | N/A | • | |
| Frequency Bands | N/A | 2.412 - 2.484 GHz 4.900 - 5.850 GHz | |
| Data Rates | N/A | 802.11b: 1, 2, 5.5, 11 Mbps 802.11a/g: 6, 9, 12, 18, 24, 36, 48, 54 Mbps 802.11n: 6.5, 13, 19.5, 26, 39, 52, 58.5, 65 Mbps (MCS 0-7 | |
| Modulation | N/A | DBPSK, DQPSK, CCK, BPSK, QPSK, 16-QAM, 64-QAM | |
| 802.11n Features | N/A | A-MPDU / A-MSDU, PSMP, MTBA, STBC, Greenfield Preamble, RIFS | |
| Transmit Power (±2 dBm) | N/A | 802.11b: 17 dBm typical 802.11g/n: 15 dBm typical 802.11a: 12 dBm typical | |
| Security | N/A | WEP, WPA-PSK/WPA2-Personal, WPA/WPA2 Enterprise, 802.11 | |
| QoS | N/A | WMM, WMM-PS, 802.11e | |
| Roaming Enhancements | N/A | 802.11k/r | |
| Extended Range (802.11n) | N/A | • | |
| Radio Certifications (Pending) (Future Option) | N/A | USA, Canada, EU, Japan | |
| Bluetooth (Future Option) | | | |
| Bluetooth 2.1 + EDR | N/A | • | |
| Bluetooth 3.0 + HS | N/A N/A | | |
| Bluetooth 4.0 w/Bluetooth Low Energy | N/A | • | |
| Class | N/A | 1.5 | |
| нсі | N/A | • | |
| Power Requirements ¹ | | | |
| Typical / Idle | 700 mA @ 3.75 V | / 200 mA @ 3.75 V | |

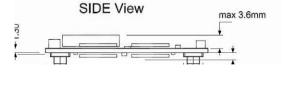
¹ Baseline power consumption based on standard use case without WLAN and Ethernet. See Hardware Reference Manual for more detailed information.

Module I
Contact your local distributor or Digi sales office for details.

Module Feature

| Specifications | ConnectCore® i.MX53 | ConnectCore® Wi-i.MX53 | |
|-------------------------------------|---|------------------------|--|
| Mechanical | | | |
| Dimensions (L x W x H) | 82 mm x 50 mm x 6.5 mm | 82 mm x 50 mm x 8 mm | |
| Module Connectors | 2 x 180-pin board-to-board connectors, 0.8 mm pitch (Mating connector FCI P/N 61083-184409LF or similar) | | |
| Environmental | | | |
| Operating Temperature | -40°C to +85°C (800 MHz) -20°C to +70°C (1000 MHz) | | |
| Storage Temperature | -50°C to +125°C | | |
| Relative Humidity | 5% to 90% (non-condensing) | | |
| Altitude | 12,000 feet (3,658 meters) | | |
| Temperature / Climate Tests | IEC 60068-2-1 (Ab/Ad Cold: 16 h with -40°C), IEC 60068-2-2 (Bb/Bd: Dry heat: 16 h with +85°C), IEC 60068-2-78 (Damp heat steady state: 16h with +40°C and 93%rH) | | |
| Vibration / Shock Tests | IEC 60068-2-6 Method Fc, IEC 60068-2-64 Method Fh, IEC 60068-2-27 Method Ea | | |
| Regulatory Approvals | | | |
| FCC Part 15 Class B | • | | |
| FCC Part 15 Sub C Section 15.247 | • | | |
| IC RSS-210 Issue 5 Section 6.2.2(0) | • | | |
| EN55022:2006 Class B | • | | |
| ICES-003, Class B | • | | |
| VCCI, Class B | • | | |
| EN55024:1998 +A1:2001, A2:2003 | • | | |
| EN61000-3-2:2006 | • | | |
| EN61000-3-3:1995 +A1:2001, A2:2005 | • | | |
| EN60950-1:2001 (UL60950-equivalent) | • | | |
| CSA C22.2, No. 60950 | • | | |







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• Module Feature

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