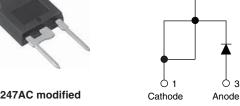




Vishay Semiconductors

Hyperfast Rectifier, 30 A FRED Pt®





TO-247AC	modified
I O LTIAO	mounica

PRODUCT SUMMARY

Package

I_{F(AV)}

 V_R

V_F at I_F

t_{rr} typ.

T_J max.

Diode variation

Base common cathode

TO-247AC modified (2 pins)

30 A

600 V

2.6 V

See Recovery table

175 °C

Single die

FEATURES

- · Hyperfast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Single diode device
- AEC-Q101 qualified, meets JESD 201 class 1A whisker test









FREE

DESCRIPTION/APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage	V_{RRM}		600	V	
Average rectified forward current	I _{F(AV)}	T _C = 116 °C	30	۸	
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	300	A	
Operating junction and storage temperatures	T _J , T _{Stg}		- 65 to 175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	600	-	-	
Forward voltage	and reliance	I _F = 30 A	-	2.0	2.6	V
Forward voltage V _F	I _F = 30 A, T _J = 150 °C	-	1.34	1.75		
Poverse leekage ourrent		$V_R = V_R$ rated	-	0.3	50	
Reverse leakage current I _R	I'R	$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	60	500	μΑ
Junction capacitance	C _T	V _R = 600 V	-	33	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	3.5	-	nΗ



VS-30EPH06HN3

Vishay Semiconductors

DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	28	35	
Reverse recovery time	t _{rr}	T _J = 25 °C		-	31	-	ns
		T _J = 125 °C	$I_F = 30 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	77	-	
Peak recovery current I _{RRM}	ı	T _J = 25 °C		-	3.5	-	۸
	IRRM	T _J = 125 °C		-	7.7	-	A
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	65	-	nC
		T _J = 125 °C		-	345	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case per leg	R _{thJC}		-	0.5	0.9	
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	70	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.4	-	
Weight			-	6.0	-	g
weignt		-	0.22	-	oz.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-247AC modified		30EP	H06H	



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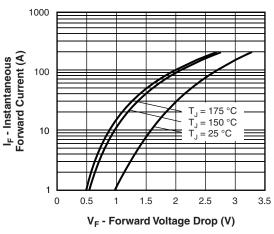


Fig. 1 - Typical Forward Voltage Drop Characteristics

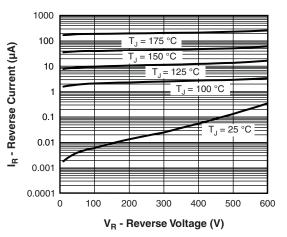


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

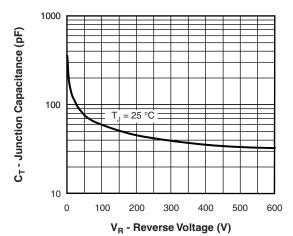


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

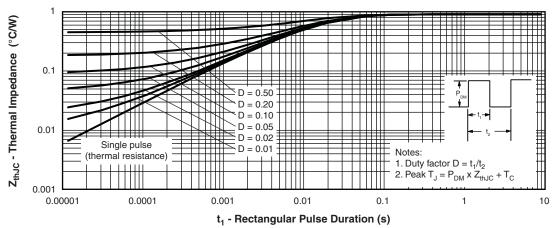


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics





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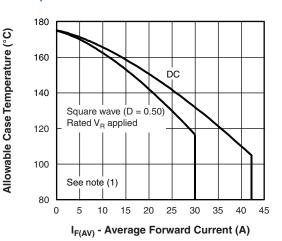


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

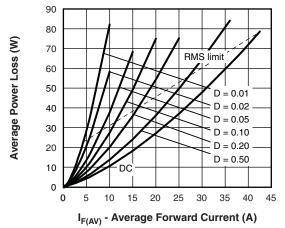


Fig. 6 - Forward Power Loss Characteristics

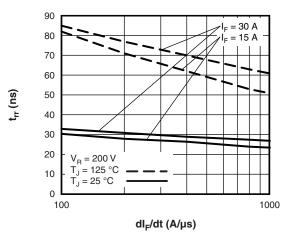


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

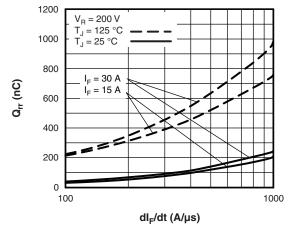


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{l} \text{(1)} \ \ \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{Rated } V_R \\ \end{array}$



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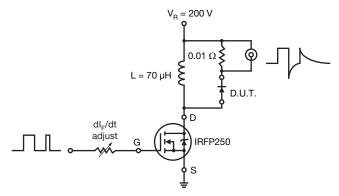
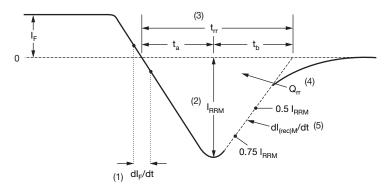


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) $\mathbf{Q}_{\rm rr}$ area under curve defined by $\mathbf{t}_{\rm rr}$ and $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} x I_{RRM}}{2}$$

(5) dl_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

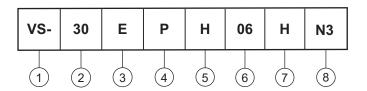


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ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Current rating (30 = 30 A)
- 3 Circuit configuration:

E = Single diode

- 4 Package:
 - P = TO-247AC modified
- 5 H = Hyperfast recovery
- 6 Voltage rating (06 = 600 V)
- 7 H = AEC-Q101 qualified
- 8 Environmental digit:
 - -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFO	RMATION (Example)		
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-30EPH06HN3	25	500	Antistatic plastic tube

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95253</u>				
Part marking information	www.vishay.com/doc?95442			



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