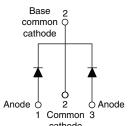




Schottky Rectifier, 2 x 20 A





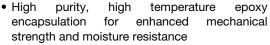
TΩ	-220	1 A D	•
- 10	-ノノ(ΙДΕ	

Base g common (cathode	2
Anode 0 2	Anode mode

PRODUCT SUMMARY				
Package	TO-220AB			
I _{F(AV)}	2 x 20 A			
V_{R}	45 V			
V _F at I _F	0.58 V			
I _{RM} max.	95 mA at 125 °C			
T _J max.	150 °C			
Diode variation	Common cathode			
E _{AS}	20 mJ			

FEATURES

- 150 °C T_J operation
- · Low forward voltage drop
- High frequency operation





- · Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

DESCRIPTION

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UNITS						
I _{F(AV)}	Rectangular waveform (per device)	40	Α					
V _{RRM}		45	V					
I _{FRM}	T _C = 118 °C (per leg)	40	^					
I _{FSM}	t _p = 5 μs sine	900	— A					
V _F	20 A _{pk} , T _J = 125 °C	0.58	V					
T _J	Range	- 65 to 150	°C					

VOLTAGE RATINGS							
PARAMETER	SYMBOL	VS-MBR4045CTPbF	VS-MBR4045CT-N3	UNITS			
Maximum DC reverse voltage	V _R	45	45	V			
Maximum working peak reverse voltage	V _{RWM}	45	45	V			

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDI	TIONS	VALUES	UNITS		
Maximum average per	leg	T _C = 118 °C, rated V _R		T 440.00 + 11/	20		
forward current per dev	vice I _{F(AV)}			40			
Peak repetitive forward current per leg	I _{FRM}	Rated V _R , square wave, 20 kHz, T _C = 118 °C		40	Α		
Maximum peak one cycle non-repetitive	l=a	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	900			
surge current per leg	I _{FSM}	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	210			
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 3 A, L = 4.40 mH		20	mJ		
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical		3	Α		



VS-MBR4045CTPbF, VS-MBR4045CT-N3

Vishay Semiconductors

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS	
		20 A	T _{.1} = 25 °C	0.60		
Maximum forward voltage drap	V _{FM} ⁽¹⁾	40 A	1j=25 C	0.78	v	
Maximum forward voltage drop	V FM (1)	20 A	T _{.1} = 125 °C	0.58	V	
		40 A	1J = 125 C	0.75		
	I _{RM} ⁽¹⁾	T _J = 25 °C		1	mA	
Maximum instantaneus reverse current		T _J = 100 °C	Rated DC voltage	50		
		T _J = 125 °C		95		
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$, (test signal range 100 kHz to 1 MHz) 25 °C		900	pF	
Typical series inductance	L _S	Measured from top of terminal to mounting plane		8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V _R			V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction temperature range	T_{J}		- 65 to 150	°C		
Maximum storage temperature range	T _{Stg}		- 65 to 175	C		
Maximum thermal resistance, junction to case per leg	R _{thJC}	DC operation	1.5			
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased (Only for TO-220)	0.50	°C/W		
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation (For D ² PAK and TO-262)	50			
Approximate weight			2	g		
Approximate weight			0.07	OZ.		
Mounting torque minimum		Non-lubricated threads	6 (5)	kgf · cm		
Mounting torque maximum		Non-lubricated tilleads	12 (10)	(lbf · in)		
Marking device		Case style TO-220AB	MBR4	045CT		

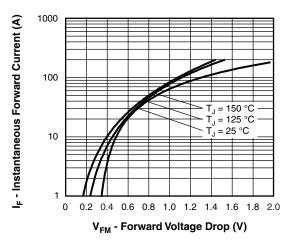


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

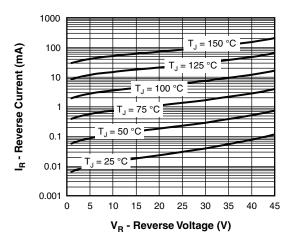


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

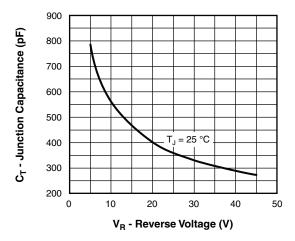


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

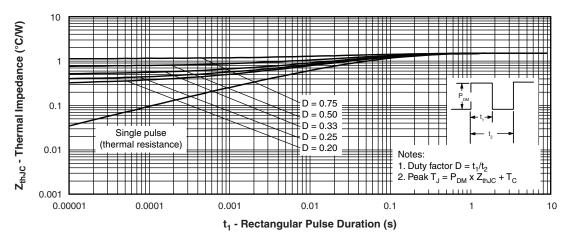


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

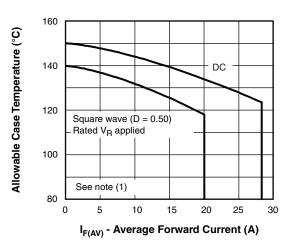


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

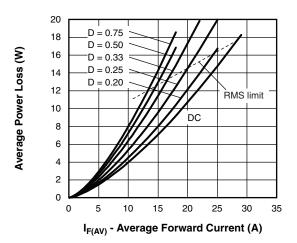


Fig. 6 - Forward Power Loss Characteristics

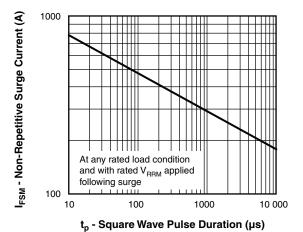


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

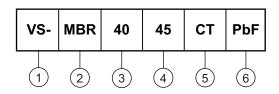
 $\begin{array}{ll} \text{(1)} \ \ \text{Formula used:} \ T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \ \text{at } (I_{F(AV)}/D) \ \text{(see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \ \text{(1 - D)}; \ I_R \ \text{at } V_{R1} = \text{Rated } V_R \\ \end{array}$

VS-MBR4045CTPbF, VS-MBR4045CT-N3

Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Schottky MBR series

- Current rating (40 = 40 A)

Voltage rating (45 = 45 V)

- CT = Essential part number

6 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

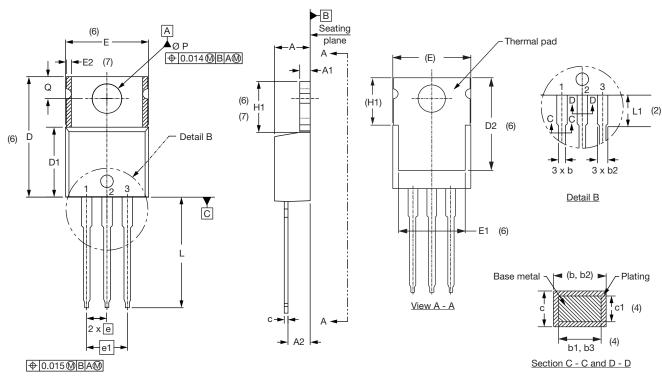
ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-MBR4045CTPbF	50	1000	Antistatic plastic tube				
VS-MBR4045CT-N3	50	1000	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS				
Dimensions		www.vishay.com/doc?95222		
But we directify weather	TO-220AB PbF	www.vishay.com/doc?95225		
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028		
SPICE model		www.vishay.com/doc?95296		



TO-220AB

DIMENSIONS in millimeters and inches



Lead assignments

Diodes

- 1. Anode/open
- 2. Cathode
- 3. Anode

Conforms to JEDEC outline TO-220AB

MILLIN	IETERS	INC	HES	NOTES
MIN.	MAX.	MIN.	MAX.	NOTES
4.25	4.65	0.167	0.183	
1.14	1.40	0.045	0.055	
2.56	2.92	0.101	0.115	
0.69	1.01	0.027	0.040	
0.38	0.97	0.015	0.038	4
1.20	1.73	0.047	0.068	
1.14	1.73	0.045	0.068	4
0.36	0.61	0.014	0.024	
0.36	0.56	0.014	0.022	4
14.85	15.25	0.585	0.600	3
8.38	9.02	0.330	0.355	
11.68	12.88	0.460	0.507	6
	MIN. 4.25 1.14 2.56 0.69 0.38 1.20 1.14 0.36 0.36 14.85 8.38	4.25 4.65 1.14 1.40 2.56 2.92 0.69 1.01 0.38 0.97 1.20 1.73 1.14 1.73 0.36 0.61 0.36 0.56 14.85 15.25 8.38 9.02	MIN. MAX. MIN. 4.25 4.65 0.167 1.14 1.40 0.045 2.56 2.92 0.101 0.69 1.01 0.027 0.38 0.97 0.015 1.20 1.73 0.047 1.14 1.73 0.045 0.36 0.61 0.014 0.36 0.56 0.014 14.85 15.25 0.585 8.38 9.02 0.330	MIN. MAX. MIN. MAX. 4.25 4.65 0.167 0.183 1.14 1.40 0.045 0.055 2.56 2.92 0.101 0.115 0.69 1.01 0.027 0.040 0.38 0.97 0.015 0.038 1.20 1.73 0.047 0.068 1.14 1.73 0.045 0.068 0.36 0.61 0.014 0.024 0.36 0.56 0.014 0.022 14.85 15.25 0.585 0.600 8.38 9.02 0.330 0.355

SYMBOL	MILLIN	IETERS	INCHES		NOTES
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Е	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° to 93°		90° t	o 93°	
	•			•	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline

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Lead tip



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Vishay

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