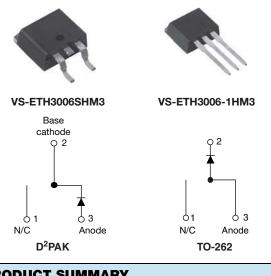




### **Vishay Semiconductors**

## Hyperfast Rectifier, 30 A FRED Pt®



PRODUCT SUMMARY	
Package	TO-263AB (D <sup>2</sup> PAK), TO-262AA
I <sub>F(AV)</sub>	30 A
V <sub>R</sub>	600 V
V <sub>F</sub> at I <sub>F</sub>	2.65 V
t <sub>rr</sub> (typ.)	27 ns
T <sub>J</sub> max.	175 °C
Diode variation	Single die

### FEATURES

- · Hyperfast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- AEC-Q101 qualified, meets JESD 201 class 1A whisker test



 Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

 Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

#### **DESCRIPTION/APPLICATIONS**

Hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC Boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS			
Repetitive peak reverse voltage	V <sub>RRM</sub>		600	V			
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 95 °C	30	٨			
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>C</sub> = 25 °C	180	A			
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		- 65 to 175	°C			

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	600	-	-			
Forward voltage V <sub>F</sub>	M	I <sub>F</sub> = 30 A	-	2.0	2.65	V		
	I <sub>F</sub> = 30 A, T <sub>J</sub> = 150 °C	-	1.4	1.8				
Reverse leakage current I <sub>R</sub>		$V_{R} = V_{R}$ rated	-	0.02	30	- μΑ		
		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	50	300			
Junction capacitance	CT	V <sub>R</sub> = 600 V	-	20	-	pF		
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8.0	-	nH		

Revision: 22-May-12

1

Document Number: 94425



VS-ETH3006SHM3, VS-ETH3006-1HM3

Vishay Semiconductors

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J$ = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS		
		$I_F = 1 \text{ A}, dI_F/dt = 50$	$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, V_R = 30 \text{ V}$		26	35		
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	26	-	ns	
		T <sub>J</sub> = 125 °C	I <sub>F</sub> = 30 A dI <sub>F</sub> /dt = 200 A/μs V <sub>R</sub> = 200 V	-	70	-		
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	3.5	-	A	
		T <sub>J</sub> = 125 °C		-	7.6	-		
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	50	-	5	
		T <sub>J</sub> = 125 °C		-	280	-	nC	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 65	-	175	°C	
Thermal resistance, junction to case	R <sub>thJC</sub>		-	0.95	1.4	°C/W	
Thermal resistance, junction to ambient	R <sub>thJA</sub>	Typical socket mount	-	-	70		
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.5	-		
Woight			-	2.0	-	g	
Weight			-	0.07	-	oz.	
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)	
		Case style D <sup>2</sup> PAK modified	ETH3006SH				
Marking device		Case style TO-262		ETH30	006-1H		



VS-ETH3006SHM3, VS-ETH3006-1HM3

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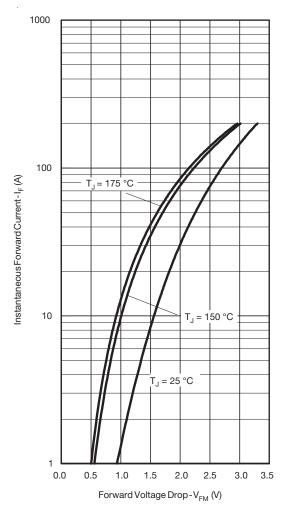


Fig. 1 - Typical Forward Voltage Drop Characteristics

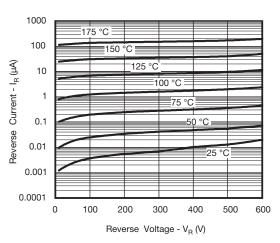


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

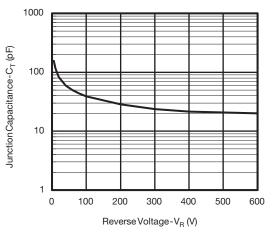


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

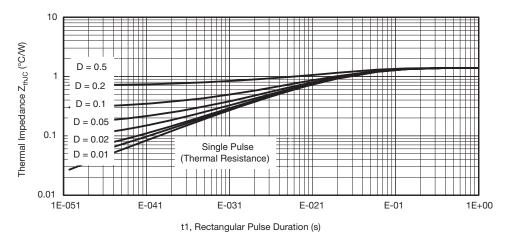


Fig. 4 - Max. Thermal Impedance Z<sub>thJC</sub> Characteristics

Revision: 22-May-12

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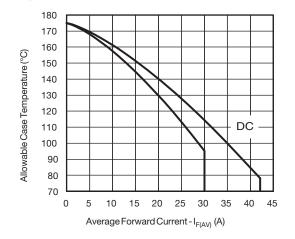
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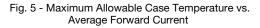
Document Number: 94425



VS-ETH3006SHM3, VS-ETH3006-1HM3

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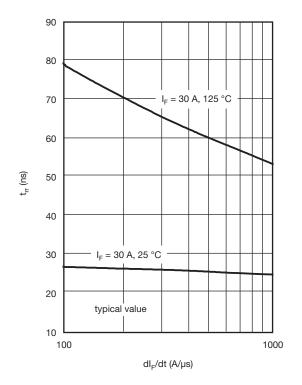


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

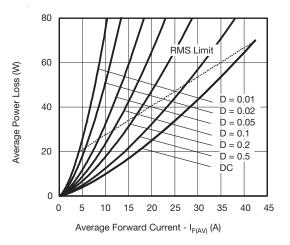


Fig. 6 - Forward Power Loss Characteristics

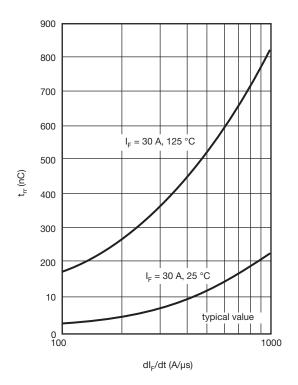


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

Document Number: 94425

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## VS-ETH3006SHM3, VS-ETH3006-1HM3

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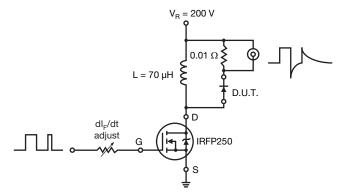


Fig. 9 - Reverse Recovery Parameter Test Circuit

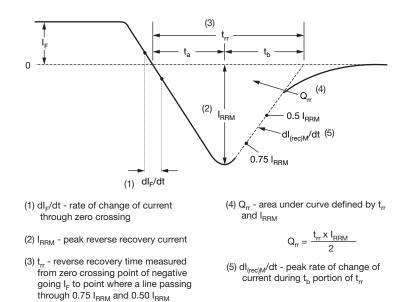


Fig. 10 - Reverse Recovery Waveform and Definitions

extrapolated to zero current.

# VS-ETH3006SHM3, VS-ETH3006-1HM3



## Vishay Semiconductors

### **ORDERING INFORMATION TABLE**

Device code	VS-	E	т	н	30	06	S	TRL	н	М3
	1	2	3	4	5	6	7	8	9	10
	1	- Visl	nay Sem	niconduc	ctors pro	oduct				
	2		uit conf Single	iguratior diode	ı					
	3	- T=	TO-220							
	4	- H=	Hyperfa	ast recov	very tim	е				
	5	- Cur	Current code (30 = 30 A)							
	6	- Voli	age coo	le (06 =	600 V)					
	7	• <b>S</b>	• S = $D^2 PAK$							
		- •-1	= TO-2	62						
	8	- • No	one = Ti	ube						
		- • TF	RL = Tap	be and r	eel (left	oriente	d, for D	<sup>2</sup> PAK p	ackage	)
		- • TF	RR = Ta	pe and i	reel (rigl	nt orient	ed, for	D <sup>2</sup> PAK	packag	je)
	9	- н=	H = AEC-Q101 qualified							
	10	- Env	rironmer	ntal digit	:					
	_	-M3	8 = Halo	gen-free	e, RoHS	complia	ant, and	d termin	ations I	ead (Pb

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-ETH3006SHM3	50	1000	Antistatic plastic tube				
VS-ETH3006-1HM3	50	1000	Antistatic plastic tube				
VS-ETH3006STRRHM3	800	800	13" diameter reel				
VS-ETH3006STRLHM3	800	800	13" diameter reel				

LINKS TO RELATED DOCUMENTS					
Dimensions	TO-263AB (D <sup>2</sup> PAK)	www.vishay.com/doc?95046			
Dimensions	TO-262AA	www.vishay.com/doc?95419			
Part marking information	TO-263AB (D <sup>2</sup> PAK)	www.vishay.com/doc?95444			
Fait marking mornation	TO-262AA	www.vishay.com/doc?95443			
Packaging information	TO-263AB (D <sup>2</sup> PAK)	www.vishay.com/doc?95032			

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