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VS-20ETS..FPPbF Series, VS-20ETS..FP-M3 Series

Vishay Semiconductors

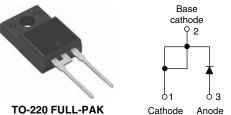
RoHS

COMPLIANT HALOGEN

FREE

Available

High Voltage, Input Rectifier Diode, 20 A



TO-220 FULL-PAK

PRODUCT SUMMARY		
Package TO-220FP		
I _{F(AV)}	20 A	
V _R	800 V, 1200 V	
V _F at I _F	1.1 V	
I _{FSM}	300 A	
T _J max.	150 °C	
Diode variation	Single die	

FEATURES

- Very low forward voltage drop
- 150 °C max. operating junction temperature · Designed and qualified according to
- JEDEC-JESD47
- Fully isolated package (V_{INS} = 2500 V_{RMS})
- UL E78996 approved
- Compliant to RoHS Directive 2002/95/EC
- Halogen-free according to IEC 61249-2-21 definition (-M3 only)

APPLICATIONS

- Input rectification
- Vishay Semiconductors switches and output rectifiers which are available in identical package outlines

DESCRIPTION

High voltage rectifiers optimized for very low forward voltage drop with moderate leakage.

These devices are intended for use in main rectification (single or three phase bridge).

OUTPUT CURRENT IN TYPICAL APPLICATIONS				
APPLICATIONS	ATIONS SINGLE-PHASE BRIDGE THREE-PHASE BRIDGE UNITS			
Capacitive input filter $T_A = 55$ °C, $T_J = 125$ °C common heatsink of 1 °C/W	18	22	A	

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES		
I _{F(AV)}	Sinusoidal waveform	20	A	
V _{RRM}	Range	800/1200	V	
I _{FSM}		300	A	
V _F	10 A, T _J = 25 °C	1.0	V	
TJ		- 40 to 150	°C	

VOLTAGE RATINGS					
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA		
VS-20ETS08FPPbF, VS-20ETS08FP-M3	800	900	1		
VS-20ETS12FPPbF, VS-20ETS12FP-M3	1200	1300	I		

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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum average forward current	I _{F(AV)}	$T_{C} = 51 \text{ °C}$, 180° conduction half sine wave	20		
Maximum peak one cycle		10 ms sine pulse, rated V_{RRM} applied	250	A	
non-repetitive surge current	IFSM	10 ms sine pulse, no voltage reapplied	300		
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rated V _{RRM} applied	316	– A ² s	
	1-1	10 ms sine pulse, no voltage reapplied	442	A-S	
Maximum I ² √t for fusing	l²√t	t = 0.1 ms to 10 ms, no voltage reapplied	4420	A²√s	

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	DL TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop	V _{FM}	20 A, T _J = 25 °C		1.1	V
Forward slope resistance	rt	$\frac{r_t}{V_{F(TO)}} T_J = 150 \ ^{\circ}C$		10.4	mΩ
Threshold voltage	V _{F(TO)}			0.85	V
Maximum reverse leakage current	I _{RM}	$T_J = 25 \ ^{\circ}C$	V_{R} = Rated V_{RRM}	0.1	mA
		T _J = 150 °C		1.0	

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage tempera	ature range	T _J , T _{Stg}		- 40 to 150	°C
Maximum thermal resistance, junction to case		R _{thJC}	DC operation	2.8	
Maximum thermal resistance, junction to ambient		R _{thJA}		62	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.5	
Approximate weight				2	g
				0.07	oz.
Mounting torque minimu maximu				6.0 (5.0)	kgf ⋅ cm
				12 (10)	(lbf · in)
Marking device				20ETS	S08FP
			Case style TO-220 FULL-PAK (94/V0)		20ETS12FP

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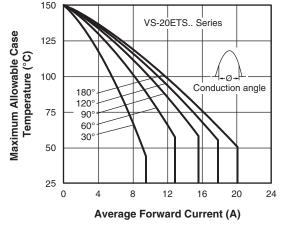


Fig. 1 - Current Rating Characteristics

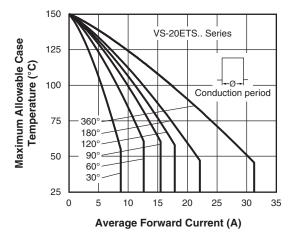
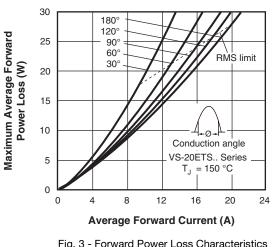
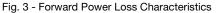


Fig. 2 - Current Rating Characteristics





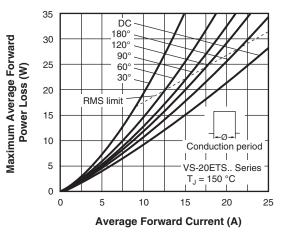
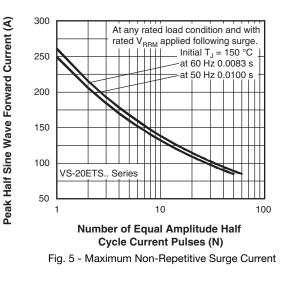
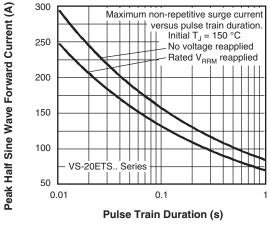


Fig. 4 - Forward Power Loss Characteristics







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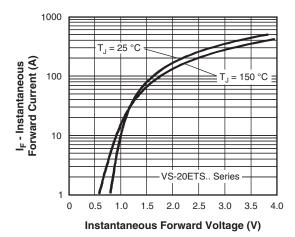


Fig. 7 - Forward Voltage Drop Characteristics

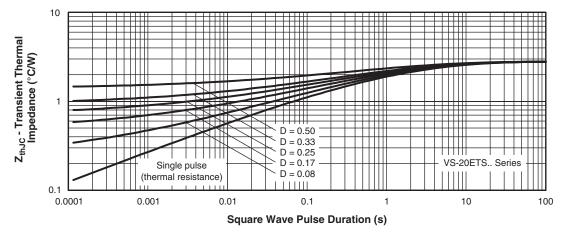


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

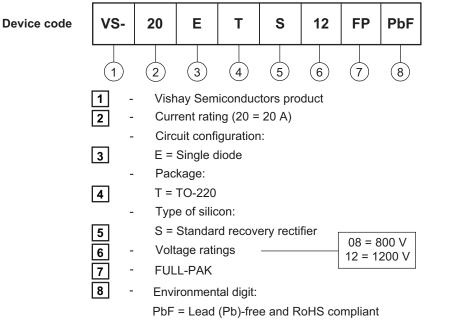
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ORDERING INFORMATION TABLE



-M3 = Halogen-free, RoHS compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-20ETS08FPPbF	50	1000	Antistatic plastic tubes		
VS-20ETS08FP-M3	50	1000	Antistatic plastic tubes		
VS-20ETS12FPPbF	50	1000	Antistatic plastic tubes		
VS-20ETS12FP-M3	50	1000	Antistatic plastic tubes		

LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95005				
Dout modeling information	TO-220 FP PbF	www.vishay.com/doc?95009		
Part marking information	TO-220 FP -M3	www.vishay.com/doc?95440		

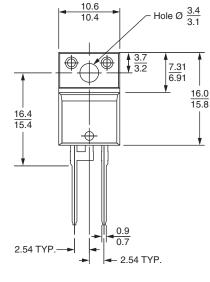
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Outline Dimensions

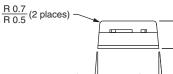
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DIMENSIONS in millimeters

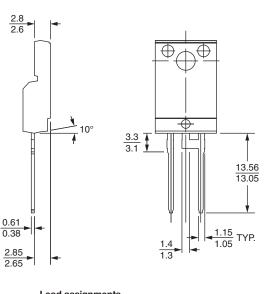


 $\frac{4.8}{4.6}$

 $5^{\circ} \pm 0.5^{\circ}$



 $5^{\circ} \pm 0.5^{\circ}$



Lead assignments Diodes 1 + 2 - Cathode 3 - Anode

Conforms to JEDEC outline TO-220 FULL-PAK



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